

TOSHIBA Zener Diode Silicon Diffused Type

CMZ16, CMZ22, CMZ53

Applications:

Communication, Control and
Measurement Equipment
Constant Voltage Regulation
Transient Suppressors

- Average power dissipation : $P = 2 \text{ W}$
- Zener voltage : $V_Z = 16, 22, 53 \text{ V}$
- Suitable for compact assembly due to small surface-mount package
“M-FLAT™” (Toshiba package name)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power dissipation	P	2 (Note 1)	W
Junction temperature	T _j	-40~150	°C
Storage temperature range	T _{stg}	-40~150	°C

Note 1: Ta = 30°C

Device mounted on a ceramic board

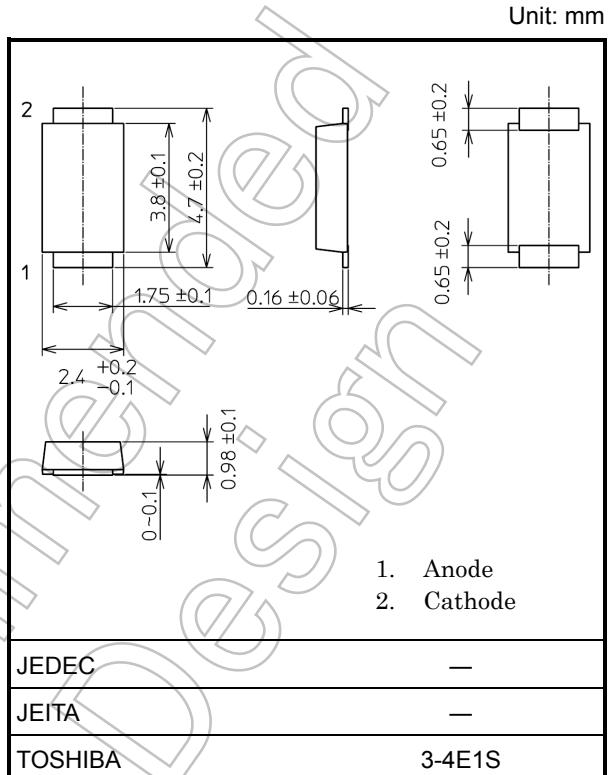
Board size: 50 mm × 50 mm

Soldering size: 2 mm × 2 mm

Board thickness: 0.64 mm

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).



Weight: 0.023 g (typ.)

Land Pattern Dimensions (for reference only)Start of commercial production
2002-10

Electrical Characteristics (Ta = 25°C)

Type	Zener Voltage Vz (V)			Zener Impedance r _d (Ω)	Temperature Coefficient Of Zener αT (mV/°C)		Forward Voltage V _F (V)		Reverse Current I _R (μA)				
	Min	Typ.	Max		Max	Max	Typ.	Max	Max	Measure- ment Current I _F (A)	Max	Measure- ment Voltage V _R (V)	
CMZ16	14.4	16	17.6	10	30	10	12	19	1.2	0.2	10	11	
CMZ22	19.8	22	24.2	10	30	10	18	28	1.2	0.2	10	16	
CMZ53	47.7	53	58.3	5	85	5	49	77	1.2	0.2	10	42.4	

Marking

Abbreviation Code	Part No.
16	CMZ16
22	CMZ22
53	CMZ53

Handling Precaution

1) The absolute maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend when you design a circuit with a device.

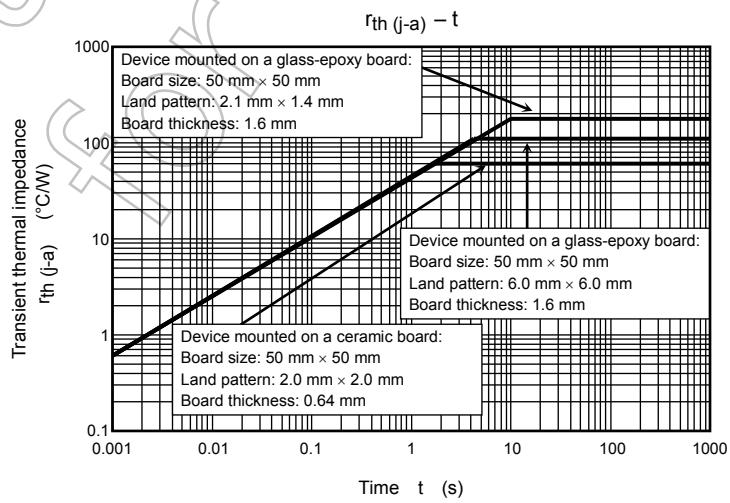
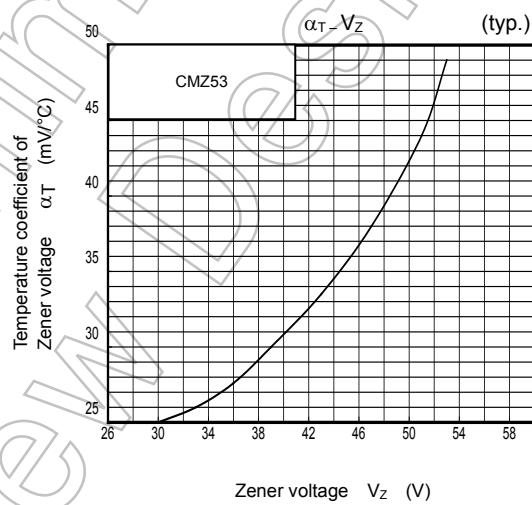
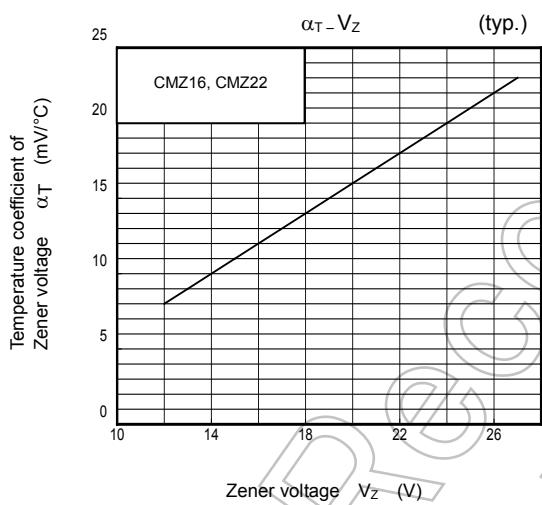
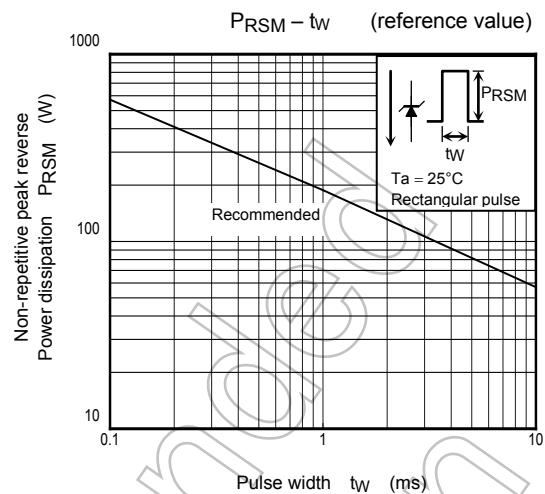
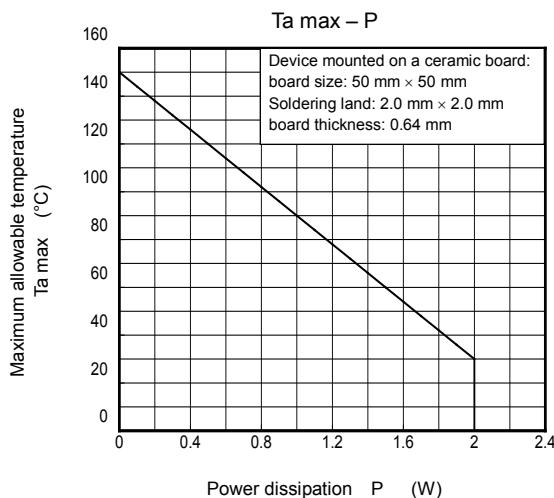
P : We recommend that the worst case power dissipation be no greater than 50% of the absolute maximum rating of power dissipation. Carry out adequate heat design.

PRSM : We recommend that a device be used within the recommended area in the figure, PRSM-tw.

T_j : Derate this rating when using a device in order to ensure high reliability. We recommend that the device be used at a T_j of below 120°C.

2) Thermal resistance between junction and ambient fluctuates depending on the device's mounting condition. When using a device, design a circuit board and a soldering land size to match the appropriate thermal resistance value.

3) Please refer to the Rectifiers databook for further information.



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