

#### 2.5V Drive Nch+SBD MOSFET

V <sub>DSS</sub>	30V
R <sub>DS(on)</sub> (Max.)	154mΩ
I <sub>D</sub>	±2.0A
P <sub>D</sub>	1.25W

#### Features

- The QS5U13 combines Nch MOSFET with a Schottky barrier diode in a single TSMT5 package.
- 2) Low on-state resistance with fast swicthing
- 3) Low voltage drive (2.5V drive).
- 4) Built-in Low V<sub>F</sub> schottky barrier diode.
- 5) Pb-free lead plating; RoHS compliant.

### Application

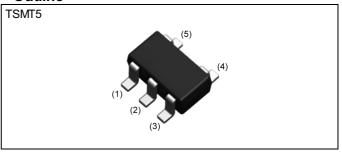
Load switch, DC/ DC conversion

### ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C)

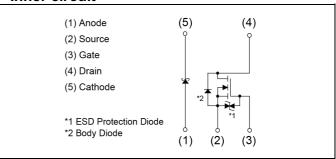
#### <MOSFET>

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	30	V
Gate - Source voltage	V <sub>GSS</sub>	12	V
Continuous drain current	I <sub>D</sub>	±2.0	А
Pulsed drain current	I <sub>D, pulse</sub> *1	±8.0	А
Continuous source current (body diode)	I <sub>S</sub>	0.8	А
Pulsed source current (body diode)	I <sub>S, pulse</sub> *1	3.2	А
Power dissipation	P <sub>D</sub> *3	0.9	W/elemer
Junction temperature	T <sub>j</sub>	150	°C

#### Outline



#### •Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Basic ordering unit (pcs)	3000
	Taping code	TR
	Marking	U13

## ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

#### <SBD>

Parameter	Symbol	Value	Unit
Repetitive peak reverse voltage	$V_{RM}$	30	V
Reverse voltage	$V_R$	20	V
Forward current	I <sub>F</sub>	0.5	А
Forward current surge peak	I <sub>FSM</sub> *2	2.0	А
Power dissipation	P <sub>D</sub> *3	0.7	W/element
Junction temperature	T <sub>j</sub>	150	°C

#### <MOSFET + SBD>

Parameter	Symbol	Value	Unit	
Power dissipation	P <sub>D</sub> *3	1.25	W/total	
Range of storage temperature	T <sub>stg</sub>	-55 to +150	°C	

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

#### <MOSFET>

Daramatar	Cymah al	Conditions		Values		l leit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Gate - Source leakage current	I <sub>GSS</sub>	V <sub>GS</sub> = 12V, V <sub>DS</sub> = 0V	-	-	10	μA
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	30	-	-	V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	-	-	1	μA
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA	0.5	-	1.5	V
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 2.0A	-	71	100	
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 4.0V, I <sub>D</sub> = 2.0A	-	76	107	mΩ
		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 2.0A	-	110	154	
Transconductance	9 <sub>fs</sub> *4	V <sub>DS</sub> = 10V, I <sub>D</sub> = 2.0A	1.5	-	-	S



## ● Electrical characteristics (T<sub>a</sub> = 25°C)

### <MOSFET>

Parameter	Cumbal	Conditions		Values		Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	175	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10V	-	50	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	1	25	-	
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq 15V$ , $V_{GS} = 4.5V$	1	8	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 1.0A	-	10	-	
Turn - off delay time	t <sub>d(off)</sub> *4	R <sub>L</sub> = 15Ω	-	21	-	ns
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	8	-	

# • Gate charge characteristics ( $T_a = 25$ °C)

#### <MOSFET>

Parameter	Cumbal	Conditions		Values		Lloit
raiametei	Symbol Conditions -	Min.	Тур.	Max.	Unit	
Total gate charge	$Q_g^{*4}$		-	2.8	3.9	
Gate - Source charge	Q <sub>gs</sub> *4	$V_{DD} \approx 15V, I_{D} = 2.0A$ $V_{GS} = 4.5V$	-	0.6	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	763 7	-	0.8	-	

## ● Body diode electirical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

#### <MOSFET>

Parameter	Symbol	Conditions	Values			
raianetei	Symbol Conditions	Min.	Тур.	Max.	Uniil	
Forward voltage	V <sub>SD</sub> *4	$V_{GS} = 0V, I_{S} = 3.2A$	-	-	1.2	V



## ● Electrical characteristics (T<sub>a</sub> = 25°C)

### <SBD>

Downston	Cy made al	Conditions	Values			1.1-24
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
		I <sub>F</sub> = 0.1A	-	-	0.36	V
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 0.5A	-	-	0.47	V
Reverse current	I <sub>R</sub>	V <sub>R</sub> = 20V	-	-	100	μA

<sup>\*1</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

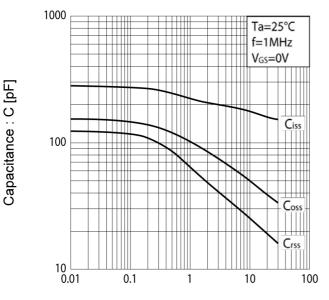
<sup>\*2 60</sup>Hz-1 cycle

<sup>\*3</sup> Mounted on a ceramic board

<sup>\*4</sup> Pulsed

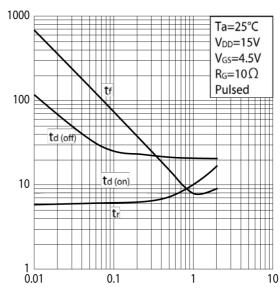
#### • Electrical characteristic curves < MOSFET>

Fig.1 Typical Capacitance vs. Drain - Source Voltage



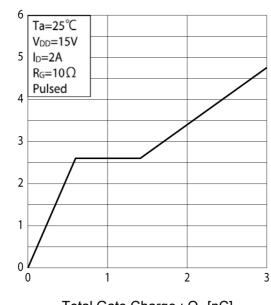
Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.2 Switching Characteristics



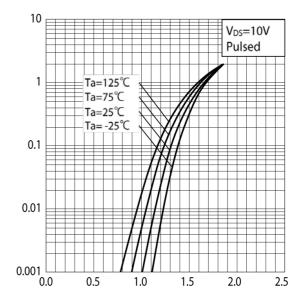
Drain Current : I<sub>D</sub> [A]

Fig.3 Dynamic Input Characteristics



Total Gate Charge : Qg [nC]

Fig.4 Typical Transfer Characteristics



Gate - Source Voltage : V<sub>GS</sub> [V]

Gate - Source Voltage :  $V_{GS}\left[V\right]$ 

Switching Time : t [ns]

Drain Current : I<sub>D</sub> [A]

#### ● Electrical characteristic curves < MOSFET>

Fig.5 Static Drain - Source On - State Resistance vs. Gate Source Voltage

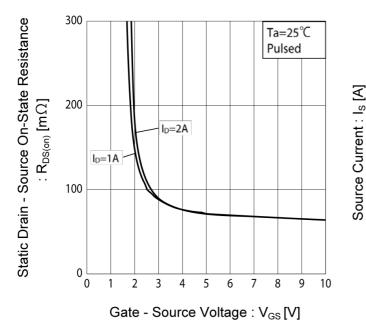
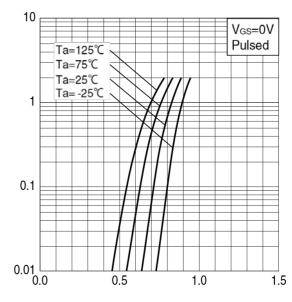


Fig.6 Source Current vs. Source Drain Voltage



Source - Drain Voltage : V<sub>SD</sub> [V]

Fig.7 Static Drain - Source On - State Resistance vs. Drain Current (I)

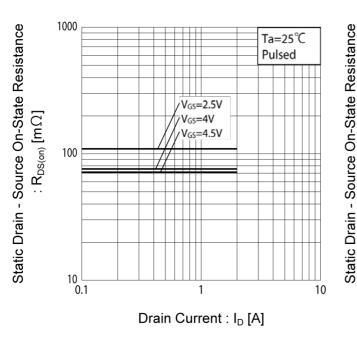
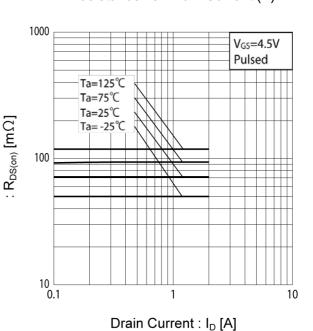


Fig.8 Static Drain - Source On - State Resistance vs. Drain Current (II)



#### • Electrical characteristic curves < MOSFET>

Fig.9 Static Drain - Source On - State Resistance vs. Drain Current (III)

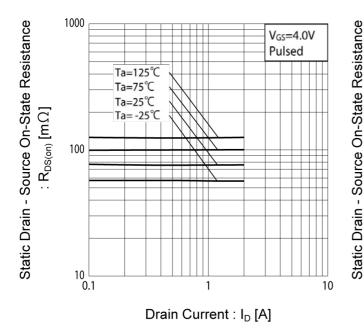
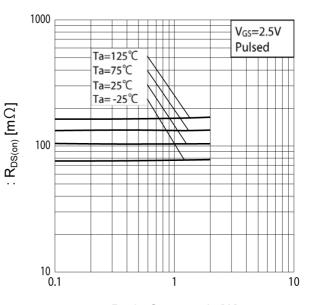


Fig.10 Static Drain - Source On - State Resistance vs. Drain Current (IV)



Drain Current : I<sub>D</sub> [A]



#### • Electrical characteristic curves <SBD>

Fig.11 Forward Current vs. Forward Voltage

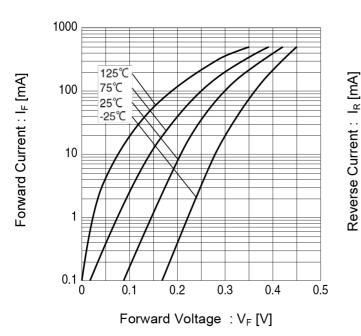
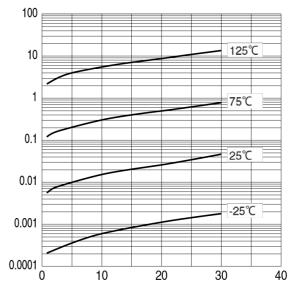


Fig.12 Reverse Current vs. Reverse Voltage



Reverse Voltage: V<sub>R</sub> [V]

#### Notice

- SBD has a large reverse leak current compared to other type of diode. Therefore, it would raise a junction temperature, and increase a reverse power loss. Further rise of inside temperature would cause a thermal runaway. This built-in SBD has low V<sub>F</sub> characteristics and therefore, higher leak current. Please consider enough the surrounding temperature, generating heat of MOSFET and the reverse current.
- 2. This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

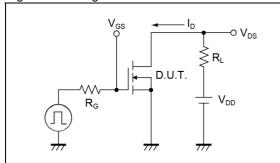


Fig.2-1 Gate Charge Measurement Circuit

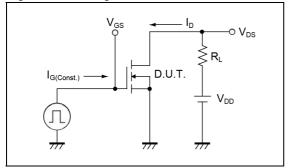


Fig.1-2 Switching Waveforms

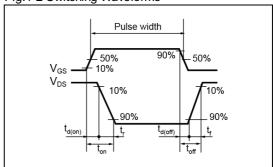
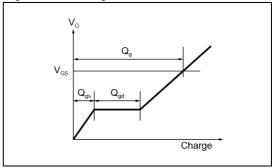
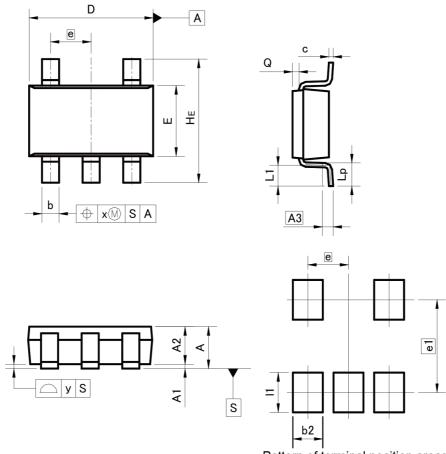


Fig.2-2 Gate Charge Waveform



#### Dimensions

TSMT5



Pattern of terminal position areas
[Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES	
4751474.7431	MIN	MAX	MIN	MAX	
Α	+	1.00	Н	0.039	
A1	0.00	0.10	0.000	0.004	
A2	0.75	0.95	0.030	0.037	
A3	0.3	0.25		10	
b	0.35	0.50	0.014	0.020	
С	0.10	0.26	0.004	0.010	
D	2.80	3.00	0.110	0.118	
E	1.50	1.80	0.059	0.071	
е	0.9	95	0.037		
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.05	0.25	0.002	0.010	
x	_	0.20	7-7	0.008	
у	¥5	0.10	7-1	0.004	

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2		0.70	-	0.028
e1	2.	2.10		083
11	<del>=</del> 5	0.90	-	0.035

Dimension in mm/inches



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