TPA0243 2-W MONO AUDIO POWER AMPLIFIER WITH HEADPHONE DRIVE

SLOS279C - JANUARY 2000 - REVISED NOVEMBER 2002

- Ideal for Notebook Computers, PDAs, and Other Small Portable Audio Devices
- 2 W Into 4-Ω From 5-V Supply
- 0.6 W Into 4- Ω From 3-V Supply
- Stereo Head Phone Drive
- Mono (BTL) Signal Created by Summing Left and Right Signals
- Wide Power Supply Compatibility 3 V to 5 V
- Meets PC99 Desktop Specs (target)
- Low Supply Current
 - 10 mA Typical at 5 V
 - 9 mA Typical at 3 V
- Shutdown Control . . . 1 μA Typical
- Shutdown Pin Is TTL Compatible
- -40°C to 85°C Operating Temperature Range
- Space-Saving, Thermally-Enhanced MSOP Packaging

description

The TPA0243 is a 2-W mono bridge-tied-load (BTL) amplifier designed to drive speakers with as low as $4-\Omega$ impedance. The mono signal is created by summing left and right inputs. The amplifier can be reconfigured on the fly to drive two stereo single-ended (SE) signals into headphones. This makes the device ideal for small notebook computers, PDAs, digital personal audio players, anyplace a mono speaker and stereo headphones are required. From a 5-V supply, the TPA0243 can deliver 2 W of power into a 4- Ω speaker.

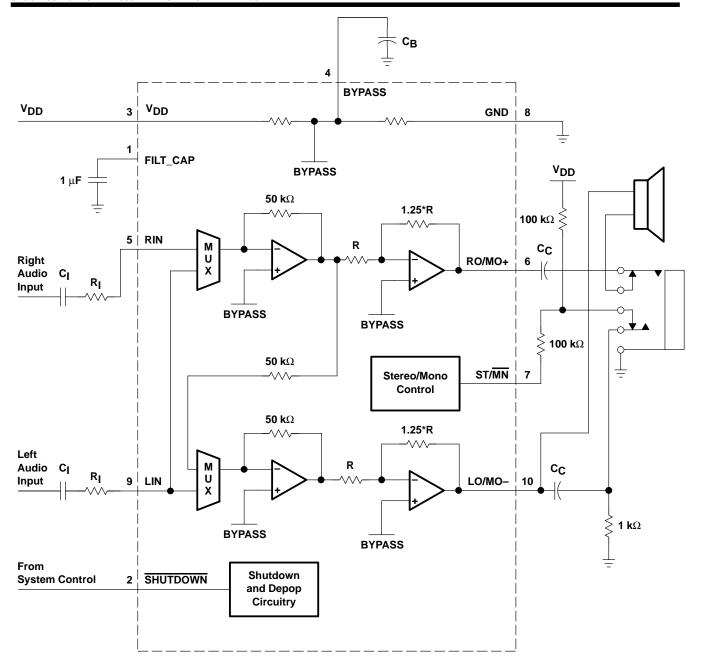
The gain of the input stage is set by the user-selected input resistor and a 50-k Ω internal feedback resistor (A_V = - R_F/R_I). The power stage is internally configured with a gain of -1.25 V/V in SE mode, and -2.5 V/V in BTL mode. Thus, the overall gain of the amplifier is 62.5 k Ω /R_I in SE mode and 125 k Ω /R_I in BTL mode. The input terminals are high-impedance CMOS inputs, and can be used as summing nodes.

The TPA0243 is available in the 10-pin thermally-enhanced MSOP package (DGQ) and operates over an ambient temperature range of –40°C to 85°C.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.





AVAILABLE OPTIONS

	PACKAGED DEVICES	MOOD
TA	MSOP† (DGQ)	MSOP SYMBOLIZATION
-40°C to 85°C	TPA0243DGQ	AEK

[†] The DGQ package are available taped and reeled. To order a taped and reeled part, add the suffix R to the part number (e.g., TPA0243DGQR).



SLOS279C - JANUARY 2000 - REVISED NOVEMBER 2002

Terminal Functions

TERMINA	TERMINAL		DECORPTION					
NAME	NO.	1/0	DESCRIPTION					
BYPASS	4	I	BYPASS is the tap to the voltage divider for internal mid-supply bias. This terminal should be connected to a 0.1 - μF to 1 - μF capacitor.					
GND	8		Ground terminal					
LIN	9	I	Left-channel input terminal					
LO/MO	10	0	Left-output in SE mode and mono negative output in BTL mode.					
MONO-IN	1	I	Terminal used to filter supply					
RIN	5	I	Right-channel input terminal					
RO/MO	6	0	Right-output in SE mode and mono positive output in BTL mode					
SHUTDOWN	2	- 1	SHUTDOWN places the entire device in shutdown mode when held low. TTL compatible input.					
ST/MN	7	I	Selects between Stereo and Mono mode. When held high, the amplifier is in SE stereo mode; while held low, the amplifier is in BTL mono mode.					
V_{DD}	3	I	V _{DD} is the supply voltage terminal.					

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{DD}	6 V
Input voltage range, V _I	
Continuous total power dissipation in	ternally limited (see Dissipation Rating Table)
Operating free-air temperature range, T _A (see Table 3)	–40°C to 85°C
Operating junction temperature range, T _J	–40°C to 150°C
Storage temperature range, T _{stq}	–65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	3 260°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

DISSIPATION RATING TABLE

PACKAGE	T _A ≤ 25°C	DERATING FACTOR	T _A = 70°C	T _A = 85°C
DGQ	2.14 W [‡]	17.1 mW/°C	1.37 W	1.11 W

[‡] See PowerPAD Thermally Enhanced Package Application Report (SLMA002) for more information on the PowerPAD™ package. The thermal data was measured on a PCB layout based on the information in the section entitled Texas Instruments Recommended Board for PowerPAD on page 33 of that document.

recommended operating conditions

			MIN	MAX	UNIT
Supply voltage, V _{DD}			2.5	5.5	V
	GT/101	V _{DD} = 3 V	2.7		
High-level input voltage, VIH	ST/MN	V _{DD} = 5 V	4.5		V
	SHUTDOWN		2		
	GT/101	V _{DD} = 3 V		1.65	
Low-level input voltage, V _{II}	ST/MN	V _{DD} = 5 V		2.75	V
	SHUTDOWN			0.8	
Operating free-air temperature, TA			-40	85	°C

PowerPAD is a trademark of Texas Instruments.



SLOS279C - JANUARY 2000 - REVISED NOVEMBER 2002

electrical characteristics at specified free-air temperature, V_{DD} = 3 V, T_A = 25°C (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
IVool	Output offset voltage (measured differentially)	$\overline{\text{SHUTDOWN}}$ = 2 V, ST/ $\overline{\text{MN}}$ = 0, R _L = 4 Ω			30	mV
I _{DD}	Supply current	$V_{DD} = 2.5 \text{ V}, \overline{\text{SHUTDOWN}} = 2 \text{ V}$		9	14	mA
I _{DD(SD)}	Supply current, shutdown mode	SHUTDOWN = 0 V		1	10	μΑ
III	High level input assured	$\overline{\text{SHUTDOWN}}$, $V_{DD} = 3.3 \text{ V}$, $V_{I} = V_{DD}$			1	^
Іні	High-level input current	ST/\overline{MN} , $V_{DD} = 3.3 \text{ V}$, $V_{I} = V_{DD}$			1	μΑ
111	Lave lavel inner a summer	$\overline{\text{SHUTDOWN}}$, $V_{DD} = 3.3 \text{ V}$, $V_{I} = 0 \text{ V}$			1	^
I	Low-level input current	ST/\overline{MN} , $V_{DD} = 3.3 \text{ V}$, $V_{I} = 0 \text{ V}$			1	μΑ
R _F	Feedback resistor	$V_{DD} = 2.5 \text{ V}, R_L = 4 \Omega, \text{ST/MN} = 1.375 \text{ V},$ SHUTDOWN = 2 V	47	50	57	kΩ

operating characteristics, V_{DD} = 3 V, T_A = 25°C, R_L = 4 Ω

	PARAMETER	TEST CONDITIONS				TYP	MAX	UNIT
D. Outside assessment Mate 4		THD = 1%,	BTL mode			660		\A/
Po	Output power, see Note 1	THD = 0.1%,	SE mode,	$R_L = 32 \Omega$		34		mW
THD + N	Total harmonic distortion plus noise	$P_0 = 500 \text{ mW},$	f = 20 Hz to 20 kHz			0.3%		
ВОМ	Maximum output power bandwidth	Gain = 2,	THD = 2%			20		kHz

NOTE 1: Output power is measured at the output terminals of the device at f = 1 kHz.

electrical characteristics at specified free-air temperature, V_{DD} = 5 V, T_A = 25°C (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
IVool	Output offset voltage (measured differentially)	$\overline{\text{SHUTDOWN}} = 2 \text{ V, ST/MN} = 0, R_L = 4 \Omega$			30	mV
I _{DD}	Supply current	SHUTDOWN = 2 V		10	15	mA
I _{DD(SD)}	Supply current, shutdown mode	SHUTDOWN = 0 V		1	10	μΑ
	I Park James Comment	SHUTDOWN, V _{DD} = 5.5 V, V _I = V _{DD}			1	
IIIHI	High-level input current	ST/\overline{MN} , $V_{DD} = 5.5 \text{ V}$, $V_{I} = V_{DD}$			1	μΑ
l'ILI	Law law line of the same at	$\overline{\text{SHUTDOWN}}, V_{\text{DD}} = 5.5 \text{V}, \qquad V_{\text{I}} = 0 \text{V}$			1	^
	Low-level input current	ST/\overline{MN} , $V_{DD} = 5.5 \text{ V}$, $V_{I} = 0 \text{ V}$			1	μΑ

operating characteristics, V_{DD} = 5 V, T_A = 25°C, R_L = 4 Ω

PARAMETER		TEST CONDITIONS				TYP	MAX	UNIT
B 0		THD = 1%,	BTL mode			2		W
PO	Output power, see Note 1	THD = 0.1%,	SE mode,	$R_L = 32 \Omega$		95		mW
THD + N	Total harmonic distortion plus noise	P _O = 1 W,	f = 20 Hz to 20 kHz			0.2%		
Вом	Maximum output power bandwidth	Gain = 2.5,	THD = 2%			20		kHz

NOTE 1: Output power is measured at the output terminals of the device at f = 1 kHz.

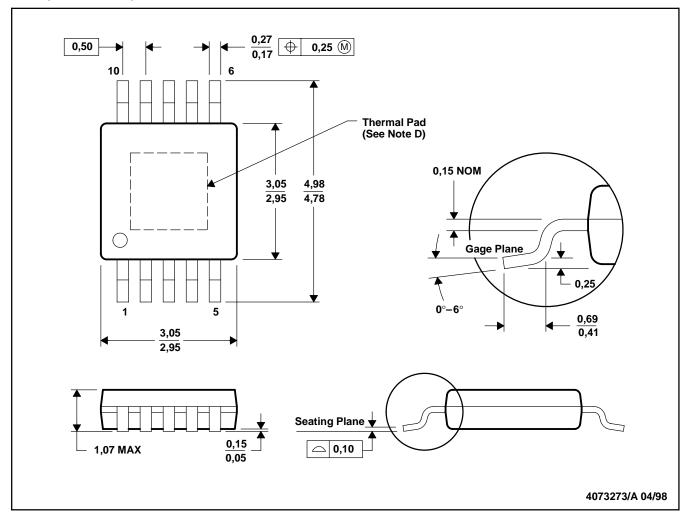


SLOS279C - JANUARY 2000 - REVISED NOVEMBER 2002

MECHANICAL DATA

DGQ (S-PDSO-G10)

PowerPAD™ PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion.
- D. The package thermal performance may be enhanced by bonding the thermal pad to an external thermal plane. This pad is electrically and thermally connected to the backside of the die and possibly selected leads.

PowerPAD is a trademark of Texas Instruments.





PACKAGE OPTION ADDENDUM

4-Feb-2005

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
TPA0243DGQ	ACTIVE	MSOP- Power PAD	DGQ	10	80	None	CU SNPB	Level-1-220C-UNLIM
TPA0243DGQR	ACTIVE	MSOP- Power PAD	DGQ	10	2500	None	CU SNPB	Level-1-220C-UNLIM

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - May not be currently available - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

None: Not yet available Lead (Pb-Free).

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean "Pb-Free" and in addition, uses package materials that do not contain halogens, including bromine (Br) or antimony (Sb) above 0.1% of total product weight.

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDECindustry standard classifications, and peak solder temperature.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

Products		Applications	
Amplifiers	amplifier.ti.com	Audio	www.ti.com/audio
Data Converters	dataconverter.ti.com	Automotive	www.ti.com/automotive
DSP	dsp.ti.com	Broadband	www.ti.com/broadband
Interface	interface.ti.com	Digital Control	www.ti.com/digitalcontrol
Logic	logic.ti.com	Military	www.ti.com/military
Power Mgmt	power.ti.com	Optical Networking	www.ti.com/opticalnetwork
Microcontrollers	microcontroller.ti.com	Security	www.ti.com/security
		Telephony	www.ti.com/telephony
		Video & Imaging	www.ti.com/video
		Wireless	www.ti.com/wireless

Mailing Address: Texas Instruments

Post Office Box 655303 Dallas, Texas 75265

Copyright © 2005, Texas Instruments Incorporated