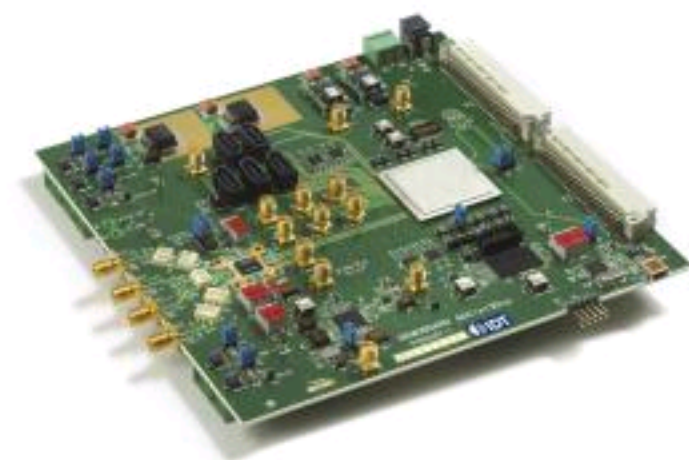


Status: **Active New**

- Product Options
- Related Products
- Technical Support
- Contact Sales

IDT's ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration. The FPGA eases the evaluation and analysis of the ADC dynamic and enables use of the full JESD204A feature set.

- ## Features
- On-board Data clock generator for ADC and FPGA
 - SMA connector for clock and analog input signals
 - Single power supply (on-board regulators)
 - On-board Virtex 5 FPGA for data-acquisition



ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
ADC1213D065W1-DB	Active New	BD0	BOARD	0	C	N	Check Inventory

There are no public documents available for this product. [Click here to request documentation.](#)

PRODUCT ID	TITLE
ADC1213D065HN	Dual 12-bit ADC; 65 Msps
ADC1213D065HN	Dual 12-bit ADC; 65 Msps

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