# AH5795

## SINGLE PHASE HALL EFFECT LATCH SMART FAN MOTOR CONTROLLER

NEW PRODUCT

## Description

The AH5795 is a single chip solution for driving single-coil brushless direct current (BLDC) fans and motors. The integrated full-bridge driver output stage uses soft switching to minimize audible switching noise and electromagnetic interference (EMI) providing a low noise solution.

Motor speed can be controlled by either changing the duty ratio of the PWM signal at the PWM pin or by varying the supply voltage at Vdd pin.

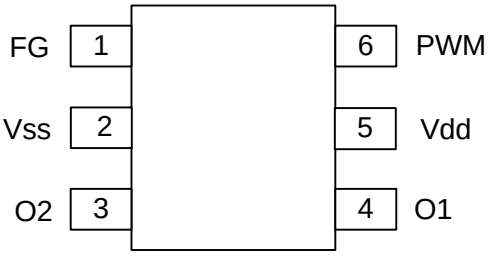
To help protect the motor coil, the AH5795 provides Rotor Lock Protection which shuts down the output drive if rotor lock is detected. The device automatically re-starts when the rotor lock is removed. Over temperature shutdown provides thermal protection for the device.

A Tachometer output is provided by open-drain Frequency Generator (FG) Pin which allows external interface to monitor motor rotation or speed. The FG output is the magnetic change frequency.

The AH5795 is available in space saving and low profile TSOT23-6 and DFN2020C-6 packages.

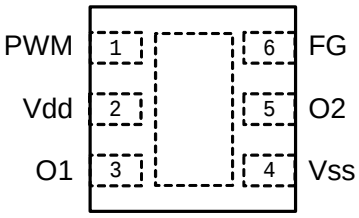
## Pin Assignments

**(Top View)**




**TSOT23-6**

**(Top View)**




**DFN2020C-6**

## Features

- Supports single-coil full-wave BLDC fan drivers
- Built-in Hall sensor and input amplifier
- Operating voltage: 1.8V to 6V
- Speed control methods
  - o Vdd voltage speed control (PWM pin tied to Vdd)
  - o PWM signal speed control via PWM pin
- Soft switching for low noise DC fan motor applications
- Rotor Lock Protection (Lock detection, output shutdown and automatic re-start)
- Toff clear when PWM is low for greater than 65ms
- Thermal protection
- Tachometer (FG) output
- No external timing capacitor - Reduces the numbers of external components required
- Low profile package: TSOT23-6 and DFN2020C-6
- “Green” Molding Compound (No Br, Sb) (Note 1)

## Applications

- 3.3V / 5V BLDC Cooling Fans
- Netbook/ Notebook BLDC fans
- Instruments cooling fans
- Low Voltage/ Low Power BLDC Motors

Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead_free.html.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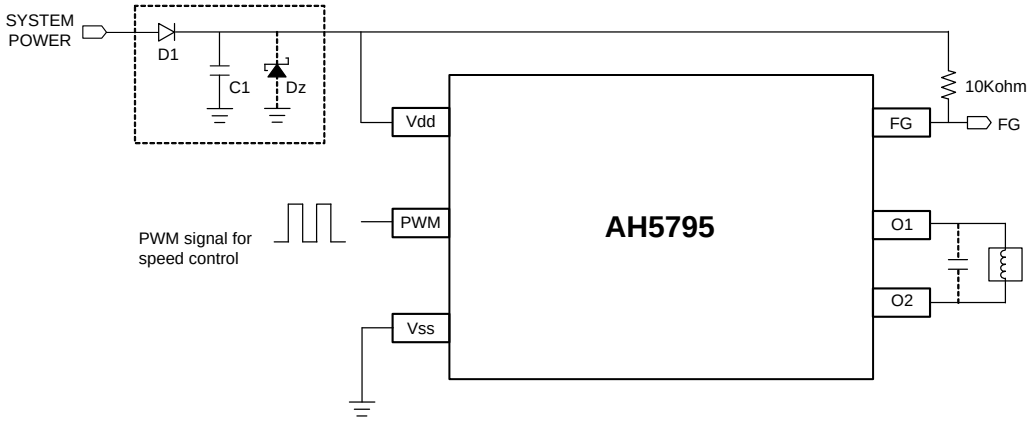
## Typical Application Circuit




## Pin Descriptions

| Pin Name | Description |
|---|---|
| $V_{dd}$ | Power supply pin |
| $V_{SS}$ | Ground pin |
| O1 | Output driving & sinking pin |
| O2 | Output driving & sinking pin |
| PWM | PWM signal input pin for speed control |
| FG | Frequency Generator (Note 2) |

Notes: 2. The FG output is the same as the magnetic change frequency.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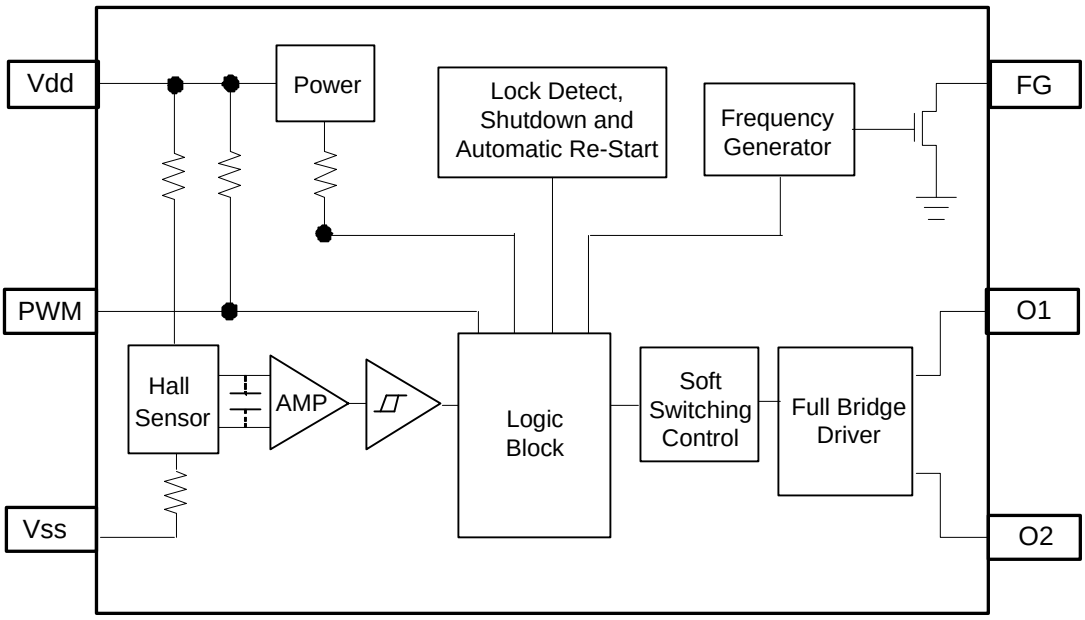
## Functional Block Diagram (Note 3)




Notes: 3. The AH5795 has an open-drain tachometer FG output that follows the magnetic change frequency. Typically a pull-up resistor of 10KΩ is recommended from FG pin to the supply voltage.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## Absolute Maximum Ratings ($T_A$ = 25°C, unless otherwise noted, Note 4)

| Symbol | Characteristics | | Values | Unit |
|---|---|---|---|---|
| Vdd | Supply voltage | | 7 | V |
| $I_{O(PEAK)}$ | Maximum Output Current (Peak) | | 1000mA | mA |
| $P_D$ | Power Dissipation | TSOT23-6 | 650 | mW |
| | | DFN2020C-6 | 780 (Note 5) | |
| $T_{ST}$ | Storage Temperature Range | | -65 ~ 150 | °C |
| ESD HBM | Human Body Model (HBM) ESD Protection | | 4 | kV |

Notes:
4. Stresses greater than the 'Absolute Maximum Ratings' specified above, may cause permanent damage to the device. These are stress ratings only; functional operation of the device at these or any other conditions exceeding those indicated in this specification is not implied. Device reliability may be affected by exposure to absolute maximum rating conditions for extended periods of time
5. DFN2020C-6 exposed pad soldered to minimum recommended landing pads (see Package Outline Dimension section) on a two-layer 2oz. copper FR4 PCB (1.6mm thickness) with no thermal vias in exposed PADs or any copper flood connecting to the landing pattern of the exposed pad.

## Recommended Operating Conditions ($T_A$ = 25°C)

| Symbol | Parameter | Conditions | Min | Max | Unit |
|---|---|---|---|---|---|
| $V_{DD}$ | Supply Voltage | Operating | 1.8 | 6.0 | V |
| $T_A$ | Operating Ambient Temperature Range | Operating | -40 | 105 | °C |

## Electrical Characteristics ($T_A$ = 25°C, $V_{DD}$ = 5V)

| Symbol | Characteristics | Conditions | Min | Typ. | Max | Unit |
|---|---|---|---|---|---|---|
| IDD | Supply Current | No Load | - | 2.2 | - | mA |
| $V_{OH}$ | Output Voltage High | $I_{OUT}$ = 300mA | 4.70 | 4.88 | - | V |
| | | $I_{OUT}$ = 500mA | 4.5 | 4.8 | - | V |
| $V_{OL}$ | Output Voltage Low | $I_{OUT}$ = 300mA | - | 0.12 | 0.3 | V |
| | | $I_{OUT}$ = 500mA | - | 0.2 | 0.5 | V |
| Voh+Vol | Output voltage of N- and PMOS combined | $I_{OUT}$ = 300mA | | 0.3 | 0.6 | V |
| | | $I_{OUT}$ = 500mA | | 0.5 | | V |
| $T_{SW}$ | Output Switching Slope Duration | 17Ω load on out1/out2 | - | 200 | - | μs |
| $I_{LEAK}$ | FG Output Leakage Current | | - | - | 5 | μA |
| $V_{FGOL}$ | FG Output Voltage Low | $I_{FG}$ = 5mA | - | - | 0.4 | V |
| $T_{ON}$ | On Time | | 350 | 500 | 650 | ms |
| $R_{DR}$ | Duty Ratio | $T_{OFF}$ / $T_{ON}$ | - | 10 | - | |
| $V_{PWMH}$ | PWM Input H Level | - | 0.5 Vdd | - | Vdd | V |
| $V_{PWML}$ | PWM Input L Level | - | 0 | - | 0.14 Vdd | V |
| $I_{PWMH}$ | PWM Input current H Level | PWM=Vdd | | 0 | | uA |
| $I_{PWMH}$ | PWM Input current L Level | PWM=GND | | -10 | | uA |
| $F_{PWM}$ | PWM Input Frequency | - | 0.02 | - | 50 | KHz |
| $D_{PWM\_MIN}$ | Output minimum duty ratio | Motor rotating; | 10% | | 100 | % |
| $Tj\_{SDN\_TH}$ | IC junction temperature thermal shutdown threshold | | | 175 | | °C |
| $Tj\_{SDN\_HYST}$ | IC junction temperature thermal shutdown hysteresis | | | 25 | | °C |


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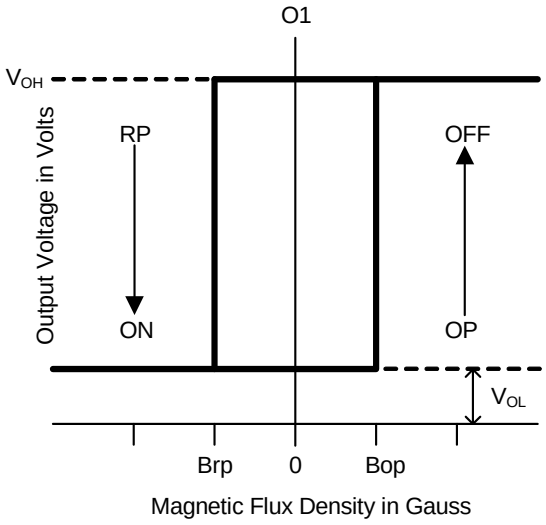
## Magnetic Characteristics ($T_A$ = 25°C, Vdd = 1.8V~6V, Note 6)

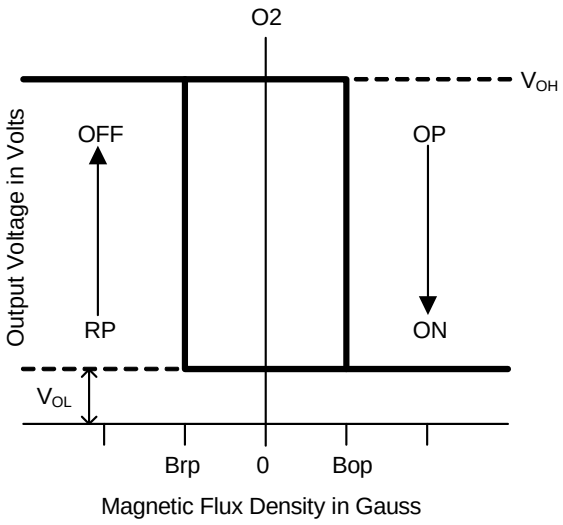
(1mT = 10 G)

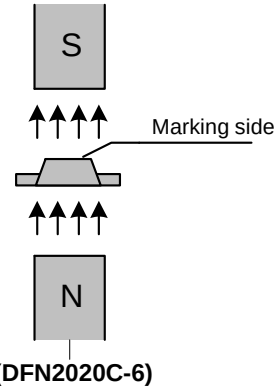
| Symbol | Parameter | Min | Typ. | Max | Unit |
|---|---|---|---|---|---|
| $B_{op}$ | Operate Point | 10 | 25 | 50 | Gauss |
| Brp | Release Point | -50 | -25 | -10 | |
| $B_{hy}$ | Hysteresis | - | 50 | - | |

Notes: 6. Magnetic characteristics may vary with supply voltage, operating temperature and after soldering.

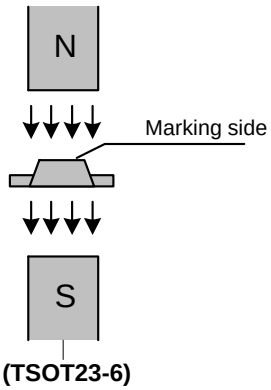
## Operating Characteristics




(DFN2020C-6)




(TSOT23-6)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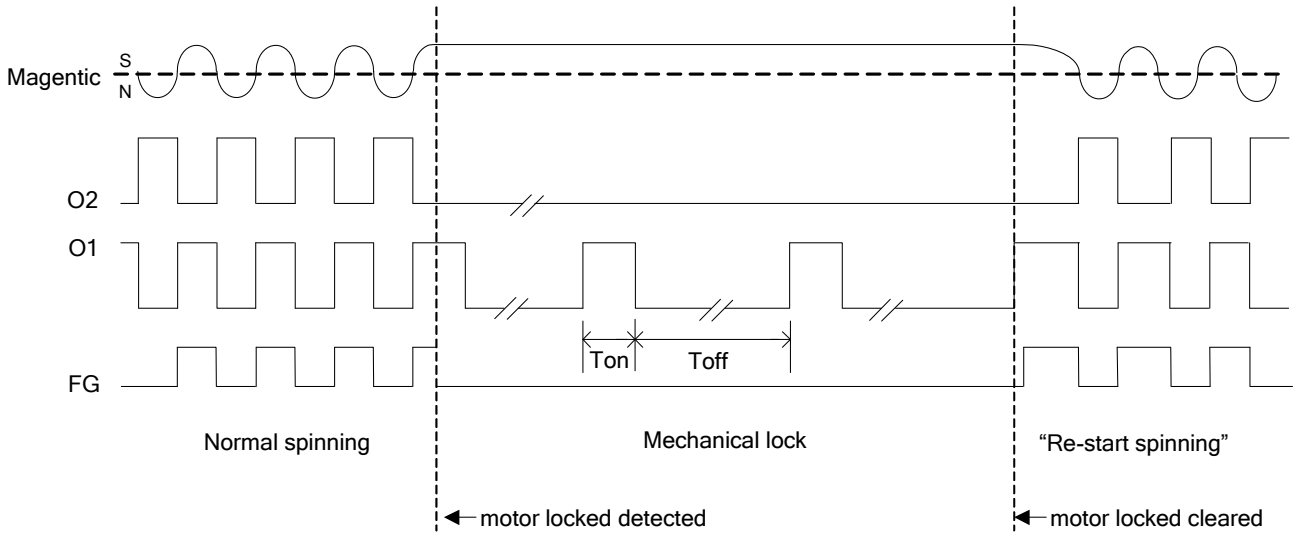
## Operating Characteristics (cont. Note 7, 8, 9 and 10)




### Truth Table

| O1 | O2 | PWM | FG |
|---|---|---|---|
| L | H | H | L |
| H | L | H | H |
| L | L | L | X (Note 10) |

Notes:
7. In "Normal spinning, the FG changes its state at each edge of O1.
8. When the motor locks with South pole at the Hall element, O2 is kept on "L" and O1 is a clock with Ton/Toff ratio. When motor locks with North pole at the Hall element, O1 is kept on "L", O2 is a clock with Ton/Toff ratio.
9. When "Re-start spinning" occurs, the motor speed ramps up to the "Normal Spinning" speed from zero. Speed ramp-up profile depends on motor characteristics.
10. X: H or L depends on magnetic pole north or South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## Application Note

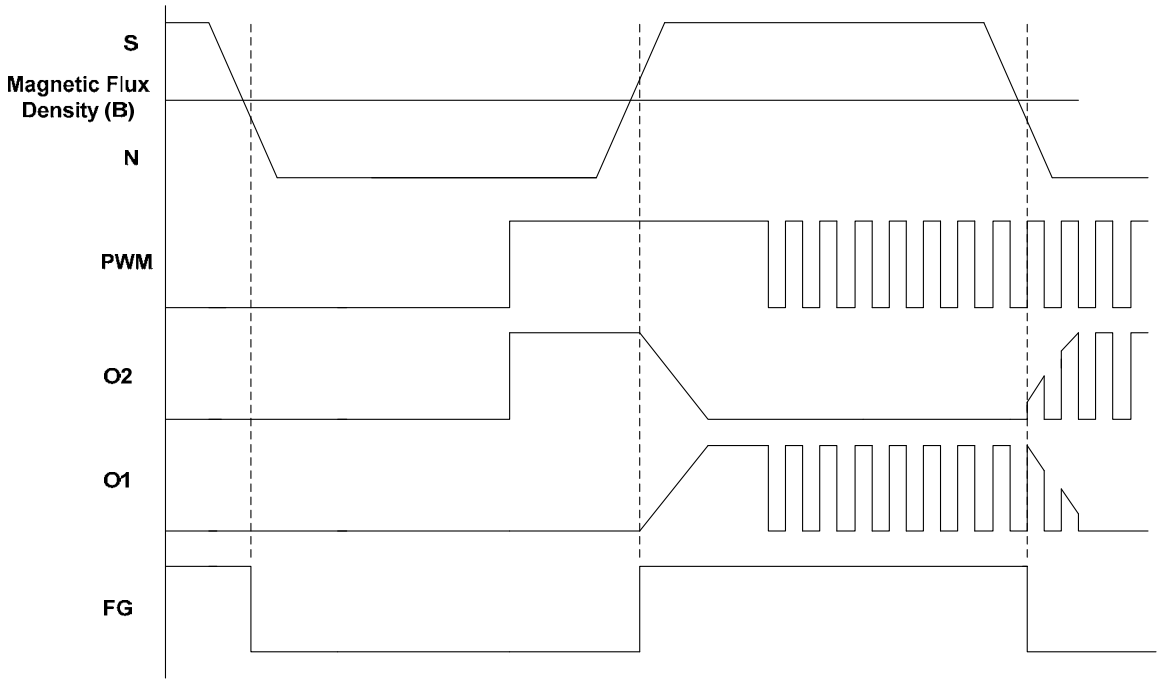
### DC Supply voltage speed control

Motor speed can be controlled by varying the Vdd supply voltage while PWM pin is tied to Vdd pin.

For example, with 5V nominal motor, changing supply voltage between 5V to 1.8V, speed can be reduced from 100% to 36% typically.

### PWM speed control

Motor speed can also be adjusted by applying a PWM speed control signal into the PWM pin while keeping the Vdd pin at nominal motor voltage. The motor speed is proportional to the PWM signal duty. For example, with 5V nominal motor, Vdd pin is maintained at 5V typical while varying the PWM control signal duty to adjust the motor speed linearly. Figure below shows the output O1 and O2 in relation to PWM speed control signal at PWM pin.




Frequency of PWM speed control signal can be between 15Hz to 50kHz. Recommended typical PWM signal frequency is 25kHz to keep switching frequency away from audible band. If PWM signal level at PWM pin stays low for longer than 65ms typical, the outputs are disabled.

Depending on the motor design and its inertia, minimum start-up PWM duty required can be typically between 30% - 40% while minimum running PWM duty can be down to 20%-25% typical. If voltage at Vdd is lower than the nominal motor voltage, both start-up PWM duty and minimum running PWM duty required will be higher.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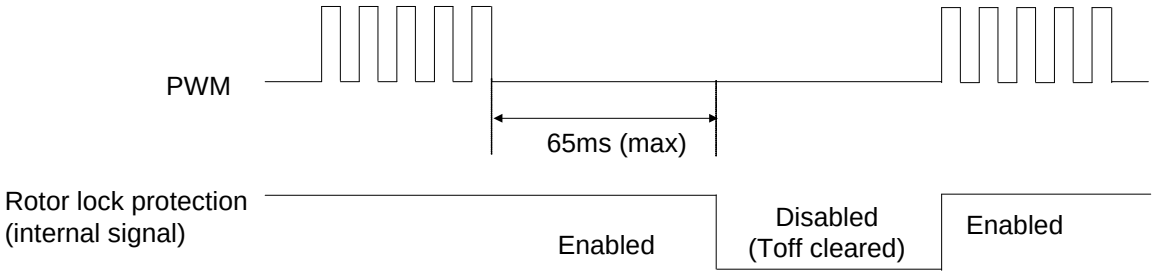
## Application Note (cont.)

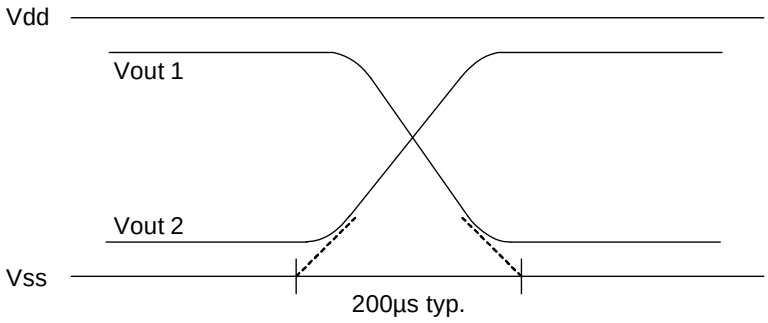
### Rotor lock Toff and PWM signal

When PWM signal input at PWM pin is low for longer than 65ms, internal rotor lock protection Toff is cleared. This allows the device to enter motor start Ton time on the next PWM high signal




### Soft Switching

AH5795 uses soft switching of the motor coil current during commutation for to minimize audible switching noise and electromagnetic interference (EMI) to provide a low noise solution.





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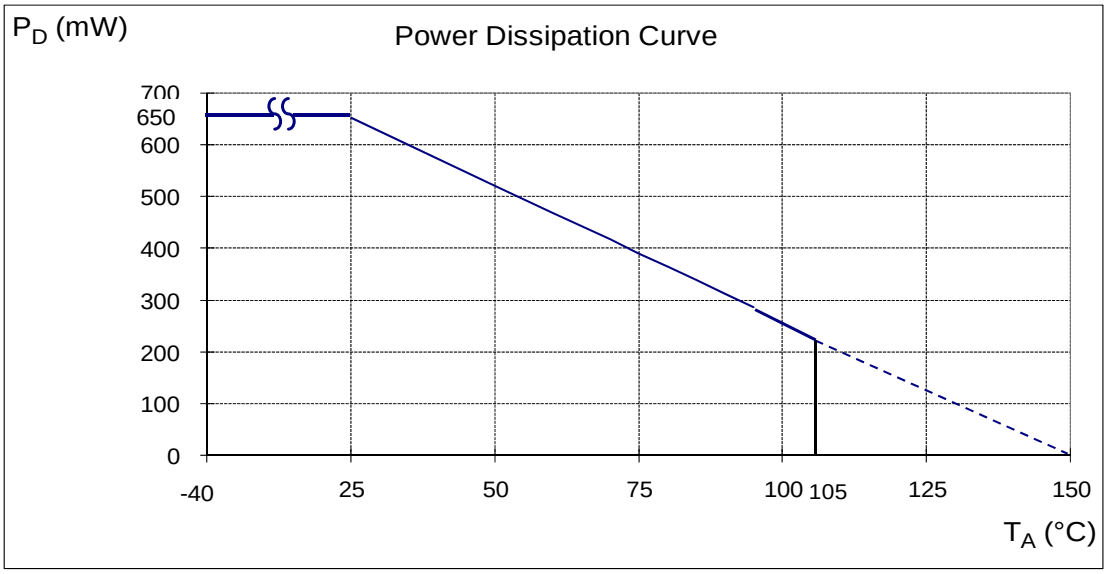
## Thermal Performance Characteristics

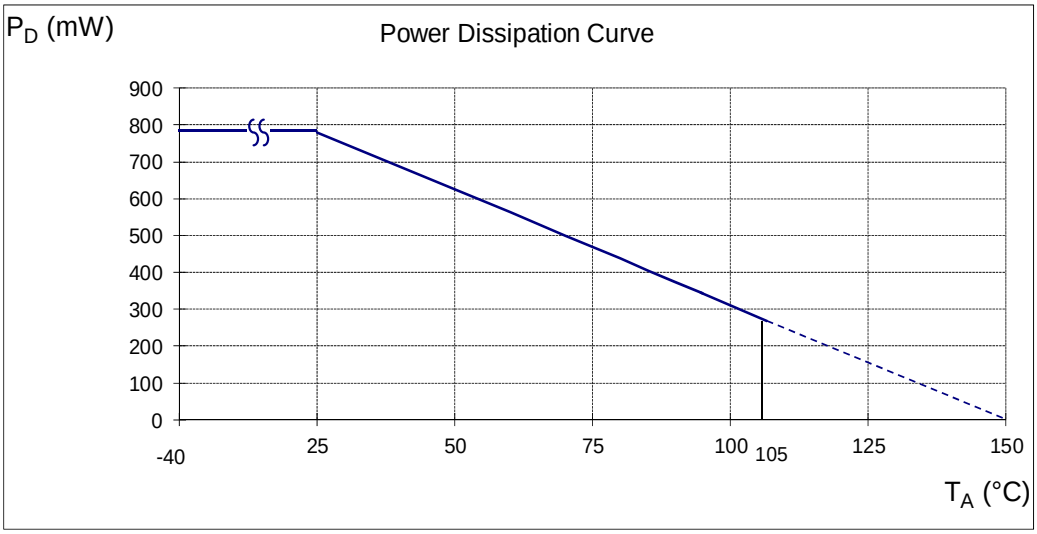
### (1) TSOT23-6

| $T_A$ (°C) | 25 | 50 | 60 | 70 | 75 | 80 | 85 | 90 | 95 | 100 |
|---|---|---|---|---|---|---|---|---|---|---|
| $P_D$ (mW) | 651 | 521 | 469 | 417 | 391 | 365 | 339 | 313 | 286 | 260 |
| **$T_A$ (°C)** | **105** | **110** | **115** | **120** | **125** | **130** | **135** | **140** | **145** | **150** |
| $P_D$ (mW) | 234 | 208 | 182 | 156 | 130 | 104 | 78 | 52 | 26 | 0 |




### (2) DFN2020C-6 (Note 11)

| $T_A$ (°C) | 25 | 50 | 60 | 70 | 75 | 80 | 85 | 90 | 95 | 100 |
|---|---|---|---|---|---|---|---|---|---|---|
| $P_D$ (mW) | 781 | 625 | 563 | 500 | 469 | 438 | 406 | 375 | 344 | 313 |
| **$T_A$ (°C)** | **105** | **110** | **115** | **120** | **125** | **130** | **135** | **140** | **145** | **150** |
| $P_D$ (mW) | 281 | 250 | 219 | 188 | 156 | 125 | 94 | 63 | 31 | 0 |




Notes: 11. DFN2020C-6 exposed pad soldered to minimum recommended landing pads (see Package Outline Dimension section) on a two-layer 2oz. copper FR4 PCB (1.6mm thickness) with no thermal vias in exposed PADs or any copper flood connecting to the landing pattern of the exposed pad.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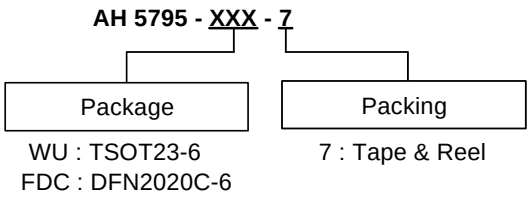
## Ordering information



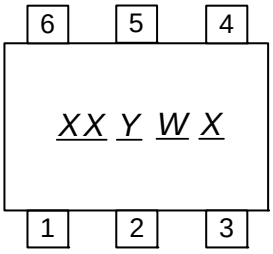

| Device | Package Code | Packaging (Note 12 & 13) | 7" Tape and Reel | |
|---|---|---|---|---|
| | | | Quantity | Part Number Suffix |
| AH5795-WU-7 | WU | TSOT23-6 | 3000/Tape & Reel | -7 |
| AH5795-FDC-7 | FDC | DFN2020C-6 | 3000/Tape & Reel | -7 |

Notes: 12. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf
13. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead_free.html

## Marking Information

### (1) TSOT23-6

**(Top View)**

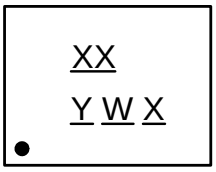



XX : Identification code
Y : Year 0~9
W : Week : A~Z : 1~26 week;
a~z : 27~52 week;
z represents 52 and 53 week
X : Internal code

| Part Number | Package | Identification Code |
|---|---|---|
| AH5795-WU-7 | TSOT23-6 | J5 |

### (2) DFN2020C-6

**( Top View )**




XX : Identification Code
Y : Year : 0~9
W : Week : A~Z : 1~26 week;
a~z : 27~52 week; z represents 52 and 53 week
X : Internal code

| Part Number | Package | Identification Code |
|---|---|---|
| AH5795-FDC-7 | DFN2020C-6 | J5 |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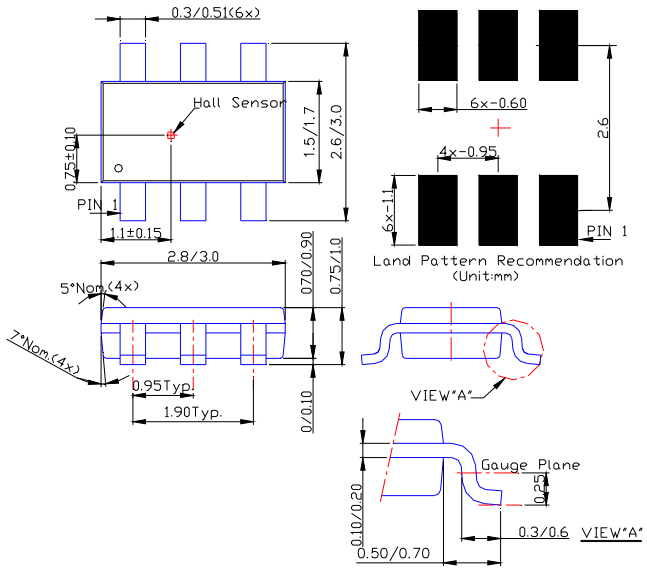
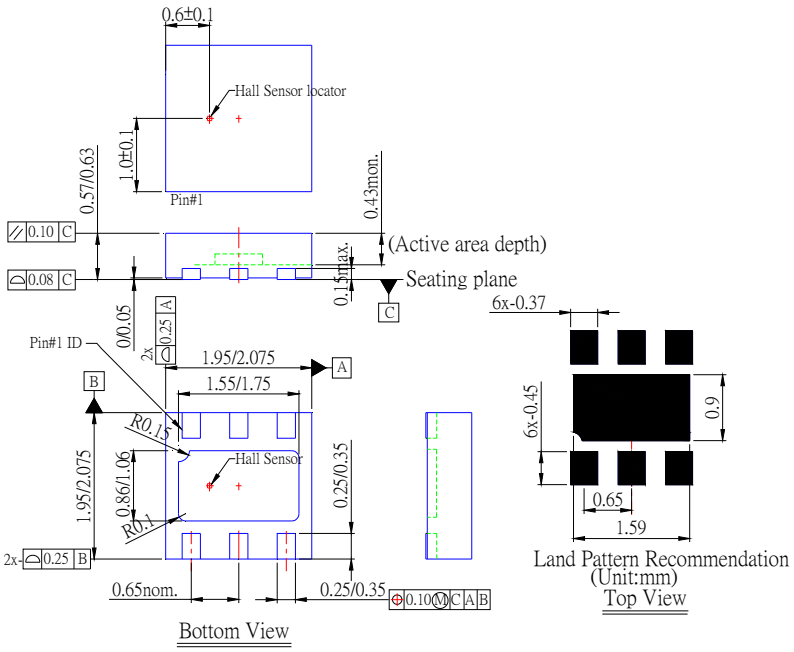
## Package Outline Dimensions (All Dimensions in mm)

### (1) Package type: TSOT23-6




### (2) Package Type: DFN2020C-6





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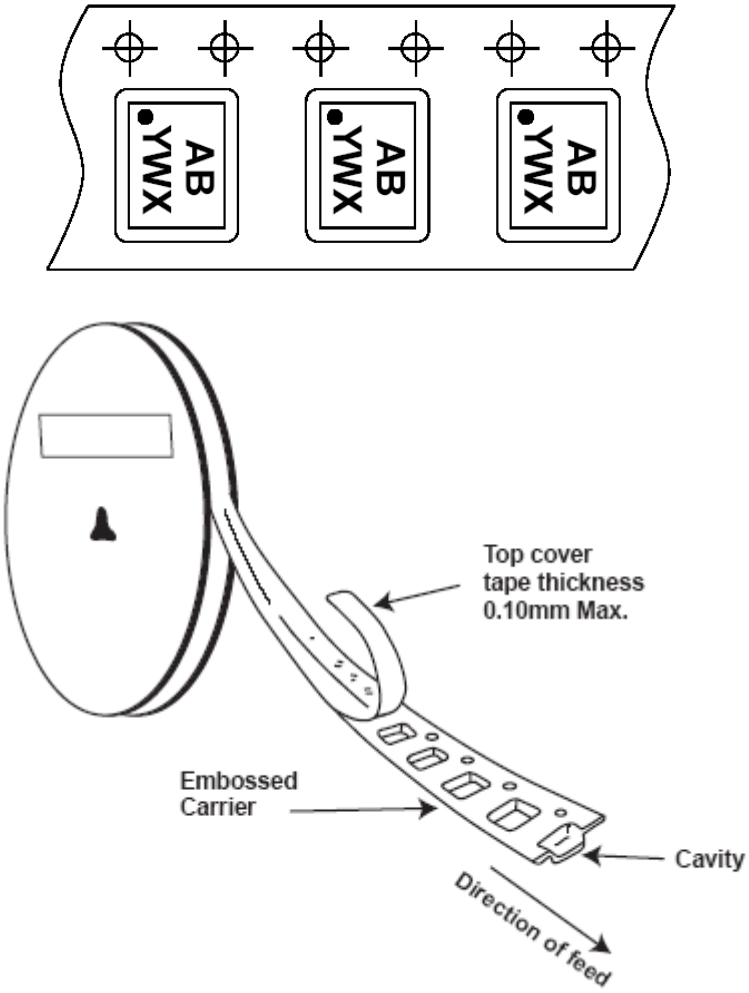
## Taping Orientation

**For DFN2020C-6**




Notes: 14. The taping orientation of the other package type can be found on our website at http://www.diodes.com/datasheets/ap02007.pdf.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