

1.25MHz, Over-The-Top Micropower, Rail-to-Rail Input and Output Op Amp in SOT-23

FEATURES

- Operates with Inputs Above V^+
- Rail-to-Rail Input and Output
- Micropower: 300 μ A Supply Current Max
- Operating Temperature Range: -40°C to 125°C
- Gain Bandwidth Product: 1.25MHz
- Slew Rate: 0.42V/ μ s
- Low Input Offset Voltage: 800 μ V Max
- Single Supply Input Range: 0V to 18V
- High Output Current: 18mA Min
- Specified on 3V, 5V and ± 5 V Supplies
- Output Shutdown on 6-Lead Version
- Reverse-Battery Protection to 18V
- High Voltage Gain: 1500V/mV
- Low Profile (1mm) ThinSOT™ Package

APPLICATIONS

- Portable Instrumentation
- Battery- or Solar-Powered Systems
- Sensor Conditioning
- Supply Current Sensing
- Battery Monitoring
- MUX Amplifiers
- 4mA to 20mA Transmitters

DESCRIPTION

The LT[®]1783 is a 1.25MHz op amp available in the small SOT-23 package that operates on all single and split supplies with a total voltage of 2.5V to 18V. The amplifier draws less than 300 μ A of quiescent current and has reverse-battery protection, drawing negligible current for reverse supply voltages up to 18V.

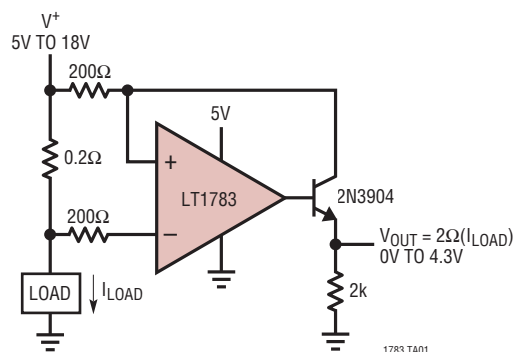
The input range of the LT1783 includes ground, and a unique feature of this device is its Over-The-Top[®] operation capability with either or both of its inputs above the positive rail. The inputs handle 18V both differential and common mode, independent of supply voltage. The input stage incorporates phase reversal protection to prevent false outputs from occurring even when the inputs are 9V below the negative supply.

The LT1783 can drive loads up to 18mA and still maintain rail-to-rail capability. A shutdown feature on the 6-lead version can disable the part, making the output high impedance and reducing quiescent current to 5 μ A. The LT1783 op amp is available in the 5- and 6-lead SOT-23 packages. For applications requiring lower power, refer to the LT1782.

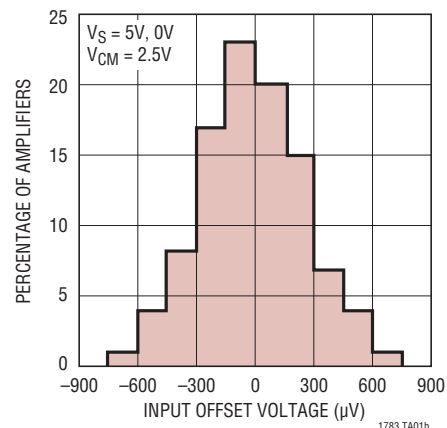
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TYPICAL APPLICATION

Positive Supply Rail Current Sense



Distribution of Input Offset Voltage



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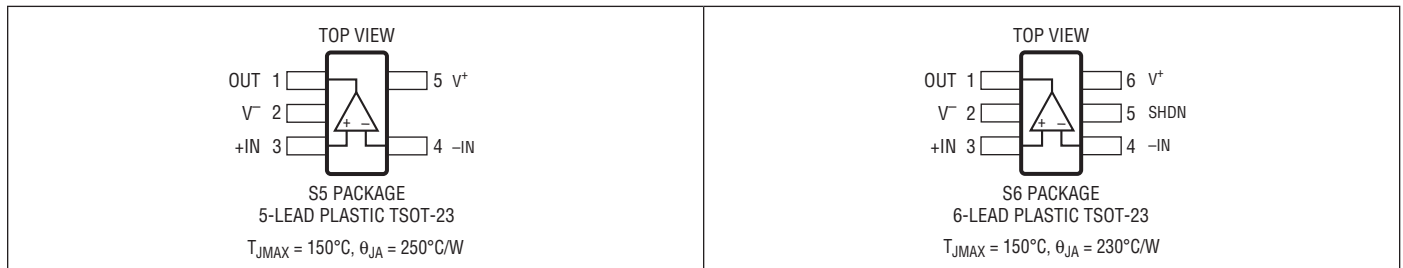
LT1783

ABSOLUTE MAXIMUM RATINGS (Note 1)

Total Supply Voltage (V^+ to V^-)	18V
Input Differential Voltage.....	18V
Input Pin Voltage to V^-	+24V/-10V
Shutdown Pin Voltage Above V^-	18V
Shutdown Pin Current	$\pm 10\text{mA}$
Output Short-Circuit Duration (Note 2)	Indefinite
Operating Temperature Range (Note 3)	
LT1783C	-40°C to 85°C
LT1783I	-40°C to 85°C
LT1783H	-40°C to 125°C

Specified Temperature Range (Note 4)	
LT1783C	-40°C to 85°C
LT1783I	-40°C to 85°C
LT1783H	-40°C to 125°C
Junction Temperature	150°C
Storage Temperature Range.....	-65°C to 150°C
Lead Temperature (Soldering, 10 sec)	300°C

PIN CONFIGURATION



ORDER INFORMATION

TAPE AND REEL (MINI)	TAPE AND REEL	PART MARKING	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE
LT1783CS5#TRMPBF	LT1783CS5#TRPBF	LTLF	5-Lead Plastic SOT-23	0°C to 70°C
LT1783IS5#TRMPBF	LT1783IS5#TRPBF	LTIG	5-Lead Plastic SOT-23	-40°C to 85°C
LT1783HS5#TRMPBF	LT1783HS5#TRPBF	LTXR	5-Lead Plastic SOT-23	-40°C to 125°C
LT1783CS6#TRMPBF	LT1783CS6#TRPBF	LTIU	6-Lead Plastic SOT-23	0°C to 70°C
LT1783IS6#TRMPBF	LT1783IS6#TRPBF	LTIV	6-Lead Plastic SOT-23	-40°C to 85°C
LT1783HS6#TRMPBF	LT1783HS6#TRPBF	LTXS	6-Lead Plastic SOT-23	-40°C to 125°C

TRM = 500 pieces. *Temperature grades are identified by a label on the shipping container.

Consult LTC Marketing for parts specified with wider operating temperature ranges.

Consult LTC Marketing for information on lead based finish parts.

For more information on lead free part marking, go to: <http://www.linear.com/leadfree/>

For more information on tape and reel specifications, go to: <http://www.linear.com/tapeandreel/>

ELECTRICAL CHARACTERISTICS The ● denotes the specifications which apply over the specified temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. $V_S = 3\text{V}$, 0V ; $V_S = 5\text{V}$, 0V ; $V_{CM} = V_{OUT} = \text{half supply}$, for the 6-lead part $V_{PIN5} = 0\text{V}$, pulse power tested unless otherwise specified. (Note 4)

SYMBOL	PARAMETER	CONDITIONS	LT1783C/LT1783I			UNITS
			MIN	TYP	MAX	
V_{OS}	Input Offset Voltage	$T_A = 25^\circ\text{C}$ $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$ $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●	400	800	μV
			●		950	μV
					1100	μV
	Input Offset Voltage Drift (Note 9)		●	2	5	$\mu\text{V}/^\circ\text{C}$
I_{OS}	Input Offset Current	$V_{CM} = 18\text{V}$ (Note 5)	●	4	8	nA
			●		7	μA
I_B	Input Bias Current	$V_{CM} = 18\text{V}$ (Note 5) SHDN or $V_S = 0\text{V}$, $V_{CM} = 0\text{V}$ to 18V	●	45	80	nA
			●	35	60	μA
				0.1		nA
	Input Bias Current Drift		●	0.06		nA/ $^\circ\text{C}$
	Input Noise Voltage	0.1Hz to 10Hz		0.6		μV_{P-P}
e_n	Input Noise Voltage Density	$f = 1\text{kHz}$		20		nV/ $\sqrt{\text{Hz}}$
i_n	Input Noise Current Density	$f = 1\text{kHz}$		0.14		pA/ $\sqrt{\text{Hz}}$
R_{IN}	Input Resistance	Differential Common Mode, $V_{CM} = 0\text{V}$ to $(V_{CC} - 1\text{V})$ Common Mode, $V_{CM} = 0\text{V}$ to 18V	●	0.65	1.3	M Ω
			●	0.3	0.5	G Ω
						M Ω
C_{IN}	Input Capacitance			5		pF
	Input Voltage Range		●	0	18	V
CMRR	Common Mode Rejection Ratio (Note 5)	$V_{CM} = 0\text{V}$ to $V_{CC} - 1\text{V}$ $V_{CM} = 0\text{V}$ to 18V (Note 8)	●	90	100	dB
			●	68	80	dB
PSRR	Power Supply Rejection Ratio	$V_S = 3\text{V}$ to 12.5V , $V_{CM} = V_O = 1\text{V}$	●	90	100	dB
A_{VOL}	Large-Signal Voltage Gain	$V_S = 3\text{V}$, $V_O = 500\text{mV}$ to 2.5V , $R_L = 10\text{k}$ $V_S = 3\text{V}$, $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$ $V_S = 3\text{V}$, $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●	200	1500	V/mV
			●	133		V/mV
			●	100		V/mV
		$V_S = 5\text{V}$, $V_O = 500\text{mV}$ to 4.5V , $R_L = 10\text{k}$ $V_S = 5\text{V}$, $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$ $V_S = 5\text{V}$, $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●	400	1500	V/mV
			●	250		V/mV
			●	200		V/mV
V_{OL}	Output Voltage Swing LOW	No Load $I_{SINK} = 5\text{mA}$ $V_S = 5\text{V}$, $I_{SINK} = 10\text{mA}$	●	3	8	mV
			●	200	400	mV
			●	330	600	mV
V_{OH}	Output Voltage Swing HIGH	$V_S = 3\text{V}$, No Load $V_S = 3\text{V}$, $I_{SOURCE} = 5\text{mA}$	●	2.91	2.94	V
			●	2.6	2.8	V
		$V_S = 5\text{V}$, No Load $V_S = 5\text{V}$, $I_{SOURCE} = 10\text{mA}$	●	4.91	4.94	V
			●	4.5	4.74	V
I_{SC}	Short-Circuit Current (Note 2)	$V_S = 3\text{V}$, Short to GND $V_S = 3\text{V}$, Short to V_{CC}		5	10	mA
				15	30	mA
		$V_S = 5\text{V}$, Short to GND $V_S = 5\text{V}$, Short to V_{CC}		15	30	mA
				20	40	mA
	Minimum Supply Voltage		●	2.5	2.7	V
	Reverse Supply Voltage	$I_S = -100\mu\text{A}$	●	18		V
I_S	Supply Current (Note 6)	$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$ $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●	210	300	μA
			●		350	μA
			●		375	μA
	Supply Current, SHDN	$V_{PIN5} = 2\text{V}$, No Load (Note 10)	●	5	18	μA
I_{SHDN}	Shutdown Pin Current	$V_{PIN5} = 0.3\text{V}$, No Load (Note 10) $V_{PIN5} = 2\text{V}$, No Load (Note 10) $V_{PIN5} = 5\text{V}$, No Load (Note 10)	●	0.5		nA
			●	2	8	μA
				5		μA
	Shutdown Output Leakage Current	$V_{PIN5} = 2\text{V}$, No Load (Note 10)	●	0.05	1	μA
	Maximum Shutdown Pin Current	$V_{PIN5} = 18\text{V}$, No Load (Note 10)	●	10	30	μA

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SYMBOL	PARAMETER	CONDITIONS	LT1783C/LT1783I			UNITS
			MIN	TYP	MAX	
V_L	Shutdown Pin Input Low Voltage	(Note 10)	●		0.3	V
V_H	Shutdown Pin Input High Voltage	(Note 10)	●	2		V
t_{ON}	Turn-On Time	$V_{PIN5} = 5\text{V to } 0\text{V}, R_L = 10\text{k}$ (Note 10)		25		μs
t_{OFF}	Turn-Off Time	$V_{PIN5} = 0\text{V to } 5\text{V}, R_L = 10\text{k}$ (Note 10)		3		μs
GBW	Gain Bandwidth Product (Note 5)	$f = 5\text{kHz}$	●	750	1250	kHz
		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	●	600		kHz
		$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●	550		kHz
SR	Slew Rate (Note 7)	$A_V = -1, R_L = \infty$	●	0.24	0.42	V/ μs
		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	●	0.21		V/ μs
		$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●	0.19		V/ μs
FPBW	Full-Power Bandwidth (Note 11)	$V_{OUT} = 2V_{P-P}$		66		kHz
t_S	Settling Time	$V_S = 5\text{V}, \Delta V_{OUT} = 2\text{V to } 0.1\%, A_V = -1$		12		μs
THD	Distortion	$V_S = 3\text{V}, V_O = 2V_{P-P}, A_V = 1, R_L = 10\text{k}, f = 1\text{kHz}$		0.001		%

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SYMBOL	PARAMETER	CONDITIONS	LT1783C/LT1783I			UNITS
			MIN	TYP	MAX	
V_{OS}	Input Offset Voltage	$T_A = 25^\circ\text{C}$	●	500	900	μV
		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	●		1050	μV
		$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●		1200	μV
	Input Offset Voltage Drift (Note 9)		●	2	5	$\mu\text{V}/^\circ\text{C}$
I_{OS}	Input Offset Current		●	4	8	nA
I_B	Input Bias Current		●	40	80	nA
			●	0.06		nA/ $^\circ\text{C}$
	Input Noise Voltage	0.1Hz to 10Hz		1		μV_{P-P}
e_n	Input Noise Voltage Density	$f = 1\text{kHz}$		20		nV/ $\sqrt{\text{Hz}}$
i_n	Input Noise Current Density	$f = 1\text{kHz}$		0.14		pA/ $\sqrt{\text{Hz}}$
R_{IN}	Input Resistance	Differential	●	0.65	1.3	M Ω
		Common Mode, $V_{CM} = -5\text{V to } 13\text{V}$	●	0.3	0.5	M Ω
C_{IN}	Input Capacitance			5		pF
	Input Voltage Range		●	-5	13	V
CMRR	Common Mode Rejection Ratio	$V_{CM} = -5\text{V to } 13\text{V}$	●	68	80	dB
A_{VOL}	Large-Signal Voltage Gain	$V_O = \pm 4\text{V}, R_L = 10\text{k}$	●	70	160	V/mV
		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	●	50		V/mV
		$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●	40		V/mV
V_{OL}	Output Voltage Swing LOW	No Load	●	-4.997	-4.992	V
		$I_{SINK} = 5\text{mA}$	●	-4.8	-4.6	V
		$I_{SINK} = 10\text{mA}$	●	-4.67	-4.4	V
V_{OH}	Output Voltage Swing HIGH	No Load	●	4.91	4.94	V
		$I_{SOURCE} = 5\text{mA}$	●	4.6	4.8	V
		$I_{SOURCE} = 10\text{mA}$	●	4.5	4.74	V
I_{SC}	Short-Circuit Current (Note 2)	Short to GND	●	18	30	mA
		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	●	15		mA
PSRR	Power Supply Rejection Ratio	$V_S = \pm 1.5\text{V to } \pm 9\text{V}$	●	90	100	dB

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SYMBOL	PARAMETER	CONDITIONS	LT1783C/LT1783I			UNITS
			MIN	TYP	MAX	
I_S	Supply Current	$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$ $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	● ●	230	325 375 400	μA μA μA
	Supply Current, SHDN	$V_{PIN5} = -3\text{V}$, $V_S = \pm 5\text{V}$, No Load (Note 10)	●	6	20	μA
I_{SHDN}	Shutdown Pin Current	$V_{PIN5} = -4.7\text{V}$, $V_S = \pm 5\text{V}$, No Load (Note 10) $V_{PIN5} = -3\text{V}$, $V_S = \pm 5\text{V}$, No Load (Note 10)	●	0.5 2	8	nA μA
	Maximum Shutdown Pin Current	$V_{PIN5} = 9\text{V}$, $V_S = \pm 9\text{V}$ (Note 10)	●	10	30	μA
	Shutdown Output Leakage Current	$V_{PIN5} = -7\text{V}$, $V_S = \pm 9\text{V}$, No Load (Note 10)	●	0.05	1	μA
V_L	Shutdown Pin Input Low Voltage	$V_S = \pm 5\text{V}$ (Note 10)	●		-4.7	V
V_H	Shutdown Pin Input High Voltage	$V_S = \pm 5\text{V}$ (Note 10)	●	-2.8		V
t_{ON}	Turn-On Time	$V_{PIN5} = 0\text{V}$ to -5V , $R_L = 10\text{k}$ (Note 10)	●	25		μs
t_{OFF}	Turn-Off Time	$V_{PIN5} = -5\text{V}$ to 0V , $R_L = 10\text{k}$ (Note 10)	●	3		μs
GBW	Gain Bandwidth Product	$f = 5\text{kHz}$ $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$ $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	● ● ●	800 700 625	1300	kHz kHz kHz
SR	Slew Rate	$A_V = -1$, $R_L = \infty$, $V_O = \pm 4\text{V}$, Measured at $V_O = \pm 2\text{V}$ $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$ $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	● ● ●	0.26 0.23 0.21	0.45	V/ μs V/ μs V/ μs
FPBW	Full-Power Bandwidth (Note 11)	$V_{OUT} = 8V_{P-P}$		18		kHz
t_S	Settling Time	$\Delta V_{OUT} = 4\text{V}$ to 0.1% , $A_V = 1$		10		μs

The ● denotes the specifications which apply over the full operating temperature range of $-40^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$. $V_S = 3\text{V}$, 0V ; $V_S = 5\text{V}$, 0V ; $V_{CM} = V_{OUT} = \text{half supply}$, for the 6-lead part $V_{PIN5} = 0\text{V}$, pulse power tested unless otherwise specified. (Note 4)

SYMBOL	PARAMETER	CONDITIONS	LT1783H			UNITS
			MIN	TYP	MAX	
V_{OS}	Input Offset Voltage		●	400	850 3	μV mV
	Input Offset Voltage Drift (Note 9)		●		15	$\mu\text{V}/^\circ\text{C}$
I_{OS}	Input Offset Current	$V_{CM} = 18\text{V}$ (Note 5)	● ●		15 10	nA μA
I_B	Input Bias Current	$V_{CM} = 18\text{V}$ (Note 5)	● ●		150 150	nA μA
	Input Voltage Range		●	0.3	18	V
CMRR	Common Mode Rejection Ratio	$V_{CM} = 0.3\text{V}$ to $V_{CC} - 1\text{V}$ $V_{CM} = 0.3\text{V}$ to 18V	● ●	76 60		dB dB
A_{VOL}	Large-Signal Voltage Gain	$V_S = 3\text{V}$, $V_O = 500\text{mV}$ to 2.5V , $R_L = 10\text{k}$	●	200 50	1500	V/mV V/mV
		$V_S = 5\text{V}$, $V_O = 500\text{mV}$ to 4.5V , $R_L = 10\text{k}$	●	400 100	1500	V/mV V/mV
V_{OL}	Output Voltage Swing LOW	No Load	●		15	mV
		$I_{SINK} = 5\text{mA}$	●		800	mV
		$V_S = 5\text{V}$, $I_{SINK} = 10\text{mA}$	●		1200	mV
V_{OH}	Output Voltage Swing HIGH	$V_S = 3\text{V}$, No Load	●	2.85		V
		$V_S = 3\text{V}$, $I_{SOURCE} = 5\text{mA}$	●	2.30		V
		$V_S = 5\text{V}$, No Load	●	4.85		V
		$V_S = 5\text{V}$, $I_{SOURCE} = 10\text{mA}$	●	4		V
PSRR	Power Supply Rejection Ratio	$V_S = 3\text{V}$ to 12.5V , $V_{CM} = V_O = 1\text{V}$	●	80		dB

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SYMBOL	PARAMETER	CONDITIONS	LT1783H			UNITS
			MIN	TYP	MAX	
	Minimum Supply Voltage		●	2.7		V
	Reverse Supply Voltage	$I_S = -100\mu\text{A}$	●	18		V
I_S	Supply Current		●	210	300 600	μA μA
	Supply Current, SHDN	$V_{PIN5} = 2\text{V}$, No Load (Note 10)	●		25	μA
I_{SHDN}	Shutdown Pin Current	$V_{PIN5} = 0.3\text{V}$, No Load (Note 10) $V_{PIN5} = 2\text{V}$, No Load (Note 10)	● ●	0.5	12	nA μA
	Output Leakage Current	$V_{PIN5} = 2\text{V}$, No Load (Note 10)	●		2.5	μA
	Maximum Shutdown Pin Current	$V_{PIN5} = 18\text{V}$, No Load (Note 10)	●		40	μA
GBW	Gain Bandwidth Product	$f = 5\text{kHz}$ (Note 5)	●	750 400	1250	kHz kHz
SR	Slew Rate	$A_V = -1$, $R_L = \infty$ (Note 7)	●	0.24 0.12	0.42	V/ μs V/ μs

The ● denotes the specifications which apply over the full operating temperature range of $-40^{\circ}\text{C} \leq T_A \leq 125^{\circ}\text{C}$. $V_S = \pm 5\text{V}$, $V_{CM} = 0\text{V}$, $V_{OUT} = 0\text{V}$, $V_{SHDN} = V^-$, $T_A = -40^{\circ}\text{C}$ to 125°C , for the 6-lead part $V_{PIN5} = V^-$, pulse power tested unless otherwise specified. (Note 4)

SYMBOL	PARAMETER	CONDITIONS	LT1783H			UNITS
			MIN	TYP	MAX	
V_{OS}	Input Offset Voltage		●	500	950 3.1	μV mV
	Input Offset Voltage Drift (Note 9)		●		15	$\mu\text{V}/^{\circ}\text{C}$
I_{OS}	Input Offset Current		●		15	nA
I_B	Input Bias Current		●		150	nA
CMRR	Common Mode Rejection Ratio	$V_{CM} = -4.7\text{V}$ to 13V	●	60		dB
A_{VOL}	Large-Signal Voltage Gain	$V_O = \pm 4\text{V}$, $R_L = 10\text{k}$	●	70 10	160	V/mV V/mV
V_O	Output Voltage Swing	No Load $I_{OUT} = \pm 5\text{mA}$ $I_{OUT} = \pm 10\text{mA}$	● ● ●	± 4.85 ± 4.20 ± 3.80		V V V
PSRR	Power Supply Rejection Ratio	$V_S = \pm 1.5\text{V}$ to $\pm 9\text{V}$	●	80		dB
	Minimum Supply Voltage		●	± 1.35		V
I_S	Supply Current		●	230	325 650	μA μA
	Supply Current, SHDN	$V_{PIN5} = -3\text{V}$, $V_S = \pm 5\text{V}$, No Load (Note 10)	●		25	μA
I_{SHDN}	Shutdown Pin Current	$V_{PIN5} = -4.7\text{V}$, $V_S = \pm 5\text{V}$, No Load (Note 10) $V_{PIN5} = -3\text{V}$, $V_S = \pm 5\text{V}$, No Load (Note 10)	● ●	0.5	12	nA μA
	Maximum Shutdown Pin Current	$V_{PIN5} = 9\text{V}$, $V_S = \pm 9\text{V}$ (Note 10)	●		45	μA
	Output Leakage Current	$V_{PIN5} = -7\text{V}$, $V_S = \pm 9\text{V}$, No Load	●		3	μA
V_L	Shutdown Pin Input Low Voltage	$V_S = \pm 5\text{V}$	●		-4.7	V
V_H	Shutdown Pin Input High Voltage	$V_S = \pm 5\text{V}$	●	-2.8		V
GBW	Gain Bandwidth Product	$f = 5\text{kHz}$	●	800 425	1300	kHz kHz
SR	Slew Rate	$A_V = -1$, $R_L = \infty$, $V_O = 4\text{V}$ Measured at $V_O = \pm 2\text{V}$	●	0.26 0.14	0.45	V/ μs V/ μs

ELECTRICAL CHARACTERISTICS

Note 1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

Note 2: A heat sink may be required to keep the junction temperature below absolute maximum.

Note 3: The LT1783C and LT1873I are guaranteed functional over the operating temperature range of -40°C to 85°C . The LT1783H is guaranteed functional over the operating temperature range of -40°C to 125°C .

Note 4: The LT1783C is guaranteed to meet specified performance from 0°C to 70°C . The LT1783C is designed, characterized and expected to meet specified performance from -40°C to 85°C but is not tested or QA sampled at these temperatures. The LT1783I is guaranteed to meet specified performance from -40°C to 85°C . The LT1783H is guaranteed to meet specified performance from -40°C to 125°C .

Note 5: $V_S = 5\text{V}$ limits are guaranteed by correlation to $V_S = 3\text{V}$ and $V_S = \pm 5\text{V}$ or $V_S = \pm 9\text{V}$ tests.

Note 6: $V_S = 3\text{V}$ limits are guaranteed by correlation to $V_S = 5\text{V}$ and $V_S = \pm 5\text{V}$ or $V_S = \pm 9\text{V}$ tests.

Note 7: Guaranteed by correlation to slew rate at $V_S = \pm 5\text{V}$, and GBW at $V_S = 3\text{V}$ and $V_S = \pm 5\text{V}$ tests.

Note 8: This specification implies a typical input offset voltage of 1.8mV at $V_{CM} = 18\text{V}$ and a maximum input offset voltage of 7.2mV at $V_{CM} = 18\text{V}$.

Note 9: This parameter is not 100% tested.

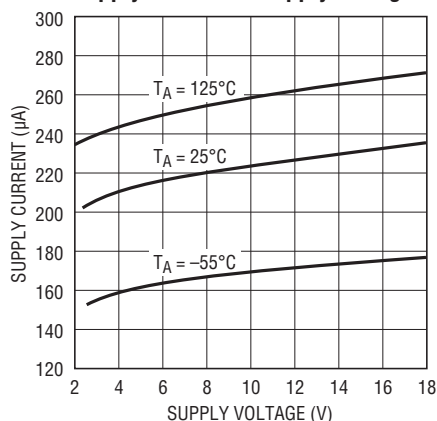
Note 10: Specifications apply to 6-lead SOT-23 with shutdown.

Note 11: Full-power bandwidth is calculated from the slew rate:

$$\text{FPBW} = \text{SR} / 2\pi V_P$$

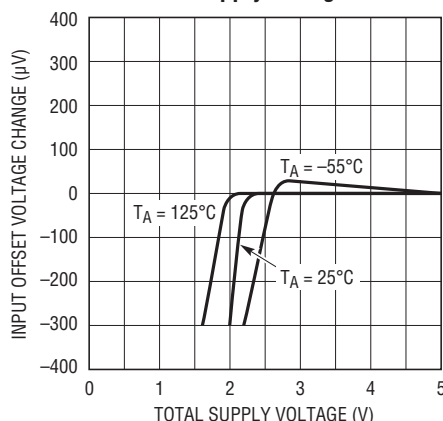
TYPICAL PERFORMANCE CHARACTERISTICS

Supply Current vs Supply Voltage



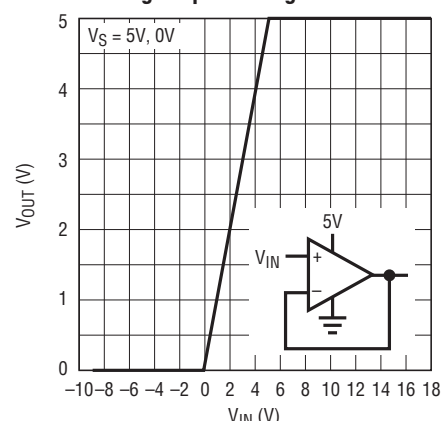
1783 G01

Minimum Supply Voltage



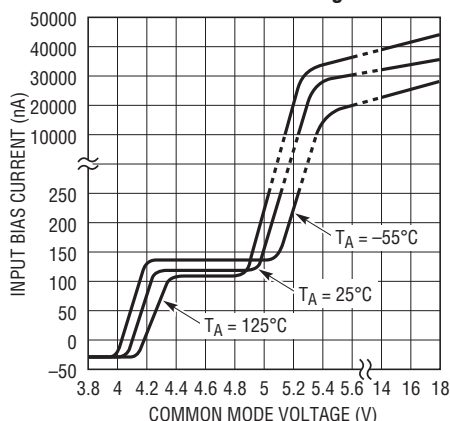
1783 G02

Output Voltage vs Large Input Voltage



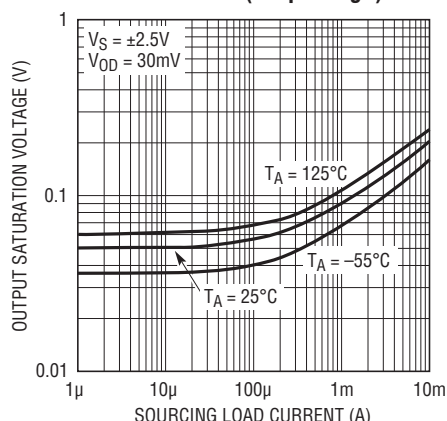
1783 G03

Input Bias Current vs Common Mode Voltage



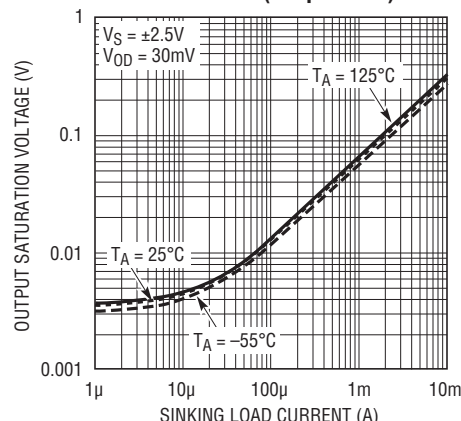
1783 G04

Output Saturation Voltage vs Load Current (Output High)



1783 G05

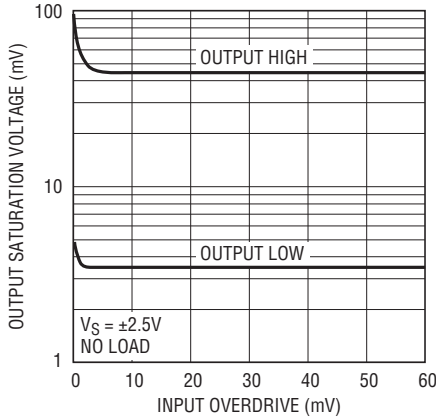
Output Saturation Voltage vs Load Current (Output Low)



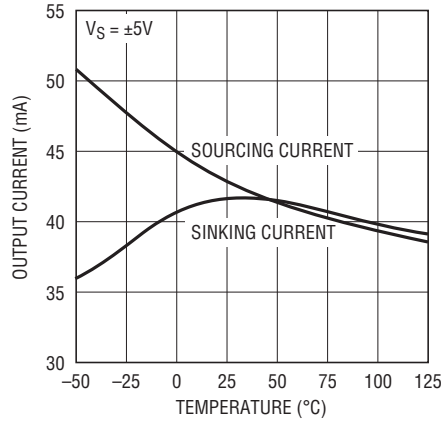
1783 G06

TYPICAL PERFORMANCE CHARACTERISTICS

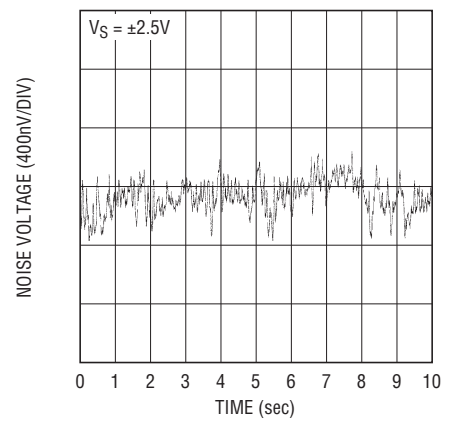
Output Saturation Voltage vs Input Overdrive



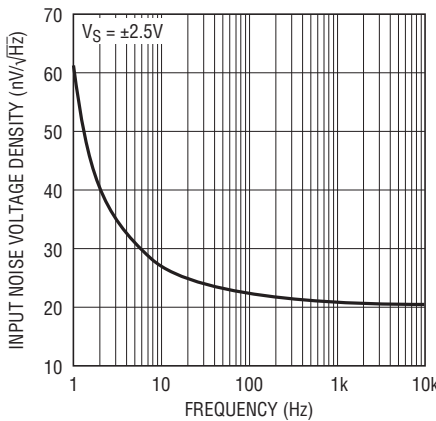
Output Short-Circuit Current vs Temperature



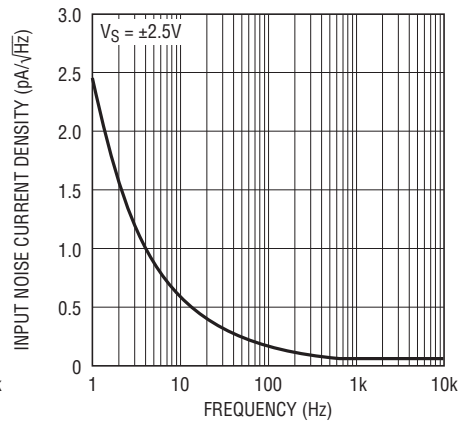
0.1Hz to 10Hz Noise Voltage



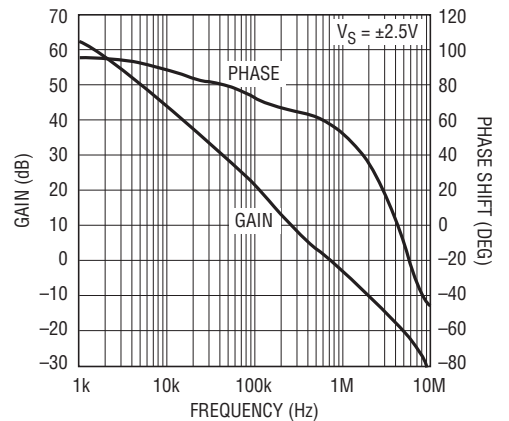
Noise Voltage Density vs Frequency



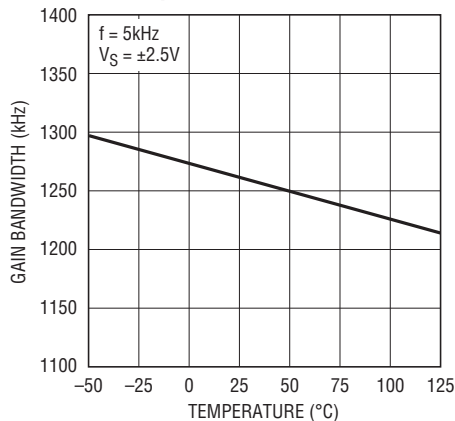
Input Noise Current vs Frequency



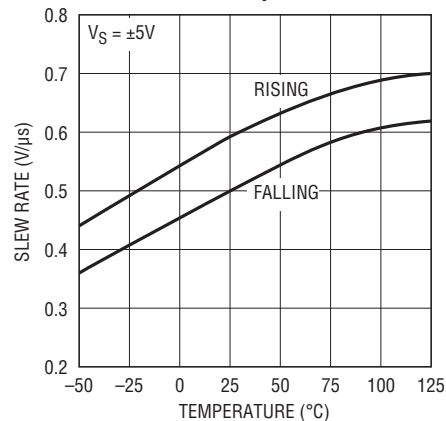
Gain and Phase Shift vs Frequency



Gain Bandwidth Product vs Temperature

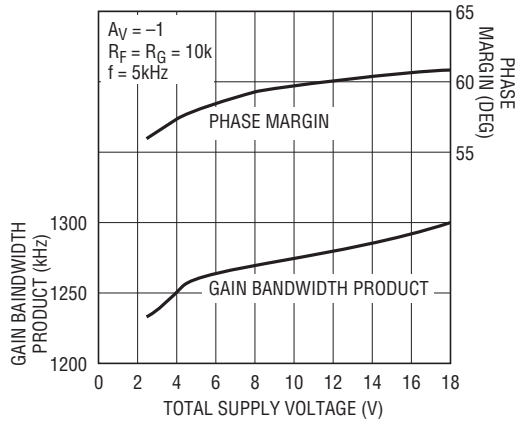


Slew Rate vs Temperature

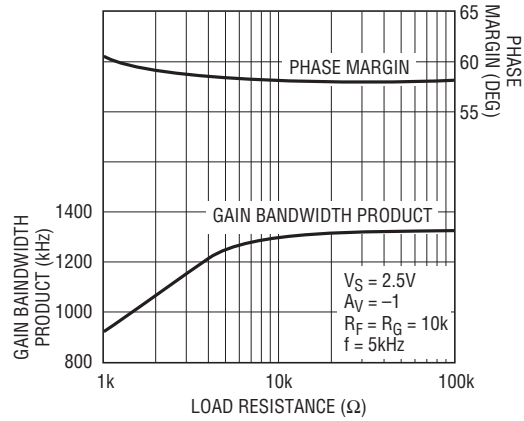


TYPICAL PERFORMANCE CHARACTERISTICS

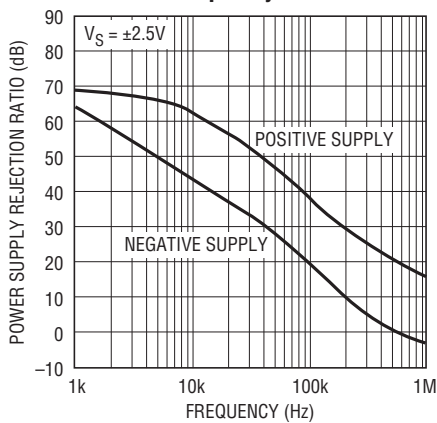
Gain Bandwidth Product and Phase Margin vs Supply Voltage



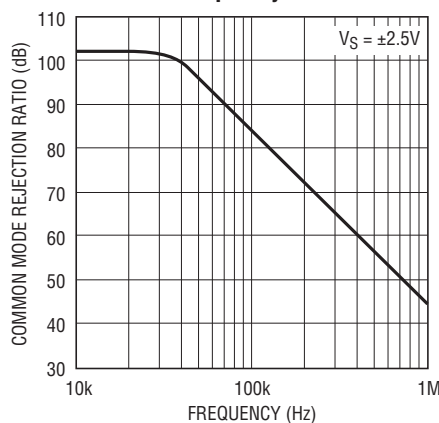
Gain Bandwidth and Phase Margin vs Load Resistance



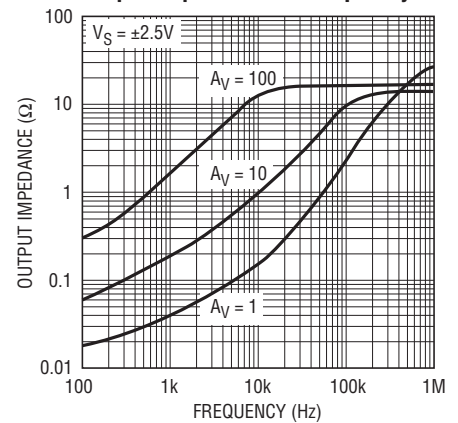
PSRR vs Frequency



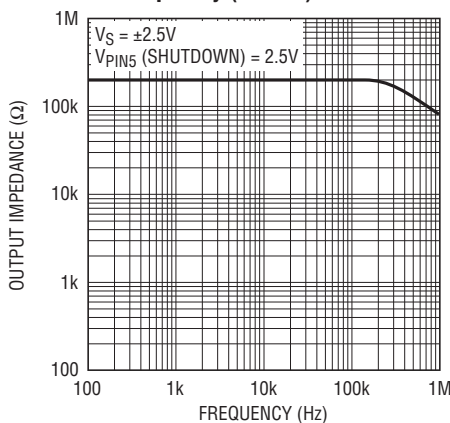
CMRR vs Frequency



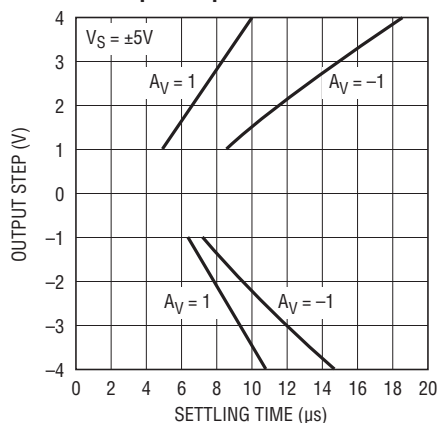
Output Impedance vs Frequency



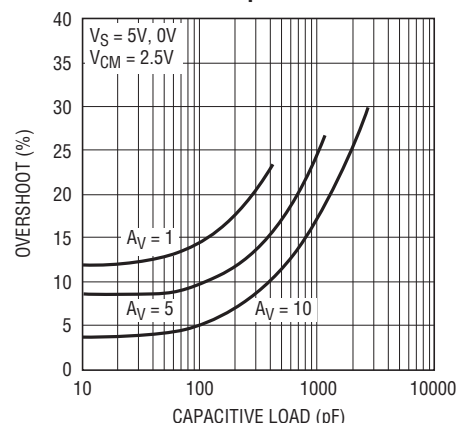
Disabled Output Impedance vs Frequency (Note 8)



Settling Time to 0.1% vs Output Step

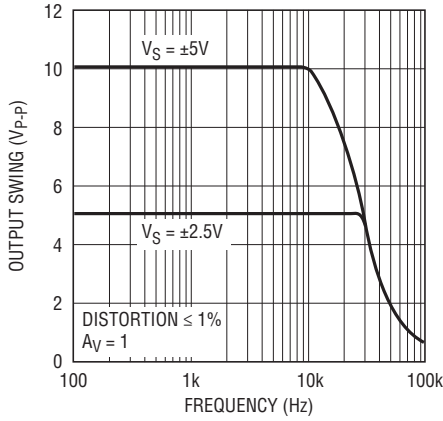


Capacitive Load Handling Overshoot vs Capacitive Load

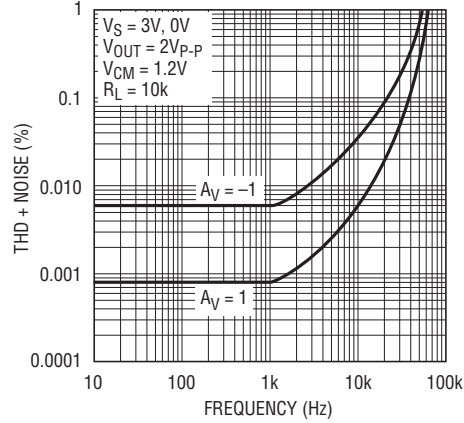


TYPICAL PERFORMANCE CHARACTERISTICS

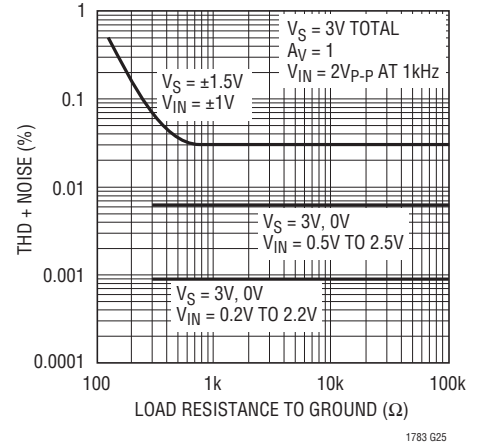
Undistorted Output Swing vs Frequency



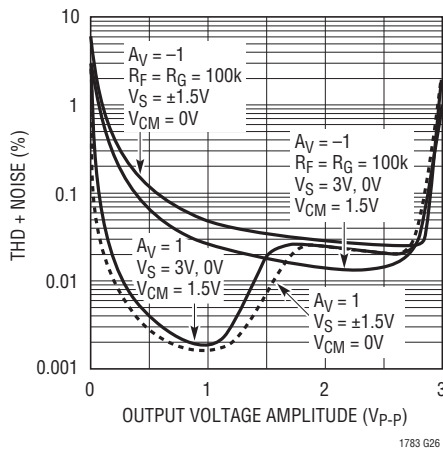
Total Harmonic Distortion + Noise vs Frequency



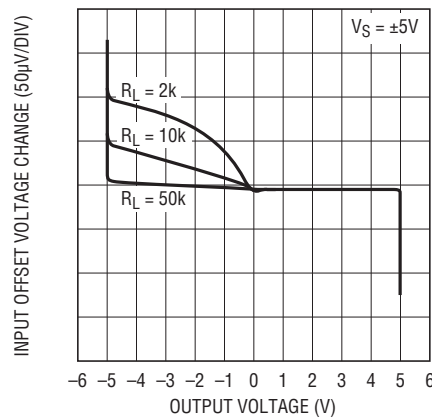
Total Harmonic Distortion + Noise vs Load Resistance



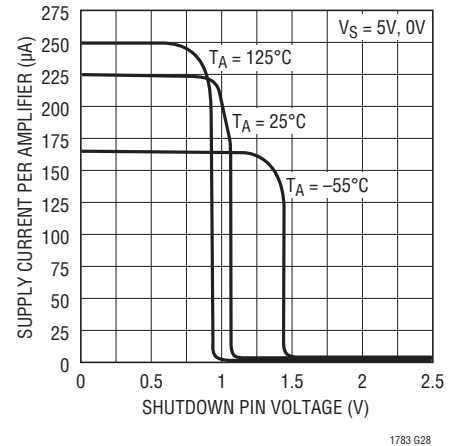
Total Harmonic Distortion + Noise vs Output Voltage Amplitude



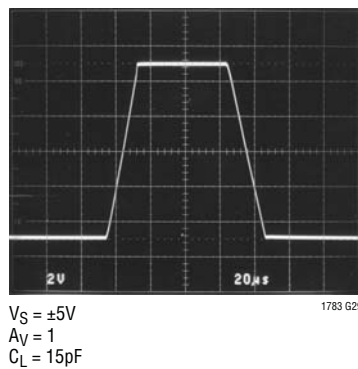
Open-Loop Gain



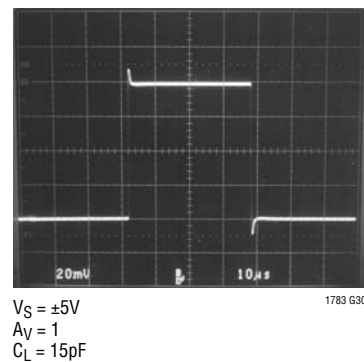
Supply Current vs SHDN Pin Voltage



Large-Signal Response



Small-Signal Response



APPLICATIONS INFORMATION

Supply Voltage

The positive supply pin of the LT1783 should be bypassed with a small capacitor (typically $0.1\mu\text{F}$) within an inch of the pin. When driving heavy loads, an additional $4.7\mu\text{F}$ electrolytic capacitor should be used. When using split supplies, the same is true for the negative supply pin.

The LT1783 is protected against reverse-battery voltages up to 18V. In the event a reverse-battery condition occurs, the supply current is less than 1nA.

Inputs

The LT1783 has two input stages, NPN and PNP (see the Simplified Schematic), resulting in three distinct operating regions as shown in the Input Bias Current vs Common Mode typical performance curve.

For input voltages about 0.8V or more below V^+ , the PNP input stage is active and the input bias current is typically -40nA . When the input common mode voltage is within 0.5V of the positive rail, the NPN stage is operating and the input bias current is typically 80nA . Increases in temperature will cause the voltage at which operation switches from the PNP input stage to the NPN input stage to move towards V^+ . The input offset voltage of the NPN stage is untrimmed and is typically 1.8mV.

A Schottky diode in the collector of the input transistors, along with special geometries for these NPN transistors, allow the LT1783 to operate with either or both of its inputs above V^+ . At about 0.3V above V^+ , the NPN input transistor is fully saturated and the input bias current is typically $30\mu\text{A}$ at room temperature. The input offset voltage is typically 1.8mV when operating above V^+ . The LT1783 will operate with its inputs 18V above V^- regardless of V^+ .

The inputs are protected against excursions as much as 10V below V^- by an internal 1k resistor in series with each input and a diode from the input to the negative supply. The input stage of the LT1783 incorporates phase reversal protection to prevent the output from phase reversing for inputs up to 9V below V^- . There are no clamping diodes between the inputs and the maximum differential input voltage is 18V.

Output

The output of the LT1783 can swing to within 60mV of the positive rail with no load and within 3mV of the negative rail with no load. When monitoring input voltages within 60mV of the positive rail or within 3mV of the negative rail, gain should be taken to keep the output from clipping. The LT1783 can sink and source over 30mA at $\pm 5\text{V}$ supplies, sourcing current is reduced to 10mA at 3V total supplies as noted in the Electrical Characteristics.

The LT1783 is internally compensated to drive at least 400pF of capacitance under any output loading conditions. A $0.22\mu\text{F}$ capacitor in series with a 150Ω resistor between the output and ground will compensate these amplifiers for larger capacitive loads, up to 10,000pF, at all output currents.

Distortion

There are two main contributors to distortion in op amps: output crossover distortion as the output transitions from sourcing to sinking current, and distortion caused by nonlinear common mode rejection. If the op amp is operating inverting, there is no common mode induced distortion. If the op amp is operating in the PNP input stage (input is not within 0.8V of V^+), the CMRR is very good, typically 100dB. When the LT1783 switches between input stages, there is significant nonlinearity in the CMRR. Lower load resistance increases the output crossover distortion but has no effect on the input stage transition distortion. For lowest distortion, the LT1783 should be operated single supply, with the output always sourcing current and with the input voltage swing between ground and $(V^+ - 0.8\text{V})$. See the Typical Performance Characteristics curves, "Total Harmonic Distortion + Noise vs Output Voltage Amplitude."

APPLICATIONS INFORMATION

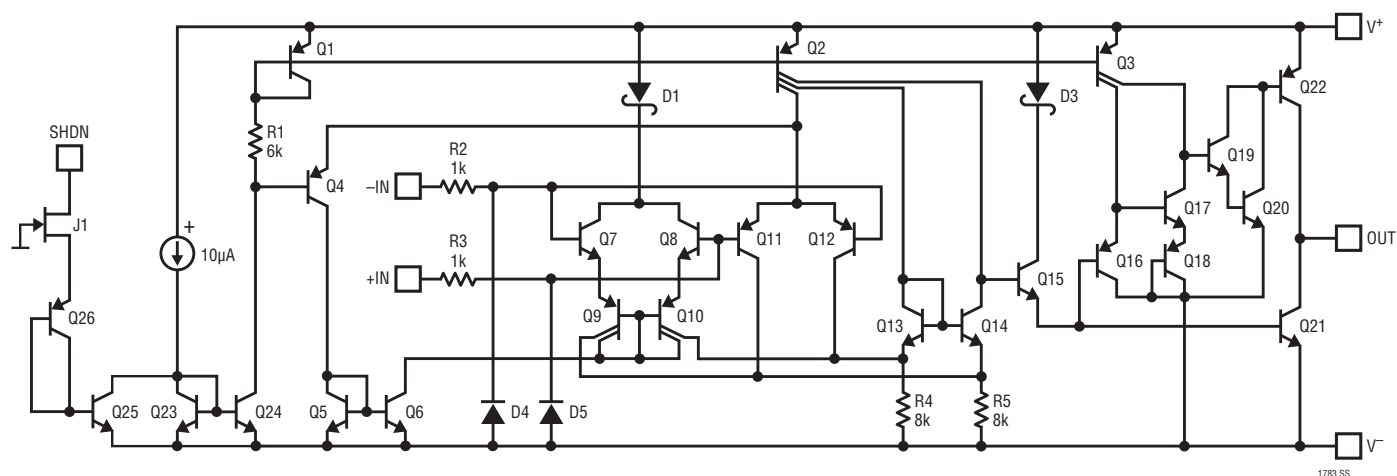
Gain

The open-loop gain is almost independent of load when the output is sourcing current. This optimizes performance in single supply applications where the load is returned to ground. The typical performance curve of open-loop gain for various loads shows the details.

Shutdown

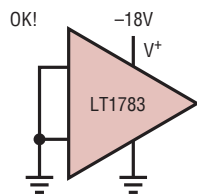
The 6-lead part includes a shutdown feature that disables the part, reducing quiescent current and making the output high impedance. The part can be shut down by bringing the SHDN pin 1.2V or more above V^- . When shut down, the supply current is about 5 μ A and the output leakage current is less than 1 μ A ($V^- \leq V_{OUT} \leq V^+$). In normal operation, the SHDN pin can be tied to V^- or left floating. See the Typical Performance Characteristics curves, "Supply Current vs Shutdown Voltage."

SIMPLIFIED SCHEMATIC

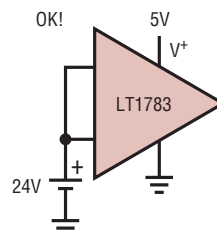


TYPICAL APPLICATION

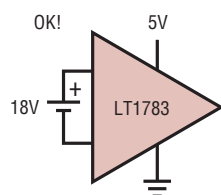
Protected Fault Conditions



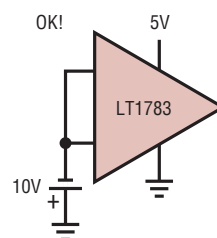
REVERSE BATTERY



INPUT OVERVOLTAGE



INPUT DIFFERENTIAL VOLTAGE

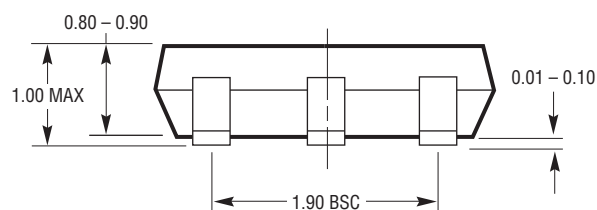
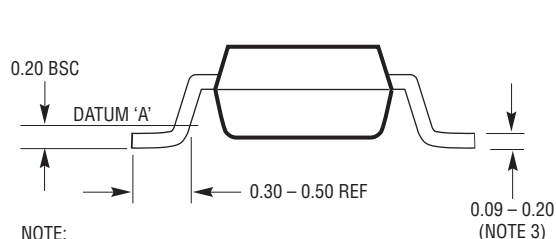
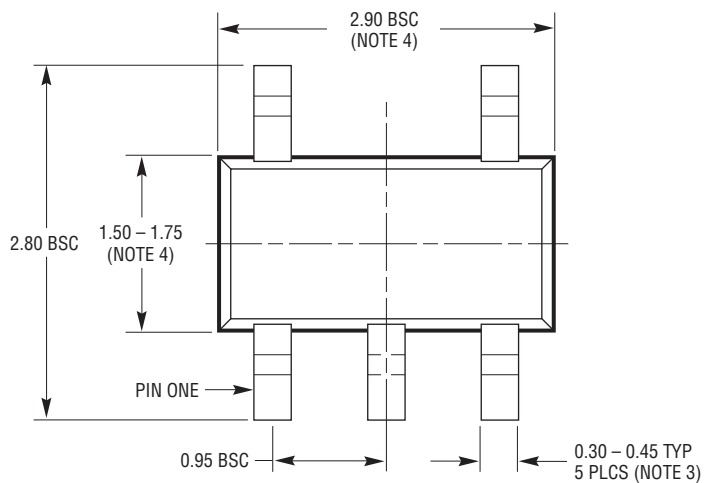
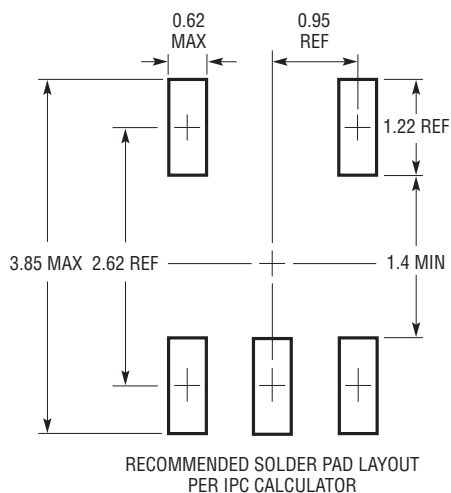


INPUTS BELOW GROUND

1783 TA02

PACKAGE DESCRIPTION

S5 Package 5-Lead Plastic TSOT-23 (Reference LTC DWG # 05-08-1635)



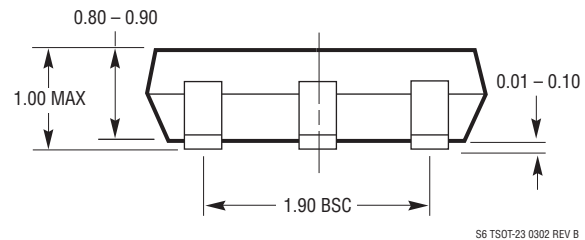
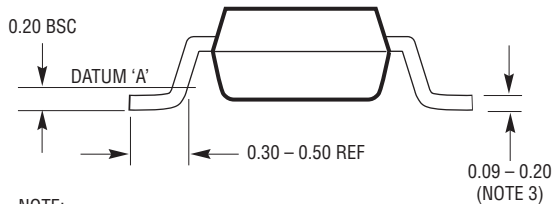
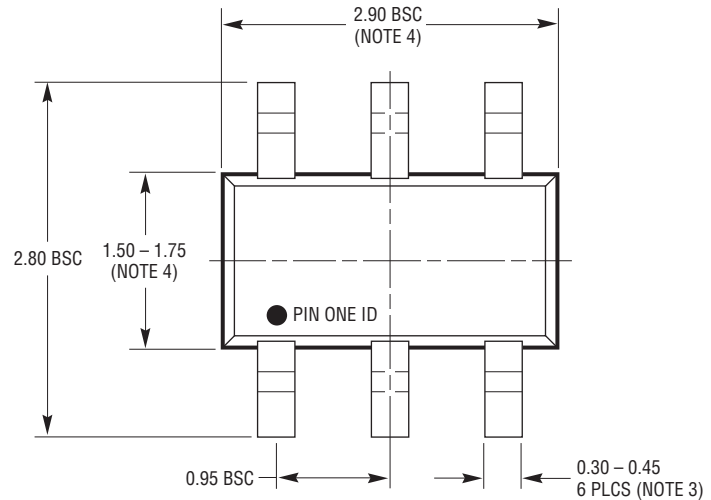
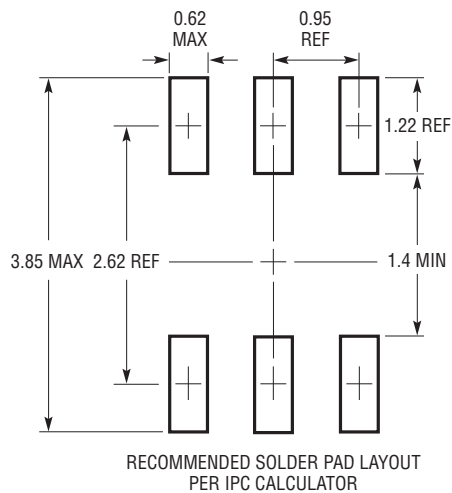
NOTE:

1. DIMENSIONS ARE IN MILLIMETERS
2. DRAWING NOT TO SCALE
3. DIMENSIONS ARE INCLUSIVE OF PLATING
4. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH AND METAL BURR
5. MOLD FLASH SHALL NOT EXCEED 0.254mm
6. JEDEC PACKAGE REFERENCE IS MO-193

S5 TSOT-23 0302 REV B

PACKAGE DESCRIPTION

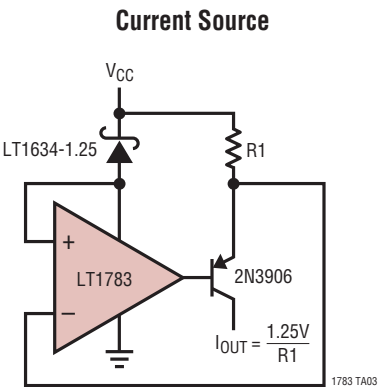
S6 Package 6-Lead Plastic TSOT-23 (Reference LTC DWG # 05-08-1636)



- NOTE:
1. DIMENSIONS ARE IN MILLIMETERS
 2. DRAWING NOT TO SCALE
 3. DIMENSIONS ARE INCLUSIVE OF PLATING
 4. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH AND METAL BURR
 5. MOLD FLASH SHALL NOT EXCEED 0.254mm
 6. JEDEC PACKAGE REFERENCE IS MO-193

S6 TSOT-23 0302 REV B

TYPICAL APPLICATION



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1782	Micropower, Over-The-Top SOT-23 Rail-to-Rail Input and Output Op Amp	SOT-23 Package, Micropower 40µA per Amplifier, Rail-to-Rail Input and Output, 200kHz GBW
LT1490/LT1491	Dual/Quad Over-The-Top Micropower Rail-to-Rail Input and Output Op Amps	Single Supply Input Range: -0.4V to 44V, Micropower 50µA per Amplifier, Rail-to-Rail Input and Output , 200kHz GBW
LT1636	Single Over-The-Top Micropower Rail-to-Rail Input and Output Op Amp	55µA Supply Current, V _{CM} Extends 44V Above V _{EE} , Independent of V _{CC} , MSOP Package, Shutdown Function
LT1638/LT1639	Dual/Quad, 1.2MHz, 0.4V/µs, Over-The-Top Micropower Rail-to-Rail Input and Output Op Amps	170µA Supply Current, Single Supply Input Range: -0.4V to 44V, Rail-to-Rail Input and Output