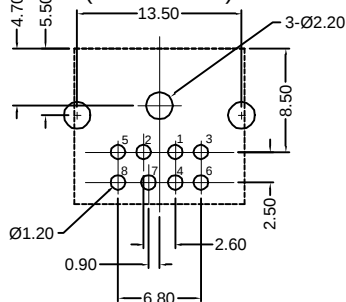


Recommended PCB Layout (Bottom View)

**Mechanical Characteristics**

Durability Test: 500 Cycles Min.

Insertion Force: 4.57kg Max

Extraction Force: 0.9kg - 3.0kg

Operating Temperature: -55°C to +85°C

Ferrite Performance:

Frequency	Impedance - Z (Ohms)
30 MHz	13-15 Ohms
50 MHz	15-18 Ohms
100 MHz	20-22 Ohms

Material and FinishBody: PBT Thermoplastic,
UL94V-0 Rated, Black

Contact Material: Phosphor Bronze

Contact Plating Options:

Standard: gold flash on contacts,
tin/lead on soldertails,
all over nickel underplate-30 Option: 30u" gold on contacts,
tin/lead on soldertails,
all over nickel underplate

Shield: Brass, Tin-Lead Plated

Electrical Characteristics

Contact Resistance:

Before Test:

Plug to Shield: 50 Milliohms Max.

Plug to Terminals: 30 Milliohms Max.

After Test:

Plug to Shield: 100 Milliohms Max.

Plug to Terminals: 60 Milliohms Max.

Insulation Resistance:

500 Megaohms Min. at 500 VDC

Dielectric Withstanding Voltage:

500V AC RMS For 1 Minute

Current Rating:

2 Amp at 12V DC

Part Number	Contact Area Plating Finish
KFMDG-xS-BS	Gold Flash (standard)
KFMDG-xS-BS30	30u" Gold (30 option)

3 Pos.	4 Pos.	5 Pos.	6 Pos.	7 Pos.	8 Pos.

Recommended PCB Layouts (Bottom View)

Tolerances

X.X ± 0.25
X.XX ± 0.15
Unless Stated
Otherwise

REVISION	DATE	DESCRIPTION	DRAWN BY	DESIGNED BY	UNITS	KFMDG-xS-BS x = No. of Contacts	Ferrite Filtered Mini-DIN	
B2	11/7/00	Ref ECD 00-182	C. Leiva		mm		Center Board Lock, Gold Flash Contacts	
B3	12/4/01	Ref ECD 01-228	REVISED BY	APPROVED BY	DATE			
B4	5/28/02	Ref ECD 02-017	M. Yiu		9/10/97			

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