

December 1986 Revised February 2000

DM7405

Hex Inverters with Open-Collector Outputs

General Description

This device contains six independent gates each of which performs the logic INVERT function. The open-collector outputs require external pull-up resistors for proper logical operation.

Pull-Up Resistor Equations

$$R_{MAX} = \frac{V_{CC} (Min) - V_{OH}}{N_1 (I_{OH}) + N_2 (I_{IH})}$$

$$R_{MIN} = \frac{V_{CC} \left(Max \right) - V_{OL}}{I_{OL} - N_3 \left(I_{IL} \right)}$$

Where: N_1 (I_{OH}) = total maximum output high current

for all outputs tied to pull-up resistor

N₂ (I_{IH}) = total maximum input high current for

all inputs tied to pull-up resistor

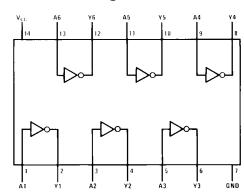
 N_3 (I_{IL}) = total maximum input low current for

all inputs tied to pull-up resistor

Ordering Code:

Order Number	Package Number	Package Description
DM7405N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Connection Diagram



Function Table

H = HIGH Logic Level L = LOW Logic Level

Absolute Maximum Ratings(Note 1)

 $\begin{array}{ccc} \text{Supply Voltage} & 7V \\ \text{Input Voltage} & 5.5V \\ \text{Output Voltage} & 7V \\ \text{Operating Free Air Temperature Range} & 0^{\circ}\text{C to } +70^{\circ}\text{C} \\ \end{array}$

Storage Temperature Range

O'C to +70°C

-65°C to +150°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
V _{OH}	HIGH Level Output Voltage			5.5	V
I _{OL}	LOW Level Output Current			16	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

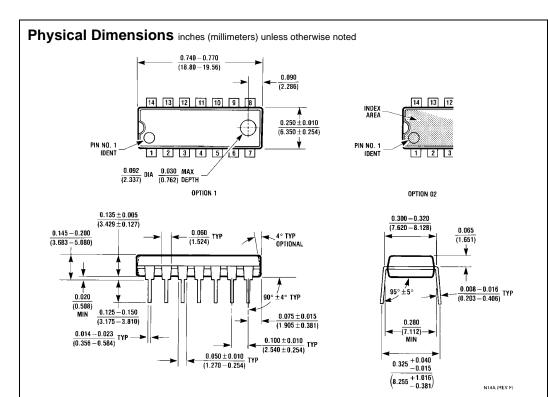
Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -12 \text{ mA}$			-1.5	V
I _{CEX}	HIGH Level	$V_{CC} = Min, V_O = 5.5V$			250	μА
	Output Current	V _{IL} = Max			230	μΛ
V _{OL}	LOW Level	V _{CC} = Min, I _{OL} = Max		0.2	0.4	٧
	Output Voltage	V _{IH} = Min				
I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$			1	mA
I _{IH}	HIGH Level Input Current	V _{CC} = Max, V _I = 2.4V			40	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-1.6	mA
I _{CCH}	Supply Current with Outputs HIGH	V _{CC} = Max		6	12	mA
I _{CCL}	Supply Current with Outputs LOW	V _{CC} = Max		18	33	mA

Note 2: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Switching Characteristics

at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$

Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}	Propagation Delay Time	C _L = 15 pF	55		ns
	LOW-to-HIGH Level Output	$R_L = 4 k\Omega (t_{PLH})$		33	115
t _{PHL}	Propagation Delay Time	$R_L = 400\Omega (t_{PHL})$		15	ns
	HIGH-to-LOW Level Output			15	115



14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N14A

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