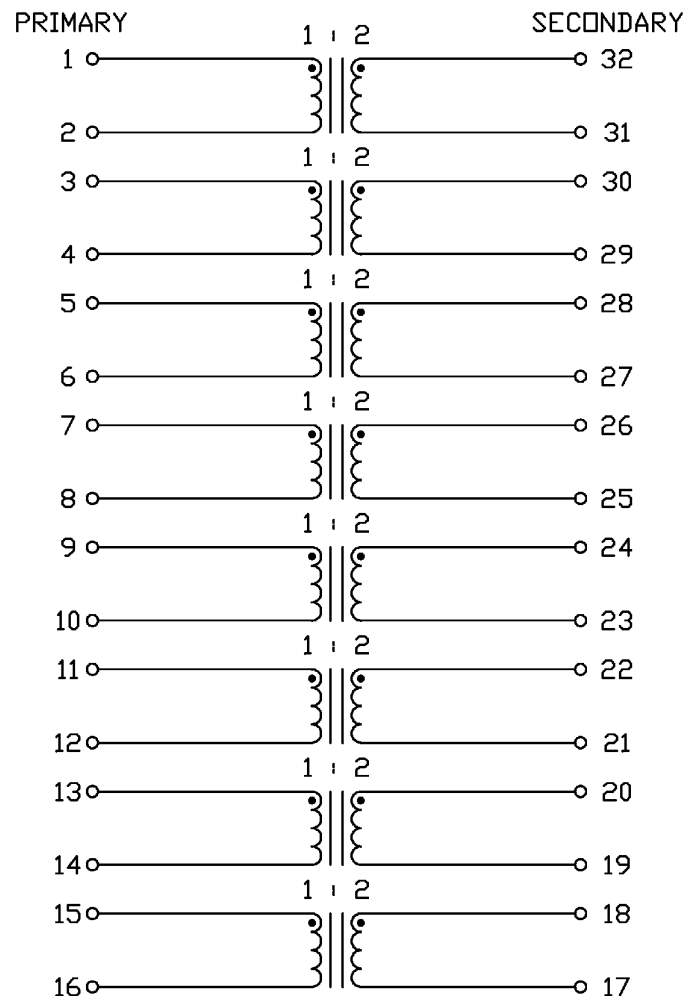


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SCHEMATIC

ELECTRICAL CHARACTERISTICS @25°C



URNS RATIO

PRIMARY : SECONDARY

1 : 2

PHASE

PER DOT CONVENTION

INDUCTANCE

PRIMARY

AT -40°C

1.5mH MIN. @10kHz, 20mV

640μH MIN.

LEAKAGE INDUCTANCE (Le)

PRIMARY (SECONDARY SHORTED)

0.30μH MAX. @1MHz, 20mV

INTERWINDING CAPACITANCE (Cw/W)

PRIMARY TO SECONDARY

35pF MAX. @1MHz, 20mV

DC RESISTANCE

PRIMARY

SECONDARY

0.60Ω MAX.

1.20Ω MAX.

HIPOT

PER HAND-WORK-03

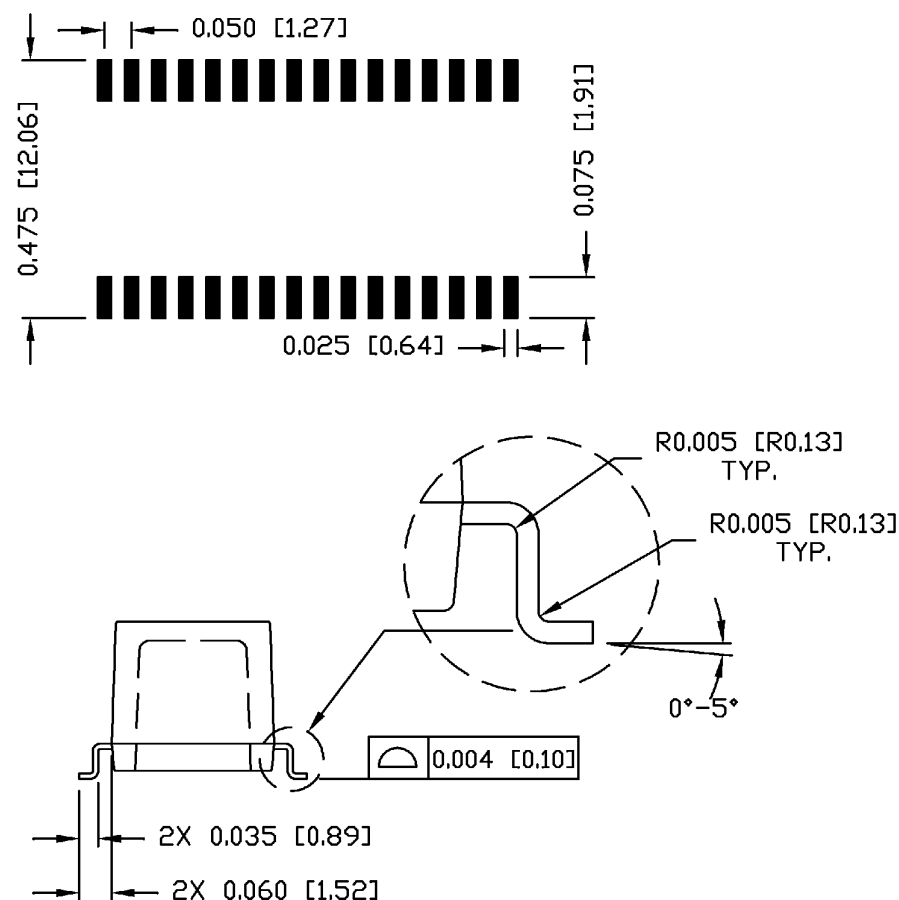
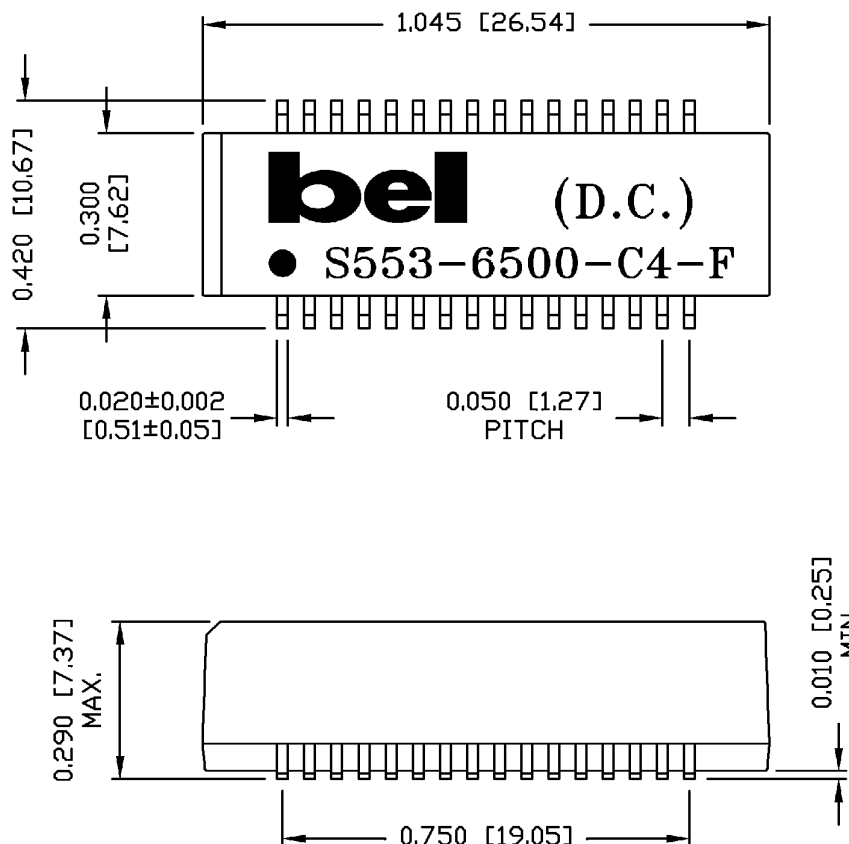
ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM. TOL. IN INCH	[] METRIC DIM. AS REFERENCE
CHAN CH	10-02-07	ELECTRICAL SPECIFICATION S553-6500-C4-F	X5536500C4-F	.X	UNIT : INCH [mm] REV. : A
DRAWN BY	DATE		FILE NAME	.XX	SCALE : N/A SIZE : A4
DQ Chen	10-02-07		X5536500C4FA.DWG	.XXX	PAGE : 2



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SUGGESTED PCB PAD LAYOUT



NOTES:

1. STANDARD MARKING REFER TO DDC, HAND-WORK-04.
2. PACKAGE CODE: "RDS001".

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.	[] METRIC DIM. AS REFERENCE	
Lawrence Tsang	10-02-07	MECHANICAL OUTLINE S553-6500-C4-F	X5536500C4-F	TOL. IN INCH	UNIT : INCH [mm]	REV. : A
DRAWN BY	DATE		FILE NAME	.X	SCALE : N/A	SIZE : A4
DQ Chen	10-02-07		X5536500C4FA.DWG	.XX ±0.01		PAGE : 3
				.XXX ±0.005		

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