

# 3-STATE Octal D-Type Latch / 3-STATE Octal D-Type Flip-Flop

## MM74HCT373/MM74HCT374

### General Description

The MM74HCT373 octal D-type latches and MM74HCT374 Octal D-type flip flops advanced silicon-gate CMOS technology, which provides the inherent benefits of low power consumption and wide power supply range, but are LS-TTL input and output characteristic & pin-out compatible. The 3-STATE outputs are capable of driving 15 LSTTL loads. All inputs are protected from damage due to static discharge by internal diodes to VCC and ground.

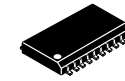
When the MM74HCT373 LATCH ENABLE input is HIGH, the Q outputs will follow the D inputs. When the LATCH ENABLE goes LOW, data at the D inputs will be retained at the outputs until LATCH ENABLE returns HIGH again. When a high logic level is applied to the OUTPUT CONTROL input, all outputs go to a high impedance state, regardless of what signals are present at the other inputs and the state of the storage elements.

The MM74HCT374 are positive edge triggered flip-flops. Data at the D inputs, meeting the setup and hold time requirements, are transferred to the Q outputs on positive going transitions of the CLOCK (CK) input. When a high logic level is applied to the OUTPUT CONTROL (OC) input, all outputs go to a high impedance state, regardless of what signals are present at the other inputs and the state of the storage elements.

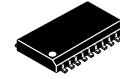
MM74HCT devices are intended to interface between TTL and NMOS components and standard CMOS devices. These parts are also plug in replacements for LS-TTL devices and can be used to reduce power consumption in existing designs.

### Features

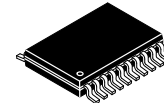
- TTL Input Characteristic Compatible
- Typical Propagation Delay: 20 ns
- Low Input Current: 1  $\mu$ A Maximum
- Low Quiescent Current: 160  $\mu$ A Maximum
- Compatible with Bus-oriented Systems
- Output Drive Capability: 15 LS-TTL Loads
- These are Pb-Free Devices



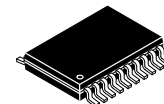
SOIC-20 WB  
CASE 751D-05



SOIC-20, 300 mills  
CASE 751BJ-01

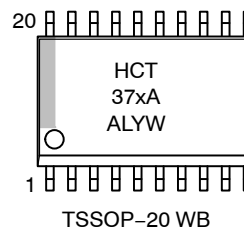
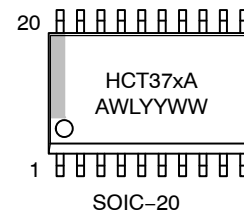


TSSOP20, 4.4x6.5  
CASE 948AQ-01



TSSOP-20 WB  
CASE 948E

### MARKING DIAGRAMS



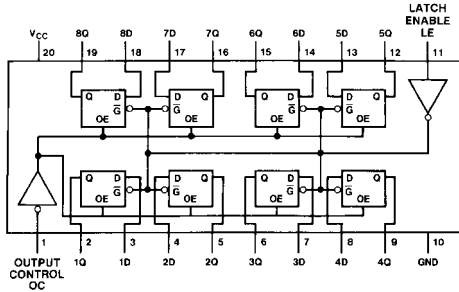
HCT37xA = Specific Device Code  
 x = 3 or 4  
 A = Assembly Location  
 WL, L = Wafer Lot  
 YY, Y = Year  
 WW, W = Work Week

### ORDERING INFORMATION

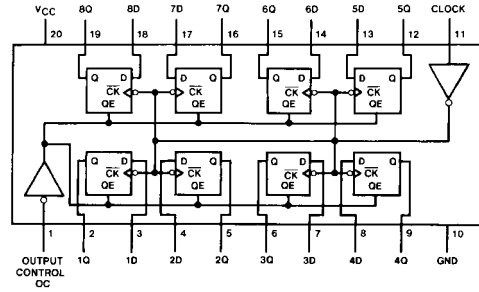
See detailed ordering and shipping information on page 7 of this data sheet.

# MM74HCT373/MM74HCT374

## Connection Diagrams



MM74HCT373  
(Top View)



MM74HCT374  
(Top View)

Figure 1. Pin Assignments for SOIC and TSSOP

## Truth Tables

MM74HCT373

| Output Control | LE | Data | 373 Output     |
|----------------|----|------|----------------|
| L              | H  | H    | H              |
| L              | H  | L    | L              |
| L              | L  | X    | Q <sub>0</sub> |
| H              | X  | X    | Z              |

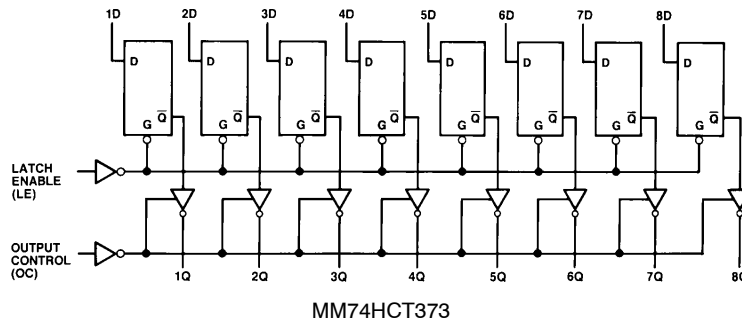
NOTES: H = HIGH Level  
L = LOW Level  
Q<sub>0</sub> = Level of output before steady-state input conditions were established.  
Z = High Impedance

MM74HCT374

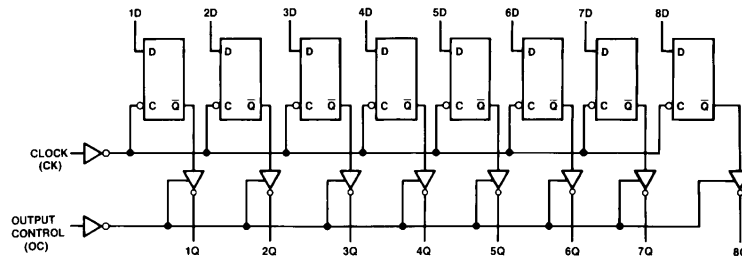
| Output Control | Clock | Data | 374 Output     |
|----------------|-------|------|----------------|
| L              | ↑     | H    | H              |
| L              | ↑     | L    | L              |
| L              | L     | X    | Q <sub>0</sub> |
| H              | X     | X    | Z              |

NOTES: H = HIGH Level  
L = LOW Level  
X = Don't Care  
↑ = Transition from LOW-to-HIGH  
Z = High Impedance State  
Q<sub>0</sub> = The level of the output before steady state input conditions were established.

## Logic Diagrams



MM74HCT373



MM74HCT374

Figure 2. Logic Diagrams

# MM74HCT373/MM74HCT374

## ABSOLUTE MAXIMUM RATINGS (Note 1)

| Symbol           | Parameter                               |                   | Rating                   |
|------------------|-----------------------------------------|-------------------|--------------------------|
| $V_{CC}$         | Supply Voltage                          |                   | –0.5 to +7.0 V           |
| $V_{IN}$         | DC Input Voltage                        |                   | –0.5 to $V_{CC} + 0.5$ V |
| $V_{OUT}$        | DC Output Voltage                       |                   | –0.5 to $V_{CC} + 0.5$ V |
| $I_{IK}, I_{OK}$ | Clamp Diode Current                     |                   | ±20 mA                   |
| $I_{OUT}$        | DC Output Current, per Pin              |                   | ±35 mA                   |
| $I_{CC}$         | DC $V_{CC}$ or GND Current, per Pin     |                   | ±70 mA                   |
| $T_{STG}$        | Storage Temperature Range               |                   | –65°C to +150°C          |
| $P_D$            | Power Dissipation                       | S.O. Package only | 500 mW                   |
| $T_L$            | Lead Temperature (Soldering 10 Seconds) |                   | 260°C                    |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Unless otherwise specified all voltages are referenced to ground.

## RECOMMENDED OPERATING CONDITIONS

| Symbol            | Parameter                   | Min | Max      | Unit |
|-------------------|-----------------------------|-----|----------|------|
| $V_{CC}$          | Supply Voltage              | 4.5 | 5.5      | V    |
| $V_{IN}, V_{OUT}$ | DC Input or Output Voltage  | 0   | $V_{CC}$ | V    |
| $T_A$             | Operating Temperature Range | –55 | +125     | °C   |
| $t_r, t_f$        | Input Rise or Fall Times    |     | 500      | ns   |

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

# MM74HCT373/MM74HCT374

## DC ELECTRICAL CHARACTERISTICS ( $V_{CC} = 5\text{ V} \pm 10\%$ , unless otherwise specified)

| Symbol          | Parameter                                 | Conditions                                                                                                    | T <sub>A</sub> = 25°C |                       | T <sub>A</sub> = −40°C<br>to 85°C | T <sub>A</sub> = −55°C<br>to 125°C | Unit |
|-----------------|-------------------------------------------|---------------------------------------------------------------------------------------------------------------|-----------------------|-----------------------|-----------------------------------|------------------------------------|------|
|                 |                                           |                                                                                                               | Typ                   | Guaranteed Limits     |                                   |                                    |      |
| V <sub>IH</sub> | Minimum HIGH Level Input Voltage          |                                                                                                               | –                     | 2.0                   | 2.0                               | 2.0                                | V    |
| V <sub>IL</sub> | Maximum LOW Level Input Voltage           |                                                                                                               | –                     | 0.8                   | 0.8                               | 0.8                                | V    |
| V <sub>OH</sub> | Minimum HIGH Level Output Voltage         | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br> I <sub>OUT</sub>   = 20 μA                           | V <sub>CC</sub>       | V <sub>CC</sub> – 0.1 | V <sub>CC</sub> – 0.1             | V <sub>CC</sub> – 0.1              | V    |
|                 |                                           | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br> I <sub>OUT</sub>   = 6.0 mA, V <sub>CC</sub> = 4.5 V | 4.2                   | 3.98                  | 3.84                              | 3.7                                | V    |
|                 |                                           | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br> I <sub>OUT</sub>   = 7.2 mA, V <sub>CC</sub> = 5.5 V | 5.7                   | 4.98                  | 4.84                              | 4.7                                | V    |
| V <sub>OL</sub> | Maximum LOW Level Voltage                 | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br> I <sub>OUT</sub>   = 20 μA                           | 0                     | 0.1                   | 0.1                               | 0.1                                | V    |
|                 |                                           | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br> I <sub>OUT</sub>   = 6.0 mA, V <sub>CC</sub> = 4.5 V | 0.2                   | 0.26                  | 0.33                              | 0.4                                | V    |
|                 |                                           | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br> I <sub>OUT</sub>   = 7.2 mA, V <sub>CC</sub> = 5.5 V | 0.2                   | 0.26                  | 0.33                              | 0.4                                | V    |
| I <sub>IN</sub> | Maximum Input Current                     | V <sub>IN</sub> = V <sub>CC</sub> or GND,<br>V <sub>IH</sub> or V <sub>IL</sub>                               | –                     | ±0.1                  | ±1.0                              | ±1.0                               | μA   |
| I <sub>OZ</sub> | Maximum 3–STATE<br>Output Leakage Current | V <sub>OUT</sub> = V <sub>CC</sub> or GND,<br>Enable = V <sub>IH</sub> or V <sub>IL</sub>                     | –                     | ±0.5                  | ±5.0                              | ±10                                | μA   |
| I <sub>CC</sub> | Maximum Quiescent Supply<br>Current       | V <sub>IN</sub> = V <sub>CC</sub> or GND,<br>I <sub>OUT</sub> = 0 μA                                          | –                     | 8.0                   | 80                                | 160                                | μA   |
|                 |                                           | V <sub>IN</sub> = 2.4 V or 0.5 V<br>(Note 2)                                                                  | –                     | 1.0                   | 1.3                               | 1.5                                | mA   |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Measured per pin. All others tied to  $V_{CC}$  or ground.

# MM74HCT373/MM74HCT374

## AC ELECTRICAL CHARACTERISTICS

(MM74HCT373:  $V_{CC} = 5.0\text{ V}$ ,  $T_A = 25^\circ\text{C}$ ,  $t_r = t_f = 6\text{ ns}$ , unless otherwise specified)

| Symbol                | Parameter                                           | Conditions                                       | Typ | Guaranteed Limit | Unit |
|-----------------------|-----------------------------------------------------|--------------------------------------------------|-----|------------------|------|
| $t_{PHL}$ , $t_{PLH}$ | Maximum Propagation Delay Data to Output            | $C_L = 45\text{ pF}$                             | 18  | 25               | ns   |
| $t_{PHL}$ , $t_{PLH}$ | Maximum Propagation Delay Latch Enable to Output    | $C_L = 45\text{ pF}$                             | 21  | 30               | ns   |
| $t_{PZH}$ , $t_{PZL}$ | Maximum Enable Propagation Delay Control to Output  | $C_L = 45\text{ pF}$<br>$R_L = 1\text{ k}\Omega$ | 20  | 28               | ns   |
| $t_{PHZ}$ , $t_{PLZ}$ | Maximum Disable Propagation Delay Control to Output | $C_L = 5\text{ pF}$<br>$R_L = 1\text{ k}\Omega$  | 18  | 25               | ns   |
| $t_W$                 | Minimum Clock Pulse Width                           |                                                  | –   | 16               | ns   |
| $t_S$                 | Minimum Setup Time Data to Clock                    |                                                  | –   | 5                | ns   |
| $t_H$                 | Minimum Hold Time Clock to Data                     |                                                  | –   | 10               | ns   |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## AC ELECTRICAL CHARACTERISTICS

(MM74HCT373:  $V_{CC} = 5.0\text{ V} \pm 10\%$ ,  $t_r = t_f = 6\text{ ns}$ , unless otherwise specified)

| Symbol                              | Parameter                                              | Conditions                                       | T <sub>A</sub> = 25°C |                   | T <sub>A</sub> = −40°C<br>to 85°C | T <sub>A</sub> = −55°C<br>to 125°C | Unit |
|-------------------------------------|--------------------------------------------------------|--------------------------------------------------|-----------------------|-------------------|-----------------------------------|------------------------------------|------|
|                                     |                                                        |                                                  | Typ                   | Guaranteed Limits |                                   |                                    |      |
| t <sub>PHL</sub> , t <sub>PLH</sub> | Maximum Propagation<br>Delay Data to Output            | C <sub>L</sub> = 50 pF                           | 22                    | 30                | 37                                | 45                                 | ns   |
|                                     |                                                        | C <sub>L</sub> = 150 pF                          | 30                    | 40                | 50                                | 60                                 | ns   |
| t <sub>PHL</sub> , t <sub>PLH</sub> | Maximum Propagation<br>Delay Latch Enable to Output    | C <sub>L</sub> = 50 pF                           | 25                    | 35                | 44                                | 53                                 | ns   |
|                                     |                                                        | C <sub>L</sub> = 150 pF                          | 32                    | 45                | 56                                | 68                                 | ns   |
| t <sub>PZH</sub> , t <sub>PZL</sub> | Maximum Enable Propagation Delay<br>Control to Output  | C <sub>L</sub> = 50 pF<br>R <sub>L</sub> = 1 kΩ  | 21                    | 30                | 37                                | 45                                 | ns   |
|                                     |                                                        | C <sub>L</sub> = 150 pF<br>R <sub>L</sub> = 1 kΩ | 30                    | 40                | 50                                | 60                                 | ns   |
| t <sub>PHZ</sub> , t <sub>PLZ</sub> | Maximum Disable Propagation Delay<br>Control to Output | C <sub>L</sub> = 50 pF<br>R <sub>L</sub> = 1 kΩ  | 21                    | 30                | 37                                | 45                                 | ns   |
| t <sub>THL</sub> , t <sub>TLH</sub> | Maximum Output Rise and Fall Time                      | C <sub>L</sub> = 50 pF                           | 8                     | 12                | 15                                | 18                                 | ns   |
| t <sub>W</sub>                      | Minimum Clock Pulse Width                              |                                                  | –                     | 16                | 20                                | 24                                 | ns   |
| t <sub>S</sub>                      | Minimum Setup Time Data to Clock                       |                                                  | –                     | 5                 | 6                                 | 8                                  | ns   |
| t <sub>H</sub>                      | Minimum Hold Time Clock to Data                        |                                                  | –                     | 10                | 13                                | 20                                 | ns   |
| C <sub>IN</sub>                     | Maximum Input Capacitance                              |                                                  | –                     | 10                | 10                                | 10                                 | pF   |
| C <sub>OUT</sub>                    | Maximum Output Capacitance                             |                                                  | –                     | 20                | 20                                | 20                                 | pF   |
| C <sub>PD</sub>                     | Power Dissipation Capacitance (Note 3)                 | OC = V <sub>CC</sub>                             | –                     | 5                 | –                                 | –                                  | pF   |
|                                     |                                                        | OC = GND                                         | –                     | 52                | –                                 | –                                  | pF   |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3.  $C_{PD}$  determines the no load dynamic power consumption,  $P_D = C_{PD} V_{CC}^2 f + I_{CC} V_{CC}$ , and the no load dynamic current consumption,  $I_S = C_{PD} V_{CC}^2 f + I_{CC}$ .

# MM74HCT373/MM74HCT374

## AC ELECTRICAL CHARACTERISTICS

(MM74HCT374:  $V_{CC} = 5.0\text{ V}$ ,  $T_A = 25^\circ\text{C}$ ,  $t_r = t_f = 6\text{ ns}$ , unless otherwise specified)

| Symbol                | Parameter                                           | Conditions                                       | Typ | Guaranteed Limit | Unit |
|-----------------------|-----------------------------------------------------|--------------------------------------------------|-----|------------------|------|
| $f_{MAX}$             | Maximum Clock Frequency                             |                                                  | 50  | 30               | MHz  |
| $t_{PHL}$ , $t_{PLH}$ | Maximum Propagation Delay Data to Output            | $C_L = 45\text{ pF}$                             | 20  | 32               | ns   |
| $t_{PZH}$ , $t_{PZL}$ | Maximum Enable Propagation Delay Control to Output  | $C_L = 45\text{ pF}$<br>$R_L = 1\text{ k}\Omega$ | 19  | 28               | ns   |
| $t_{PHZ}$ , $t_{PLZ}$ | Maximum Disable Propagation Delay Control to Output | $C_L = 5\text{ pF}$<br>$R_L = 1\text{ k}\Omega$  | 17  | 25               | ns   |
| $t_W$                 | Minimum Clock Pulse Width                           |                                                  | –   | 20               | ns   |
| $t_S$                 | Minimum Setup Time Data to Clock                    |                                                  | –   | 5                | ns   |
| $t_H$                 | Minimum Hold Time Clock to Data                     |                                                  | –   | 16               | ns   |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## AC ELECTRICAL CHARACTERISTICS

(MM74HCT374:  $V_{CC} = 5.0\text{ V} \pm 10\%$ ,  $t_r = t_f = 6\text{ ns}$ , unless otherwise specified)

| Symbol                              | Parameter                                           | Conditions                                       | T <sub>A</sub> = 25°C |                   | T <sub>A</sub> = −40°C to 85°C | T <sub>A</sub> = −55°C to 125°C | Unit |
|-------------------------------------|-----------------------------------------------------|--------------------------------------------------|-----------------------|-------------------|--------------------------------|---------------------------------|------|
|                                     |                                                     |                                                  | Typ                   | Guaranteed Limits |                                |                                 |      |
| f <sub>MAX</sub>                    | Minimum Clock Pulse Width                           |                                                  | –                     | 30                | 24                             | 20                              | MHz  |
| t <sub>PHL</sub> , t <sub>PLH</sub> | Maximum Propagation Delay to Output                 | C <sub>L</sub> = 50 pF                           | 22                    | 36                | 45                             | 48                              | ns   |
|                                     |                                                     | C <sub>L</sub> = 150 pF                          | 30                    | 46                | 57                             | 69                              | ns   |
| t <sub>PZH</sub> , t <sub>PZL</sub> | Maximum Enable Propagation Delay Control to Output  | C <sub>L</sub> = 50 pF<br>R <sub>L</sub> = 1 kΩ  | 21                    | 30                | 37                             | 45                              | ns   |
|                                     |                                                     | C <sub>L</sub> = 150 pF<br>R <sub>L</sub> = 1 kΩ | 30                    | 40                | 50                             | 60                              | ns   |
| t <sub>PHZ</sub> , t <sub>PLZ</sub> | Maximum Disable Propagation Delay Control to Output | C <sub>L</sub> = 50 pF<br>R <sub>L</sub> = 1 kΩ  | 21                    | 30                | 37                             | 45                              | ns   |
| t <sub>THL</sub> , t <sub>TLH</sub> | Maximum Output Rise and Fall Time                   | C <sub>L</sub> = 50 pF                           | 8                     | 12                | 15                             | 18                              | ns   |
| t <sub>W</sub>                      | Minimum Clock Pulse Width                           |                                                  | –                     | 16                | 20                             | 24                              | ns   |
| t <sub>S</sub>                      | Minimum Setup Time Data to Clock                    |                                                  | –                     | 20                | 25                             | 30                              | ns   |
| t <sub>H</sub>                      | Minimum Hold Time Clock to Data                     |                                                  | –                     | 5                 | 5                              | 5                               | ns   |
| C <sub>IN</sub>                     | Maximum Input Capacitance                           |                                                  | –                     | 10                | 10                             | 10                              | pF   |
| C <sub>OUT</sub>                    | Maximum Output Capacitance                          |                                                  | –                     | 20                | 20                             | 20                              | pF   |
| C <sub>PD</sub>                     | Power Dissipation Capacitance (Note 4)              | OC = V <sub>CC</sub>                             | –                     | 5                 | –                              | –                               | pF   |
|                                     |                                                     | OC = GND                                         | –                     | 58                | –                              | –                               | pF   |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4.  $C_{PD}$  determines the no load dynamic power consumption,  $P_D = C_{PD} V_{CC}^2 f + I_{CC} V_{CC}$ , and the no load dynamic current consumption,  $I_S = C_{PD} V_{CC}^2 f + I_{CC}$ .

## MM74HCT373/MM74HCT374

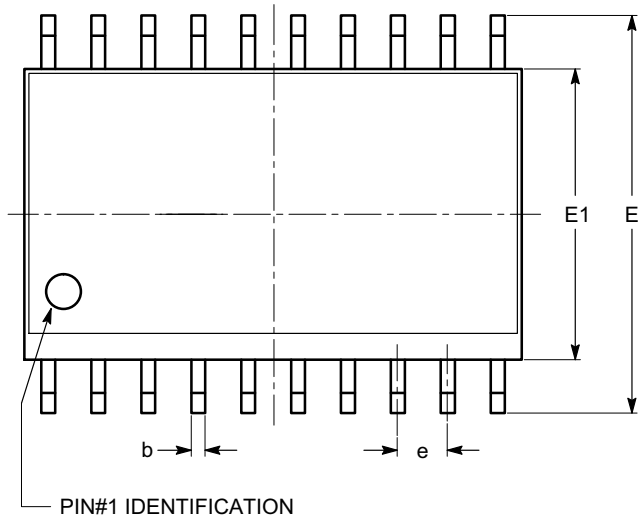
### ORDERING INFORMATION

| Part Number    | Package                                               | Shipping <sup>†</sup>    |
|----------------|-------------------------------------------------------|--------------------------|
| MM74HCT373WMX  | SOIC–20, Case 751BJ<br>(Pb–Free and Halide–Free)      | 1000 Units / Tape & Reel |
| MM74HCT373MTCX | TSSOP–20 WB, Case 948E<br>(Pb–Free)                   | 2500 Units / Tape & Reel |
| MM74HCT374WM   | SOIC–20 WB, Case 751D–05<br>(Pb–Free and Halide–Free) | 38 Units / Tube          |
| MM74HCT374WMX  |                                                       | 1000 Units / Tape & Reel |
| MM74HCT374MTCX | TSSOP20, Case 948AQ–01<br>(Pb–Free)                   | 2500 Units / Tape & Reel |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, [BRD8011/D](#).

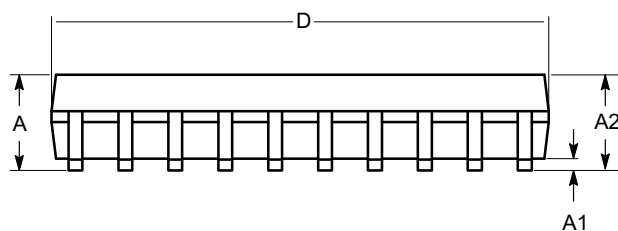
**SOIC-20, 300 mils**  
**CASE 751BJ**  
**ISSUE O**

DATE 19 DEC 2008

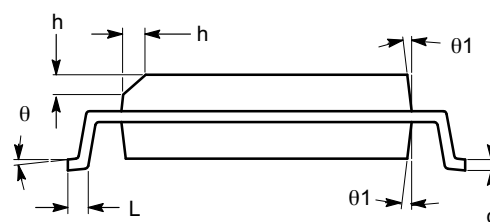


**TOP VIEW**

| SYMBOL     | MIN      | NOM   | MAX   |
|------------|----------|-------|-------|
| A          | 2.36     | 2.49  | 2.64  |
| A1         | 0.10     |       | 0.30  |
| A2         | 2.05     |       | 2.55  |
| b          | 0.31     | 0.41  | 0.51  |
| c          | 0.20     | 0.27  | 0.33  |
| D          | 12.60    | 12.80 | 13.00 |
| E          | 10.01    | 10.30 | 10.64 |
| E1         | 7.40     | 7.50  | 7.60  |
| e          | 1.27 BSC |       |       |
| h          | 0.25     |       | 0.75  |
| L          | 0.40     | 0.81  | 1.27  |
| $\theta$   | 0°       |       | 8°    |
| $\theta 1$ | 5°       |       | 15°   |



**SIDE VIEW**



**END VIEW**

**Notes:**

- (1) All dimensions are in millimeters. Angles in degrees.
- (2) Complies with JEDEC MS-013.

|                         |                          |                                                                                                                                                                                     |
|-------------------------|--------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
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| <b>DESCRIPTION:</b>     | <b>SOIC-20, 300 MILS</b> | <b>PAGE 1 OF 1</b>                                                                                                                                                                  |

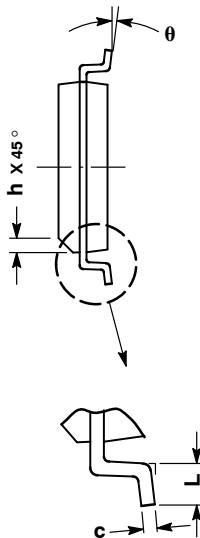
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SCALE 1:1

SOIC-20 WB  
CASE 751D-05  
ISSUE H

DATE 22 APR 2015

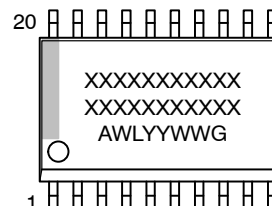


NOTES:

1. DIMENSIONS ARE IN MILLIMETERS.
2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.
3. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
5. DIMENSION B DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF B DIMENSION AT MAXIMUM MATERIAL CONDITION.

| DIM | MILLIMETERS |       |
|-----|-------------|-------|
|     | MIN         | MAX   |
| A   | 2.35        | 2.65  |
| A1  | 0.10        | 0.25  |
| b   | 0.35        | 0.49  |
| c   | 0.23        | 0.32  |
| D   | 12.65       | 12.95 |
| E   | 7.40        | 7.60  |
| e   | 1.27 BSC    |       |
| H   | 10.05       | 10.55 |
| h   | 0.25        | 0.75  |
| L   | 0.50        | 0.90  |
| θ   | 0°          | 7°    |

GENERIC  
MARKING DIAGRAM\*



XXXXXX = Specific Device Code  
A = Assembly Location  
WL = Wafer Lot  
YY = Year  
WW = Work Week  
G = Pb-Free Package

RECOMMENDED  
SOLDERING FOOTPRINT\*



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

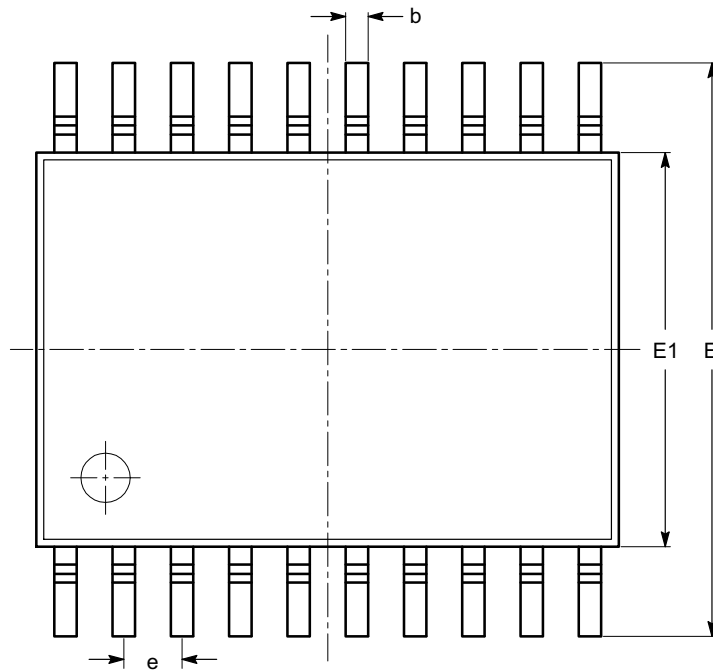
\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

|                  |             |                                                                                                                                                                                     |
|------------------|-------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
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| DESCRIPTION:     | SOIC-20 WB  | PAGE 1 OF 1                                                                                                                                                                         |

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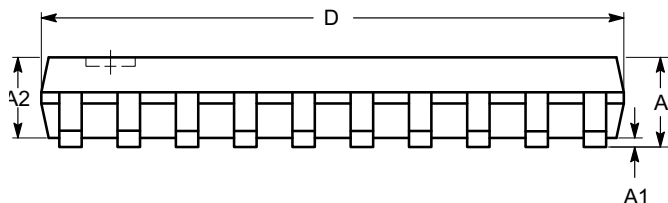
**TSSOP20, 4.4x6.5**  
CASE 948AQ  
ISSUE A

DATE 19 MAR 2009

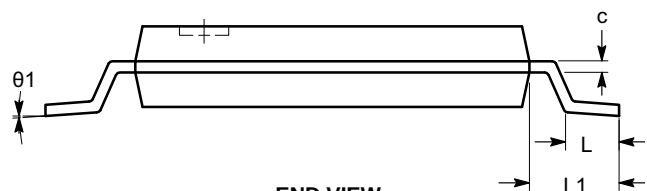


TOP VIEW

| SYMBOL | MIN      | NOM  | MAX  |
|--------|----------|------|------|
| A      |          |      | 1.20 |
| A1     | 0.05     |      | 0.15 |
| A2     | 0.80     |      | 1.05 |
| b      | 0.19     |      | 0.30 |
| c      | 0.09     |      | 0.20 |
| D      | 6.40     | 6.50 | 6.60 |
| E      | 6.30     | 6.40 | 6.50 |
| E1     | 4.30     | 4.40 | 4.50 |
| e      | 0.65 BSC |      |      |
| L      | 0.45     | 0.60 | 0.75 |
| L1     | 1.00 REF |      |      |
| θ      | 0°       |      | 8°   |



SIDE VIEW



END VIEW

**Notes:**

- (1) All dimensions are in millimeters. Angles in degrees.
- (2) Complies with JEDEC MO-153.

|                         |                         |                                                                                                                                                                                     |
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