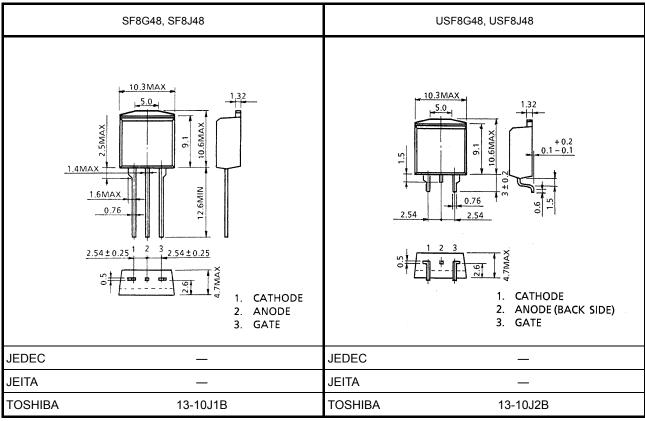
TOSHIBA Thyristor Silicon Planar Type

SF8G48,SF8J48,USF8G48,USF8J48

Medium Power Control Applications

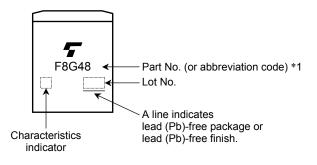
- Repetitive peak off-state voltage: VDRM = 400 V, 600 V
 Repetitive peak reverse voltage: VRRM = 400 V, 600 V
- Average on-state current: $I_{T}(AV) = 8 A$
- Gate trigger current: IGT = 10 mA max.

Unit: mm



Weight: 1.5 g Weight: 1.4 g

Marking



*1	Part No. (or abbreviation code)	Part No.
	F8G48	SF8G48, USF8G48
	F8J48	SF8J48, USF8J48

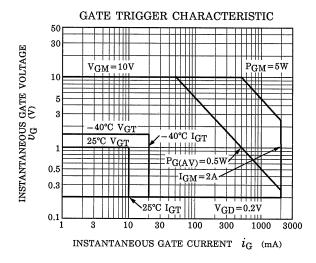


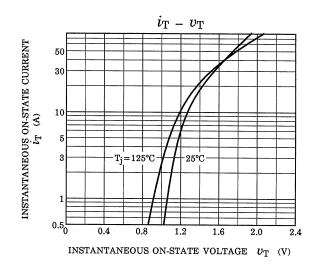
Maximum Ratings (Ta = 25°C)

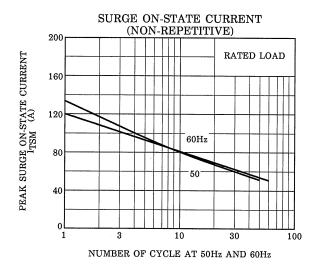
Characteris	Symbol	Rating	Unit		
	SF8G48		400		
Repetitive peakoff-state voltage	USF8G48	V _{DRM} V _{RRM}	400	V	
and repetitive peakreverse voltage	SF8J48		600	V	
p commercial temperature	USF8J48		000		
	SF8G48		500		
Non-repetitive peak reverse voltage	USF8G48	\/=	500	V	
(non-repetitive < 5 ms T _i = 0~125°C	SF8J48	V_{RSM}	720	V	
., 6 126 6	USF8J48		720		
Average on-state current	t	I _{T (AV)}	8	Α	
rms on-state current		I _{T (RMS)}	12.6	Α	
Peak one-cycle surge or	ı-state	l=a	120 (50 Hz)	z) A	
current (non-repetitive)		ITSM	132 (60 Hz)		
I ² t limit value	I ² t	72	A ² s		
Critical rate of rise of on-state current	di /dt	100	A/μs		
Peak gate power dissipa	tion	P_{GM}	5	W	
Average gate power diss	sipation	P _G (AV)	0.5	W	
Peak forward gate voltage	je	V_{FGM}	10	V	
Peak reverse gate voltage	je	V_{RGM}	-5	V	
Peak forward gate curre	nt	I _{GM}	2	Α	
Junction temperature		Tj	-40~125	°C	
Strage temperature rang	е	T _{stg}	-40~125	°C	

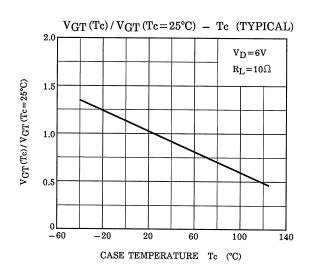
Electrical Charcteristics (Ta = 25°C)

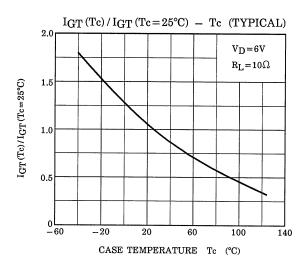
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Repetitive peak off-state current and repetitive peak reverse	I _{DRM} I _{RRM}	V _{DRM} = V _{RRM} = Rated	_	_	10	μА
Peak on-state voltage	V _{TM}	I _{TM} = 25 A	_	_	1.5	٧
Gate trigger voltage	V _{GT}	V 6 V D: - 10 O	_	_	1.0	V
Gate trigger current	I _{GT}	$V_D = 6 \text{ V}, R_L = 10 \Omega$	_	_	10	mA
Gate non-trigger voltage	V _{GD}	V _D = Rated × 2 / 3, Tc = 125°C	0.2	_	_	V
Critical rate of rise of off-state voltage	dv / dt	V _{DRM} = Rated, Tc = 125°C Exponential Rise	_	50	_	V / μs
Holding current	lΗ	V _D = 6 V, I _{TM} = 1 A	_	_	40	mA
Latching current	ΙL	V _D = 6 V, f = 50 Hz t _{gw} = 50 µs, i _G = 30 mA	_	_	50	mA
Thermal resistance	R _{th (j-c)}	Junction to Case, DC	_	_	2.8	°C / W

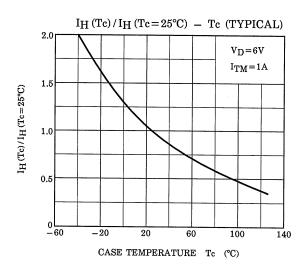


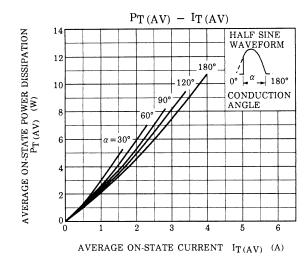


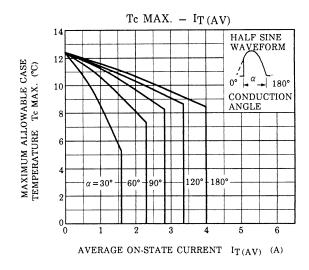


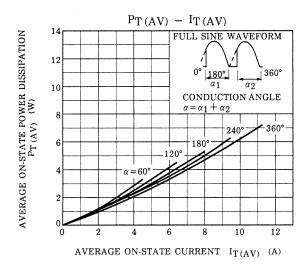


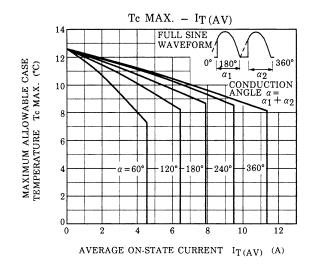


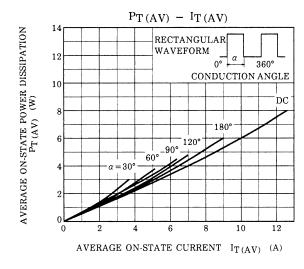


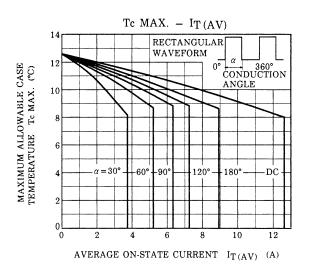




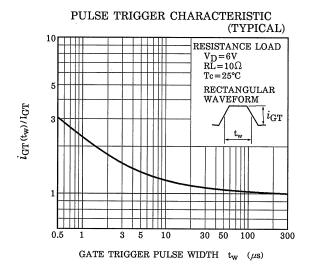


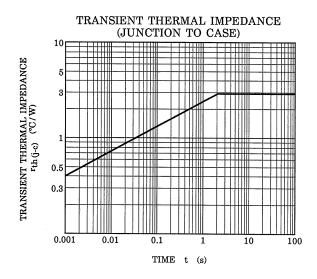






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