

# PTH03030 ART



3.3 Vin single output

DC-DC CONVERTERS

POLA Non-isolated

NEW Product





- 30 A output current
- 3.3 V input voltage
- Wide-output voltage adjust (0.8 Vdc to 2.5 Vdc)
- Auto-track<sup>™</sup> sequencing\*
- Margin up/down controls
- Pre-bias start-up capability
- Efficiencies up to 93%
- Output ON/OFF inhibit
- Output voltage sense
- Point-of-Load-Alliance (POLA) compatible
- Available RoHS compliant

The PTH03030 is a next generation series of non-isolated dc-dc converters offering some of the most advanced POL features available in the industry. The primary new feature provides for sequencing between multiple modules, a function, which is becoming a necessity for powering advanced silicon including DSP's, FPGA's and ASIC's requiring controlled power-up and power-down. Other industry leading features include margin up/down controls, pre-bias start-up capability and efficiencies up to 93%. The PTH03030 has an input voltage of 2.95 Vdc to 3.65 Vdc and offers a wide 0.8 Vdc to 2.5 Vdc output voltage range with up to 30 A output current, which allows for maximum design flexibility and a pathway for future upgrades.







2 YEAR WARRANTY

All specifications are typical at nominal input, full load at 25 °C unless otherwise stated  $C_{in}$  = 1500  $\mu$ F,  $C_{out}$  = 0  $\mu$ F

**SPECIFICATIONS** 

#### **OUTPUT SPECIFICATIONS**

Voltage adjustability	(See Note 4)	0.8-2.5 Vdc
Setpoint accuracy		±2.0% Vo
Line regulation		±10 mV typ.
Load regulation		±12 mV typ.
Total regulation		±3.0% Vo
Minimum load		0 A
Ripple and noise	20 MHz bandwidth	30 mV pk-pk
Temperature co-efficient	-40 °C to +85 °C	±0.5% Vo
Transient response (See Note 5)	Overshoot	70 µs recovery time /undershoot 100 mV
Margin adjustment		±5.0% Vo

### **EMC CHARACTERISTICS**

Electrostatic discharge	EN61000-4-2, IEC801-2
Conducted immunity	EN61000-4-6
Radiated immunity	EN61000-4-3

### GENERAL SPECIFICATIONS

Insulation voltage Non-isolated Switching frequency 275 kHz to 325 kHz
Switching frequency 275 kHz to 325 kHz
Approvals and EN60950 standards UL/cUL60950
Material flammability UL94V-0
Dimensions (L x W x H) 34.80 x 28.45 x 9.00 mm 1.370 x 1.120 x 0.354 in
Weight 10 g (0.35 oz)
MTBF Telcordia SR-332 2,821,000 hours

### **INPUT SPECIFICATIONS**

Input voltage range	(See Note 3)	2.95-3.65 Vdc
Input current	No load	10 mA typ.
Remote ON/OFF	(See Note 1)	Positive logic
Start-up time		1 V/ms
Undervoltage lockout		2.8-2.95 V typ.
Track input voltage	Pin 11 (See Note 6, 7)	±0.3 Vin

### **ENVIRONMENTAL SPECIFICATIONS**

Thermal performance (See Note 2)		-40 °C to +85 °C		
(See Note 2)	temperature Non-operating	-40 °C to +125 °C		
MSL ('Z' suffix only)	JEDEC J-STD-020C	Level 3		

Auto reset

## International Safety Standard Approvals



UL/cUL CAN/CSA-C22.2 No. 60950-1-03/UL 60950-1, File No. E174104



TÜV Product Service (EN60950) Certificate No. B 04 06 38572 044 CB Report and Certificate to IEC60950, Certificate No. US/8292/UL

\*Auto-track™ is a trade mark of Texas Instruments

PROTECTION
Short-circuit

Thermal

45 A typ.



# PTH03030 ARTI



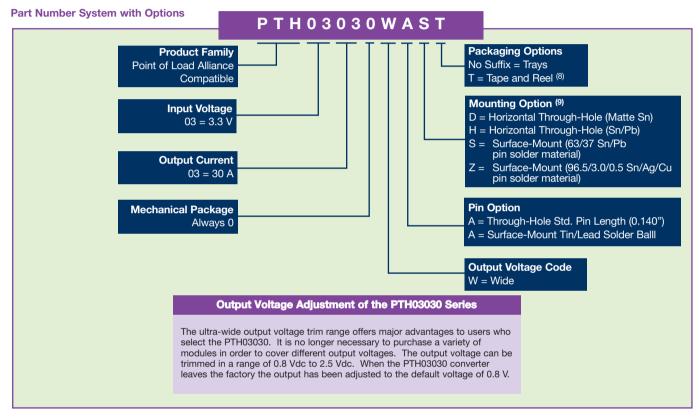
3.3 Vin single output

DC-DC CONVERTERS POLA Non-isolated

For the most current data and application support visit www.artesyn.com/powergroup/products.htm

**NEW Product** 

OUTPUT POWER	INPUT	OUTPUT	OUTPUT	OUTPUT CURRENT	EFFICIENCY	REGU	LATION	MODEL
(MAX.)	VOLTAGE	VOLTAGE	(MIN.)	(MAX.)	(MAX.)	LINE	LOAD	NUMBER <sup>(9,10)</sup>
75 W	2.95-3.65 Vdc	0.8-2.5 Vdc	0 A	30 A	93%	±10 mV	±12 mV	PTH03030



#### **Notes**

Remote ON/OFF. Positive Logic

Pin 3 open; or V > Vin - 0.5 V

Pin 3 GND; or V < 0.8 V (min - 0.2 V)

See Figure 1 for safe operating curve.

- A 1,500 µF electrolytic input capacitor is required for proper operation. The capacitor must be rated for a minimum of 900 mA rms of ripple current.
- An external output capacitor is not required for basic operation. Adding 330 µF of distributed capacitance at the load will improve the transient response.
- 1 A/ $\mu$ s load step, 50 to 100% I $_{omax}$ ,  $C_{out}$  = 330  $\mu$ F. If utilized Vout will track applied voltage by  $\pm 0.3$  V (up to Vo set point).
- The pre-bias start-up feature is not compatible with Auto-Track <sup>™</sup>. This is because when the module is under Auto-Track <sup>™</sup> control, it is fully active and will sink current if the output voltage is below that of a back-feeding source. Therefore to ensure a pre-bias hold-off, one of the following two techniques must be followed when input power is first applied to the module. The Auto-Track $^{\text{TM}}$  function must either be disabled, or the module's output held off using the Inhibit pin. Refer to Application Note 152 for more details.
- Tape and reel packaging only available on the surface-mount versions.
- To order Pb-free (RoHS compatible) surface-mount parts replace the mounting option 'S' with 'Z', e.g. PTH03030WAZ. To order Pb-free (RoHS compatible) through-hole parts replace the mounting option 'H' with 'D', e.g. PTH03030WAD.
- 10 NOTICE: Some models do not support all options. Please contact your local Artesyn representative or use the on-line model number search tool at http://www.artesyn.com/powergroup/products.htm to find a suitable alternative.

EFFICIENCY TABLE (I <sub>O</sub> = 20 A)				
OUTPUT VOLTAGE	EFFICIENCY			
Vo = 1.0 V	85%			
Vo = 1.2 V	87%			
Vo = 1.5 V	89%			
Vo = 1.8 V	91%			
Vo = 2.0 V	92%			
Vo = 2.5 V	93%			



# PTH03030 3.3 Vin single output



DC-DC CONVERTERS POLA Non-isolated 3

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**NEW Product** 

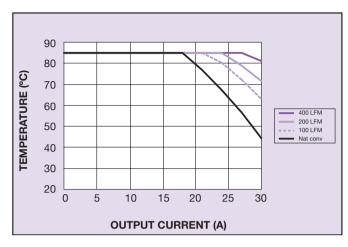


Figure 1 - Safe Operating Area
Vin = 3.3 V, Output Voltage = 2.5 V (See Note A)

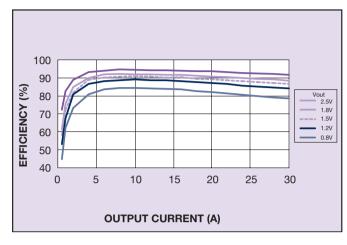


Figure 2 - Efficiency vs Load Current Vin = 3.3 V (See Note B)

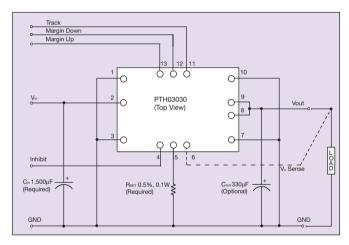


Figure 3 - Standard Application

#### **Notes**

- A SOA curves represent the conditions at which internal components are within the Artesyn derating guidelines.
   B Characteristic data has been developed from actual products tested at
- B Characteristic data has been developed from actual products tested at 25 °C. This data is considered typical data for the converter.



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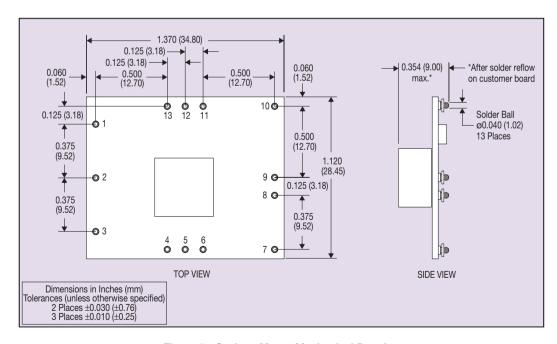
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1.370 (34.80) 0.125 (3.18) 0.140 0.125 (3.18) 0.060 0.500 (12.70) (3.55)0.060 0.500 (1.52)(1.52)(12.70)Ø0.040 (1.02) 13 Places 13 10 🕏 0.125 (3.18) 12 0.500 0.375 (12.70)(9.52)Lowest Component 1.120 (28.45) 0.010 min. (0.25) Bottom side Clearance **©** 2 90 0.125 (3.18) 0.375 (9.52) 0.375 (0.070 (1.78) (9.52)**⊚** 3 (Standoff Shoulder) 5 **©** 6 Ó TOP VIEW Host Board 0.354 (9.00) Dimensions in Inches (mm)
Tolerances (unless otherwise specified)
2 Places ±0.030 (±0.76)
3 Places ±0.010 (±0.25) max.

Figure 4 - Plated Through-Hole Mechanical Drawing

SIDE VIEW



PIN CONNECTIONS		
PIN NO.	FUNCTION	
1	Ground	
2	Vin	
3	Ground	
4	Inhibit*	
5	Vo adjust	
6	Vo sense	
7	Ground	
8	Vout	
9	Vout	
10	Ground	
11	Track	
12	Margin down*	
13	Margin up*	

\*Denotes negative logic: Open = Normal operation Ground = Function active

Figure 5 - Surface-Mount Mechanical Drawing

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