

TOSHIBA Photocoupler GaAs Ired & Photo-Triac

TLP3061(S),TLP3062(S),TLP3063(S)

Office Machine

Household Use Equipment

Triac Driver

Solid State Relay

The TOSHIBA TLP3061 (S), TLP3062 (S) and TLP3063 (S) consist of a zero voltage crossing turn-on photo-triac optically coupled to a gallium arsenide infrared emitting diode in a six lead plastic DIP package. All parameters are tested to the specification of TLP3061, TLP3062, TLP3063.

- Peak off-state voltage: 600 V (min)
- Trigger LED current: 15 mA (max) (TLP3061)
10 mA (max) (TLP3062)
5 mA(max) (TLP3063)
- On-state current: 100 mA (max)
- UL recognized: UL1577, file no. E67349
- Isolation voltage: 5000 V_{rms} (min)
- SEMKO approved: SS EN60065
SS EN60950
- BSI approved: BS EN60065
BS EN60950
- Option (D4) type
VDE approved: DIN VDE0884 / 06.92
Certificate no. 68329

Maximum operating insulation voltage: 890V_{PK}

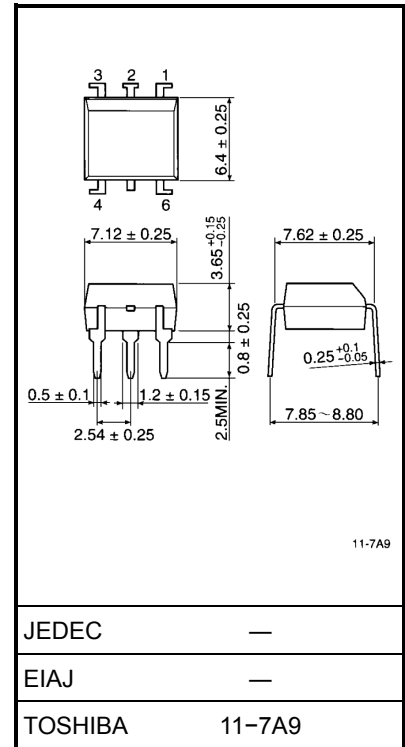
Highest permissible over voltage: 8000V_{PK}

(Note) When a VDE0884 approved type is needed, please designate the "Option (D4)"

- Device construction

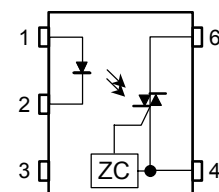
	7.62mm pich standard type	10.16mm pich (LF2) type
Creepage distance	7.0 mm (min)	8.0 mm (min)
Clearance	7.0 mm (min)	8.0 mm (min)
Insulation thickness	0.5 mm (min)	0.5 mm (min)

Unit in mm



Weight: 0.39 g

Pin Configuration(top view)



- 1 : Anode
- 2 : Cathode
- 3 : N.C.
- 4 : Terminal 1
- 6 : Terminal 2

Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit
LED	Forward current		I _F	50	mA
	Forward current derating (Ta ≥ 53°C)		ΔI _F / °C	−0.7	mA / °C
	Peak forward current (100 μs pulse, 100 pps)		I _{FP}	1	A
	Power dissipation		P _D	100	mW
	Power dissipation derating (Ta ≥ 25°C)		ΔP _D / °C	−1.0	mW / °C
	Reverse voltage		V _R	5	V
	Junction temperature		T _j	125	°C
Detector	Off-state output terminal voltage		V _{DRM}	600	V
	On-state RMS current	Ta = 25°C	I _{T(RMS)}	100	mA
		Ta = 70°C		50	
	On-state current derating (Ta ≥ 25°C)		ΔI _T / °C	−1.1	mA / °C
	Peak on-state current (100μs pulse, 120 pps)		I _{TP}	2	A
	Peak nonrepetitive surge current (P _W = 10 ms, DC = 10%)		I _{TSM}	1.2	A
	Power dissipation		P _D	300	mW
	Power dissipation derating (Ta ≥ 25°C)		ΔP _D / °C	−4.0	mW / °C
	Junction temperature		T _j	115	°C
Storage temperature range			T _{stg}	−55~150	°C
Operating temperature range			T _{opr}	−40~100	°C
Lead soldering temperature (10 s)			T _{sol}	260	°C
Total package power dissipation			P _T	330	mW
Total package power dissipation derating (Ta ≥ 25°C)			ΔP _T / °C	−4.4	mW / °C
Isolation voltage (AC, 1 min., R.H.≤ 60%) (Note 1)			BV _S	5000	Vrms

(Note 1) Device considered a two terminal device: Pins 1, 2 and 3 shorted together and pins 4 and 6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Supply voltage	V_{AC}	—	—	240	Vac
Forward current	I_F^*	15	20	25	mA
Peak on-state current	I_{TP}	—	—	1	A
Operating temperature	T_{opr}	-25	—	85	°C

※ In the case of TLP3062

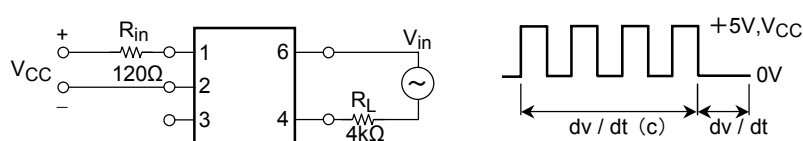
Individual Electrical Characteristics (Ta = 25°C)

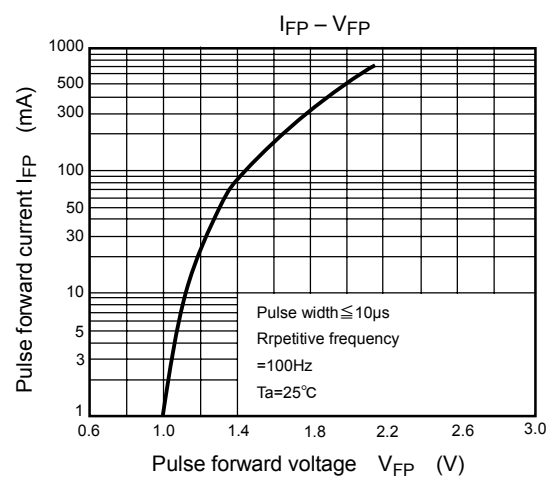
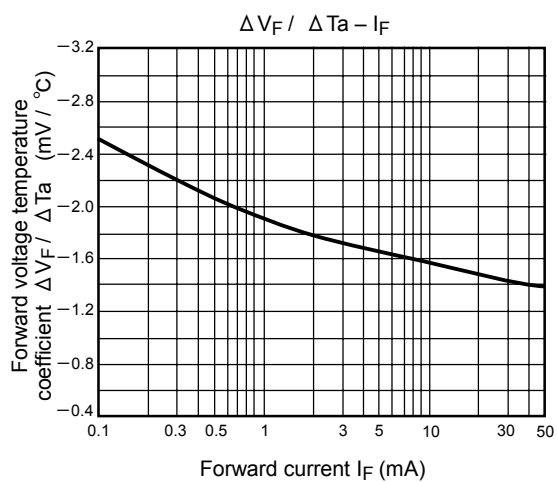
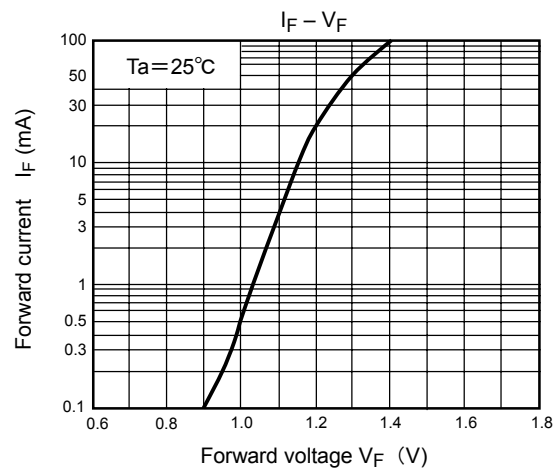
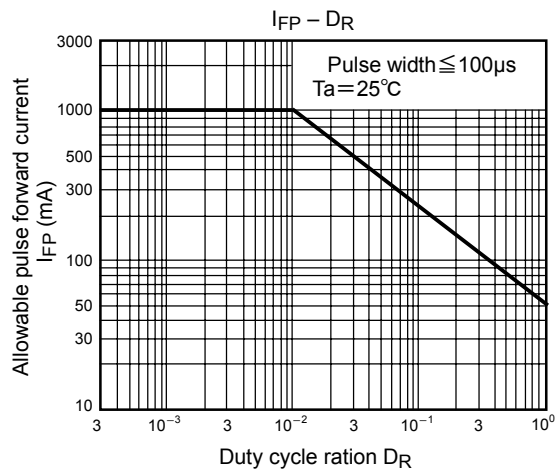
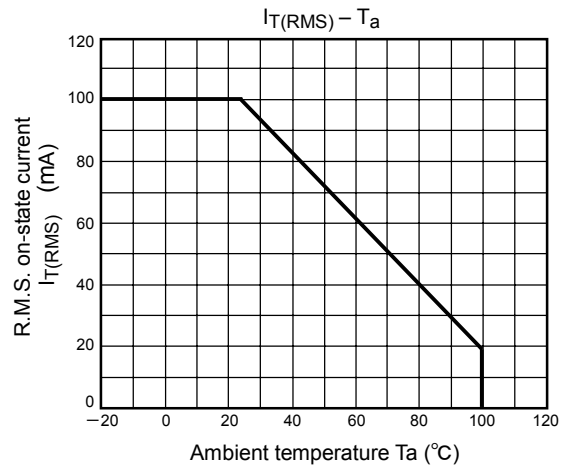
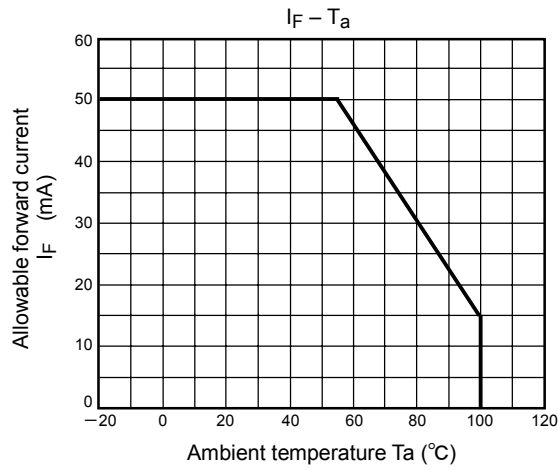
Characteristic		Symbol	Test Condition	Min.	Typ.	Max.	Unit
LED	Forward voltage	V_F	$I_F = 10 \text{ mA}$	1.0	1.15	1.3	V
	Reverse current	I_R	$V_R = 5 \text{ V}$	—	—	10	μA
	Capacitance	C_T	$V = 0, f = 1 \text{ MHz}$	—	10	—	pF
Detector	Peak off-state current	I_{DRM}	$V_{\text{DRM}} = 600 \text{ V}$	—	10	1000	nA
	Peak on-state voltage	V_{TM}	$I_{\text{TM}} = 100 \text{ mA}$	—	1.7	3.0	V
	Holding current	I_H	—	—	0.6	—	mA
	Critical rate of rise of off-state voltage	dv / dt	$V_{\text{in}} = 240 \text{ Vrms}, T_a = 85^\circ\text{C}$ (Fig.1)	200	500	—	V / μs
	Critical rate of rise of commutating voltage	$dv / dt (c)$	$V_{\text{in}} = 60 \text{ Vrms}, I_T = 15\text{mA}$ (Fig.1)	—	0.2	—	V / μs

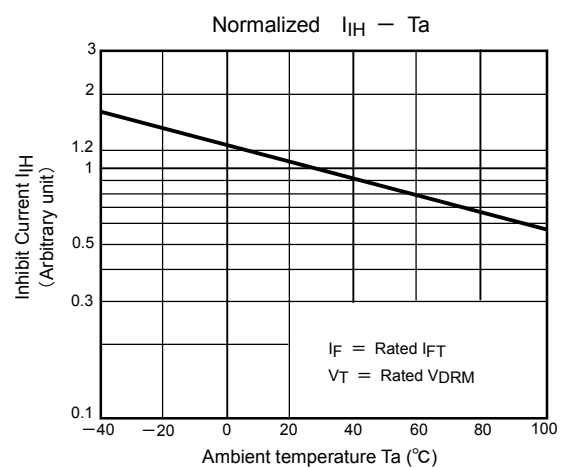
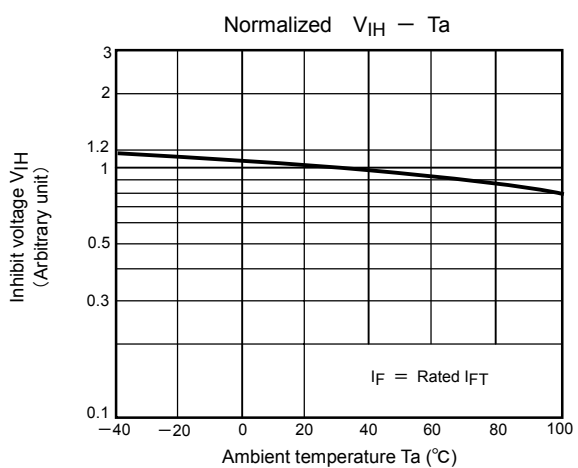
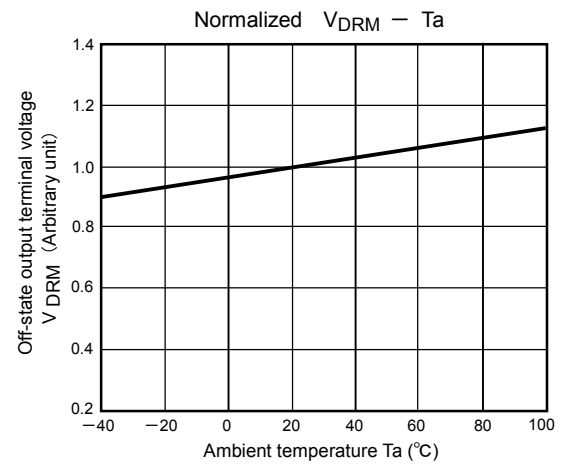
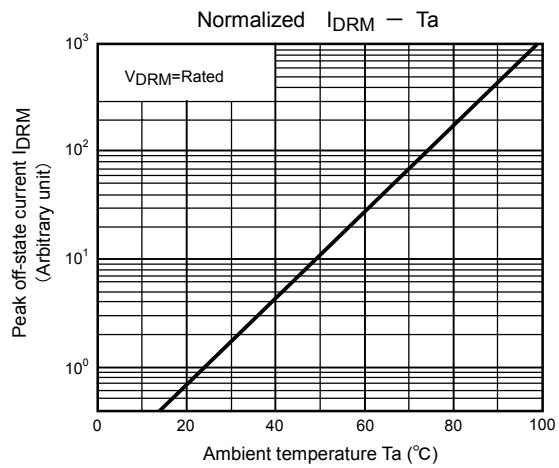
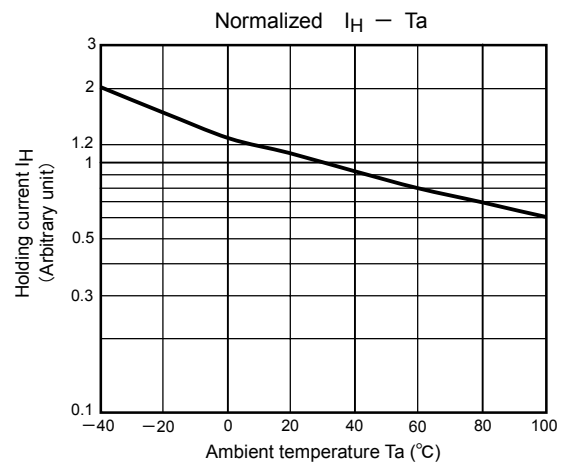
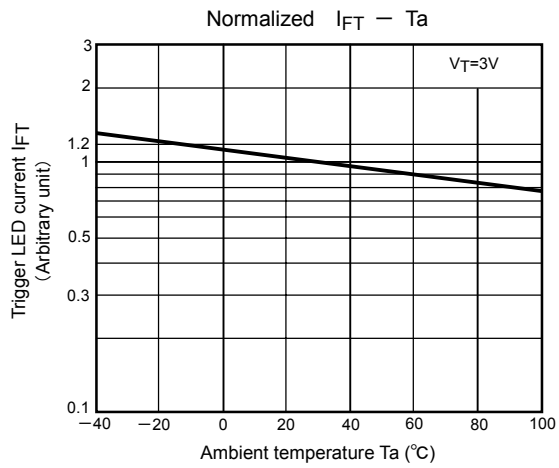
Coupled Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min.	Typ.	Max.	Unit
Trigger LED current	TLP3061	I_{FT}	$V_T = 6 \text{ V}$	—	—	15	mA
	TLP3062			—	5	10	
	TLP3063			—	—	5	
Inhibit voltage		V_{IH}	$I_F = \text{rated } I_{\text{FT}}$	—	—	50	V
Leakage in inhibited state		I_{IH}	$I_F = \text{rated } I_{\text{FT}}$ $V_T = \text{rated } V_{\text{DRM}}$	—	100	300	μA
Capacitance input to output		C_S	$V_S = 0, f = 1 \text{ MHz}$	—	0.8	—	pF
Isolation resistance		R_S	$V_S = 500 \text{ V (R.H.} \leq 60\%)$	5×10^{10}	10^{14}	—	Ω
Isolation voltage		BV_S	AC, 1 minute	5000	—	—	Vrms
			AC, 1 second, in oil	—	10000	—	
			DC, 1 minute, in oil	—	10000	—	V _{dc}

Fig. 1 dv / dt test circuit







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