

FGAF40N60UF

Ultrafast IGBT

General Description

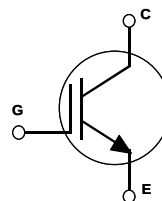
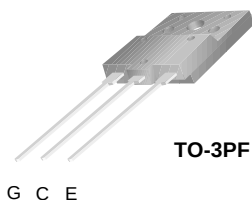
Fairchild's UF series of Insulated Gate Bipolar Transistors (IGBTs) provides low conduction and switching losses. The UF series is designed for applications such as motor control and general inverters where high speed switching is a required feature.

Features

- High speed switching
- Low saturation voltage : $V_{CE(sat)} = 2.3 \text{ V @ } I_C = 20\text{A}$
- High input impedance

Applications

AC & DC motor controls, general purpose inverters, robotics, and servo controls.



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Description	FGAF40N60UF	Units
V_{CES}	Collector-Emitter Voltage	600	V
V_{GES}	Gate-Emitter Voltage	± 20	V
I_C	Collector Current @ $T_C = 25^\circ\text{C}$	40	A
	Collector Current @ $T_C = 100^\circ\text{C}$	20	A
$I_{CM(1)}$	Pulsed Collector Current	160	A
P_D	Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$	100	W
	Maximum Power Dissipation @ $T_C = 100^\circ\text{C}$	40	W
T_J	Operating Junction Temperature	-55 to +150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temp. for Soldering Purposes, 1/8" from Case for 5 Seconds	300	$^\circ\text{C}$

Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}(\text{IGBT})$	Thermal Resistance, Junction-to-Case	--	1.2	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	--	40	$^\circ\text{C/W}$

Electrical Characteristics of the IGBT $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
Off Characteristics						
BV_{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_C = 250\mu A$	600	--	--	V
$\Delta B_{V_{CES}} / \Delta T_J$	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0V, I_C = 1mA$	--	0.6	--	V/°C
I_{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$	--	--	250	uA
I_{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$	--	--	± 100	nA
On Characteristics						
$V_{GE(th)}$	G-E Threshold Voltage	$I_C = 20mA, V_{CE} = V_{GE}$	3.5	5.1	6.5	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	$I_C = 20A, V_{GE} = 15V$	--	2.3	3.0	V
		$I_C = 40A, V_{GE} = 15V$	--	3.1	--	V
Dynamic Characteristics						
C_{ies}	Input Capacitance	$V_{CE} = 30V, V_{GE} = 0V, f = 1MHz$	--	1075	--	pF
C_{oes}	Output Capacitance		--	170	--	pF
C_{res}	Reverse Transfer Capacitance		--	50	--	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 300V, I_C = 20A, R_G = 10\Omega, V_{GE} = 15V, \text{Inductive Load, } T_C = 25^\circ C$	--	15	--	ns
t_r	Rise Time		--	30	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	65	130	ns
t_f	Fall Time		--	35	100	ns
E_{on}	Turn-On Switching Loss		--	470	--	uJ
E_{off}	Turn-Off Switching Loss		--	130	--	uJ
E_{ts}	Total Switching Loss	$V_{CC} = 300V, I_C = 20A, R_G = 10\Omega, V_{GE} = 15V, \text{Inductive Load, } T_C = 125^\circ C$	--	600	1000	uJ
$t_{d(on)}$	Turn-On Delay Time		--	30	--	ns
t_r	Rise Time		--	37	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	110	200	ns
t_f	Fall Time		--	80	250	ns
E_{on}	Turn-On Switching Loss		--	500	--	uJ
E_{off}	Turn-Off Switching Loss	$V_{CE} = 300V, I_C = 20A, V_{GE} = 15V$	--	310	--	uJ
E_{ts}	Total Switching Loss		--	810	1200	uJ
Q_g	Total Gate Charge		--	77	150	nC
Q_{ge}	Gate-Emitter Charge		--	20	30	nC
Q_{gc}	Gate-Collector Charge	--	25	40	nC	
L_e	Internal Emitter Inductance	Measured 5mm from PKG	--	14	--	nH

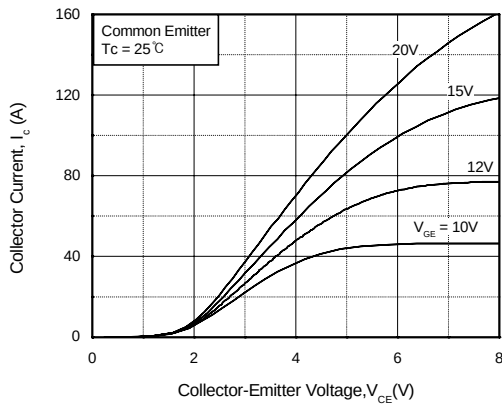


Fig 1. Typical Output Characteristics

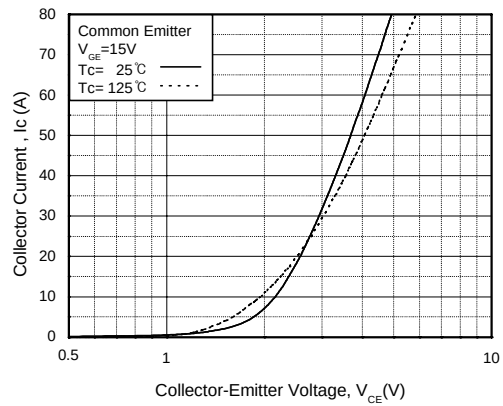


Fig 2. Typical Saturation Voltage Characteristics

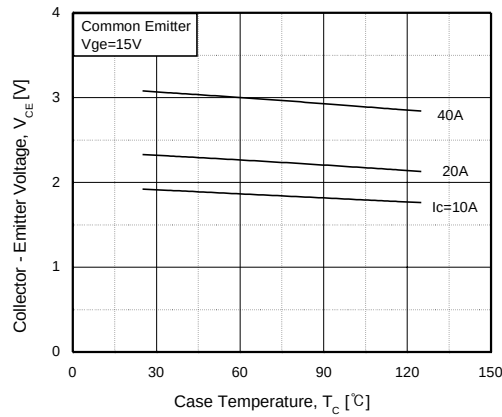


Fig 3. Saturation Voltage vs. Case Temperature at Variant Current Level

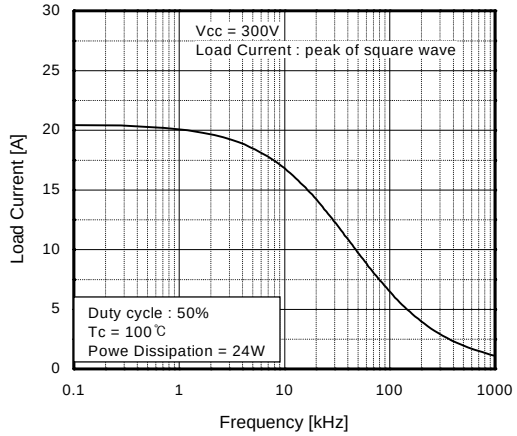


Fig 4. Load Current vs. Frequency

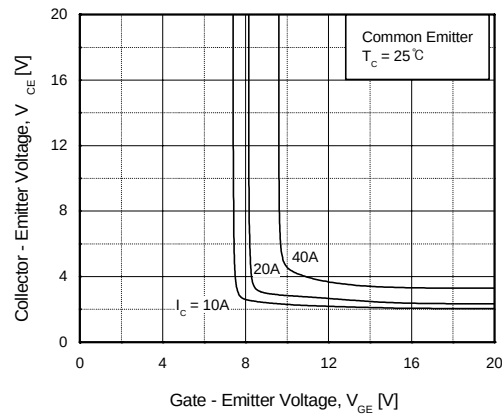


Fig 5. Saturation Voltage vs. V_{GE}

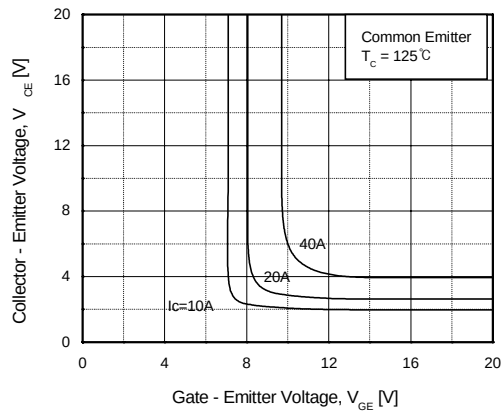


Fig 6. Saturation Voltage vs. V_{GE}

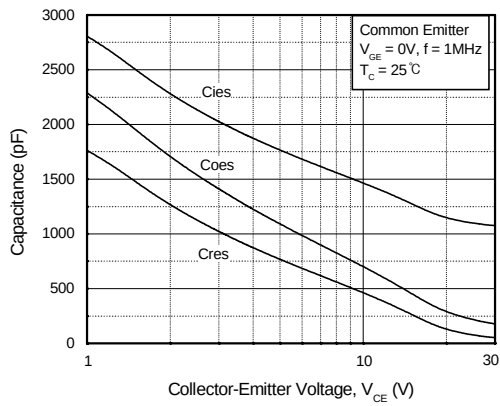


Fig 7. Capacitance Characteristics

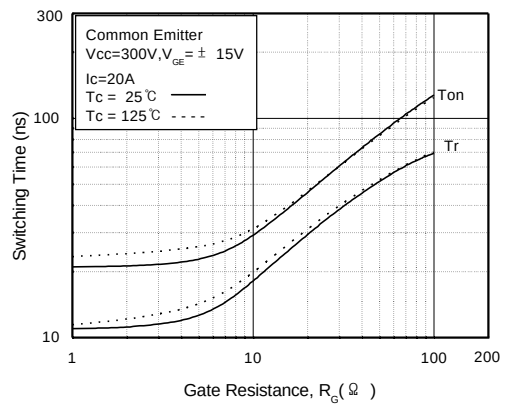


Fig 8. Turn-On Characteristics vs. Gate Resistance

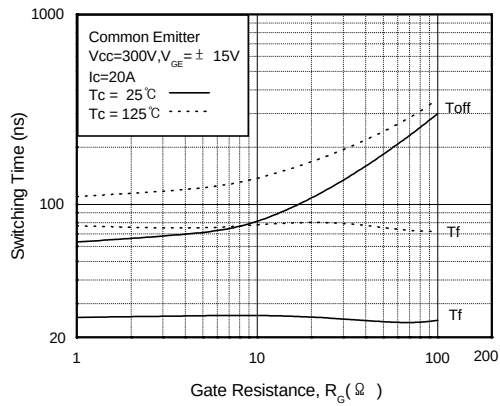


Fig 9. Turn-Off Characteristics vs. Gate Resistance

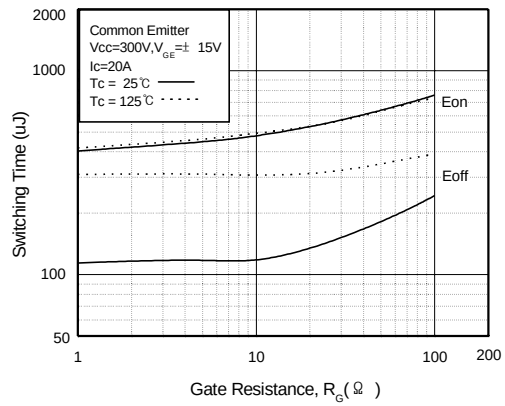


Fig 10. Switching Loss vs. Gate Resistance

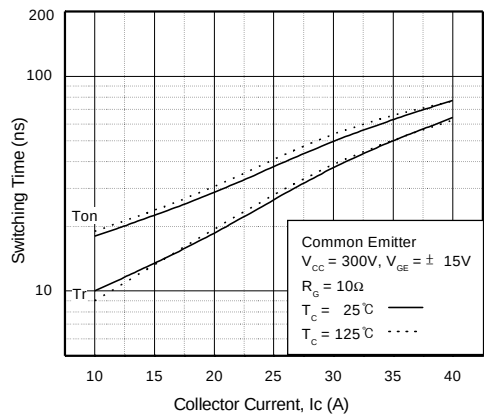


Fig 11. Turn-On Characteristics vs. Collector Current

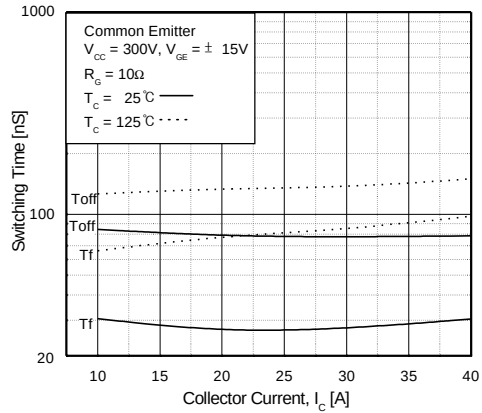


Fig 12. Turn-Off Characteristics vs. Collector Current

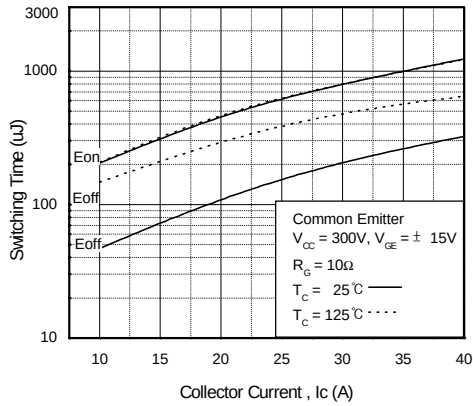


Fig 13. Switching Loss vs. Collector Current

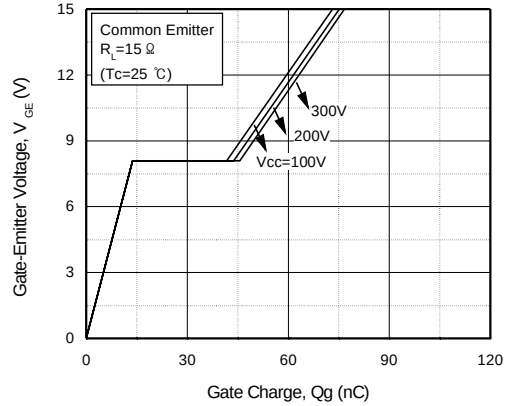


Fig 14. Gate Charge Characteristics

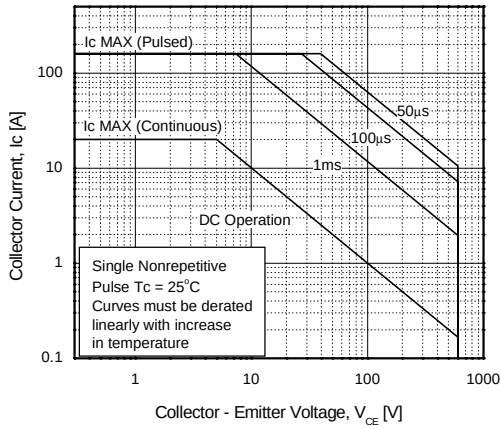


Fig 15. SOA Characteristics

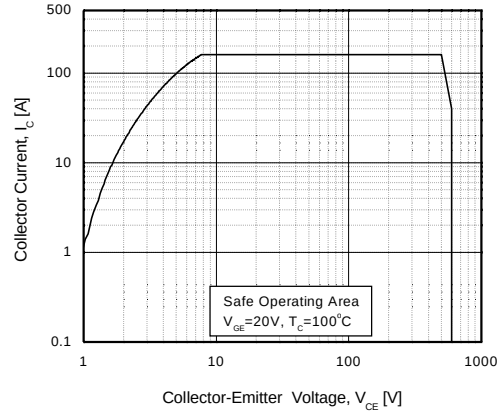


Fig 16. Turn-Off SOA Characteristics

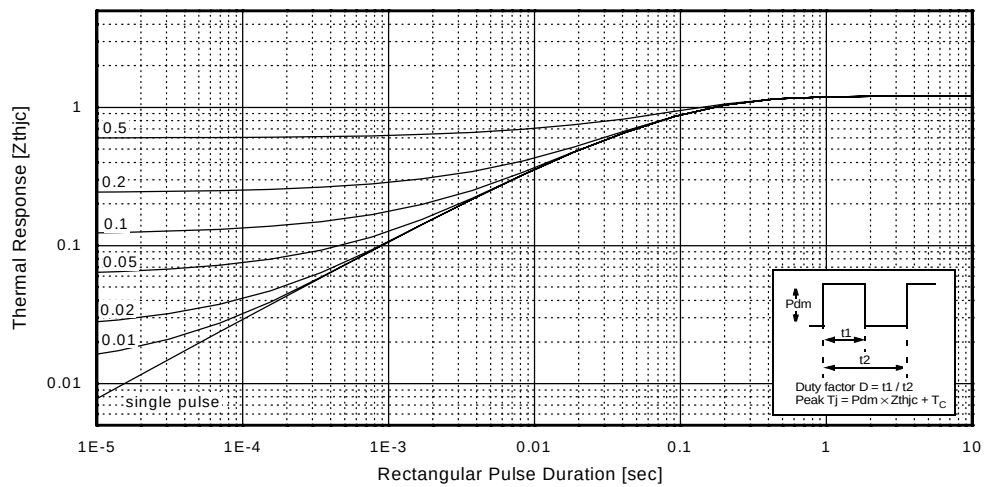
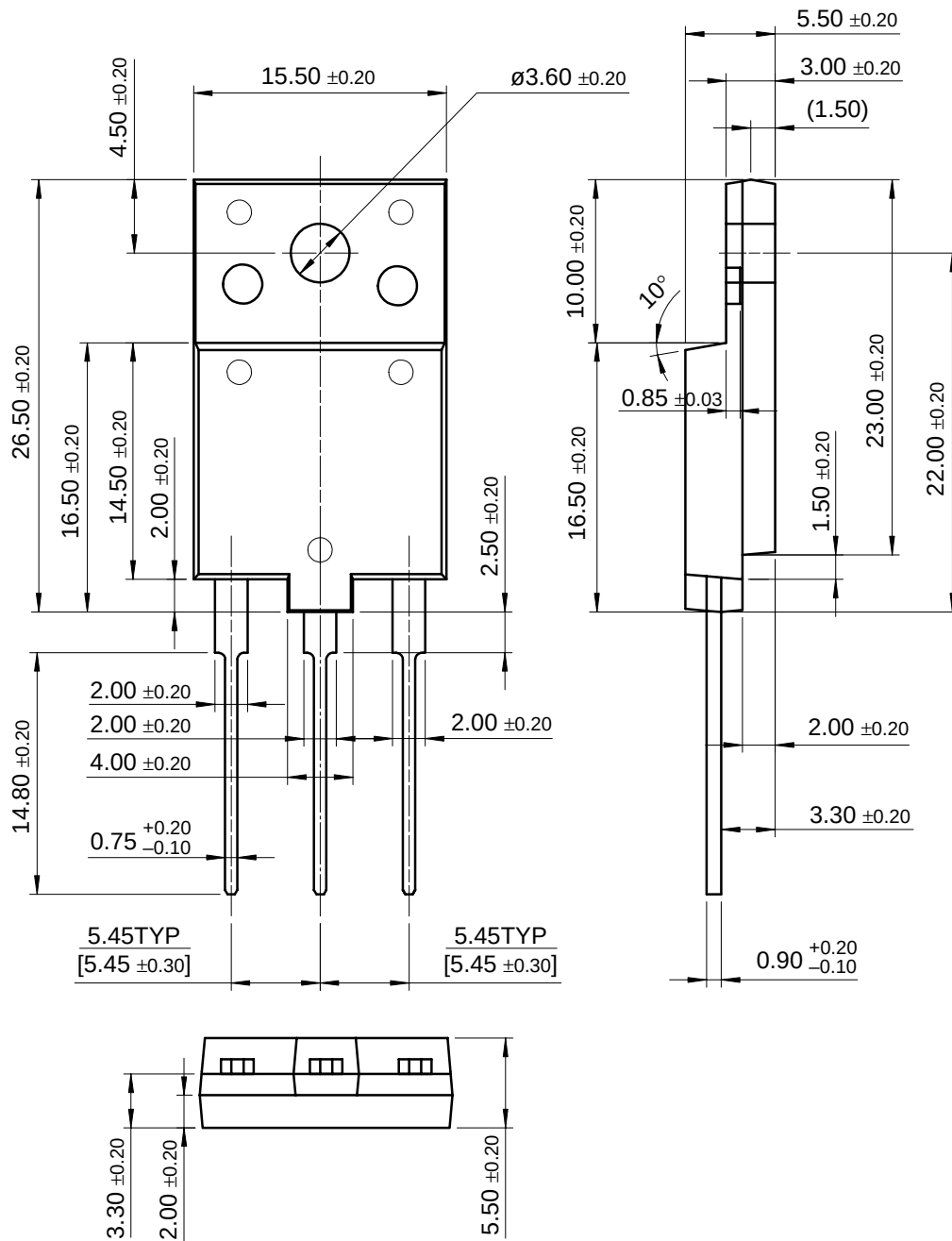


Fig 17. Transient Thermal Impedance of IGBT

Package Dimensions

TO-3PF



Dimensions in Millimeters

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACE [™]	FACT Quiet Series [™]	ISOPLANAR [™]	POP [™]	Stealth [™]
ActiveArray [™]	FAST [®]	LittleFET [™]	Power247 [™]	SuperFET [™]
Bottomless [™]	FAST [™]	MICROCOUPLER [™]	PowerSaver [™]	SuperSOT [™] -3
CoolFET [™]	FPS [™]	MicroFET [™]	PowerTrench [®]	SuperSOT [™] -6
CROSSVOLT [™]	FRFET [™]	MicroPak [™]	QFET [®]	SuperSOT [™] -8
DOME [™]	GlobalOptoisolator [™]	MICROWIRE [™]	QS [™]	SyncFET [™]
EcoSPARK [™]	GTO [™]	MSX [™]	QT Optoelectronics [™]	TinyLogic [®]
E ² CMOS [™]	HiSeC [™]	MSXPro [™]	Quiet Series [™]	TINYOPTO [™]
EnSigna [™]	I ² C [™]	OCX [™]	RapidConfigure [™]	TruTranslation [™]
FACT [™]	ImpliedDisconnect [™]	OCXPro [™]	RapidConnect [™]	UHC [™]
Across the board. Around the world. [™]		OPTOLOGIC [®]	SILENT SWITCHER [®]	UltraFET [®]
The Power Franchise [™]		OPTOPLANAR [™]	SMART START [™]	VCX [™]
Programmable Active Droop [™]		PACMAN [™]	SPM [™]	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.