

N82S126, N82S129 — 1024 Bit Field Programmable Bipolar Prom

GENERAL DESCRIPTION

The 82S126 and 82S129 are field programmable, which means that custom patterns are immediately available by following the fusing procedure given in this data sheet.

The standard 82S126 and 82S129 devices are supplied with all outputs at logical low. Outputs are programmed to a logic high level at any specified address by fusing a Ni-Cr link matrix.

These devices include on-chip decoding and 2 chip enable inputs for ease of memory expansion. They feature either open collector or tri-state outputs for optimization of word expansion in bused organizations.

Both 82S126 and 82S129 devices are available in the commercial and military temperature ranges. For the commercial temperature range (0°C to +75°C) specify N82S126/129, F or N, and for the military temperature range (–55°C to +125°C) specify S82S126/129, F only.

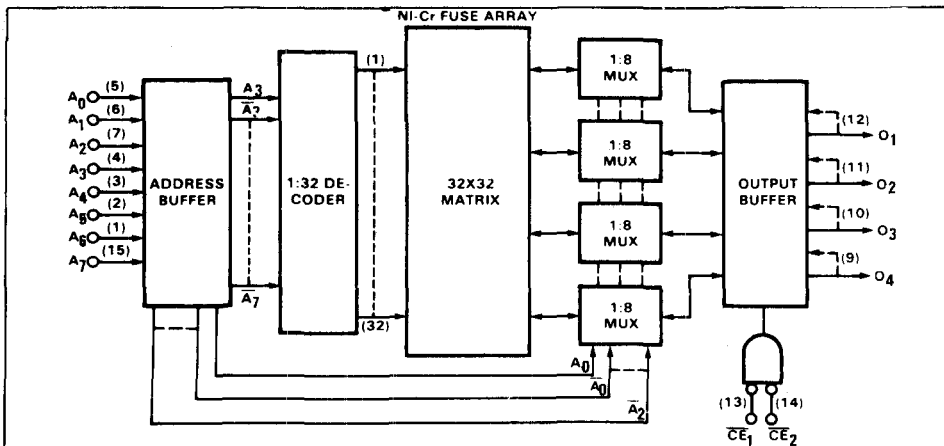
FEATURES

- Address access time:
N82S126/129: 50ns max
S82S126/129: 70ns max
- Power dissipation: 0.5mW/bit typ
- Input loading:
N82S126/129: –100µA max
S82S126/129: –150µA max
- On-chip address decoding
- Output options:
82S126: Open collector
82S129: Tri-state
- No separate fusing pins
- Unprogrammed outputs are low level
- Fully TTL compatible

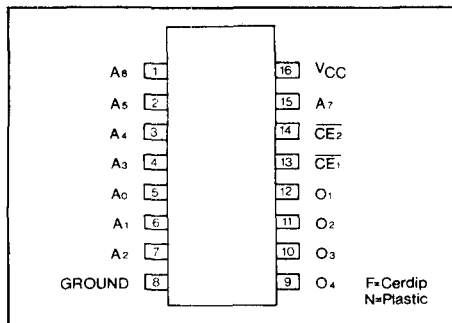
APPLICATIONS

- Prototyping/volume production
- Sequential controllers
- Microprogramming
- Hardwired algorithms
- Control store
- Random logic
- Code conversion

BLOCK DIAGRAM



CONNECTION DIAGRAM



MAXIMUM RATINGS

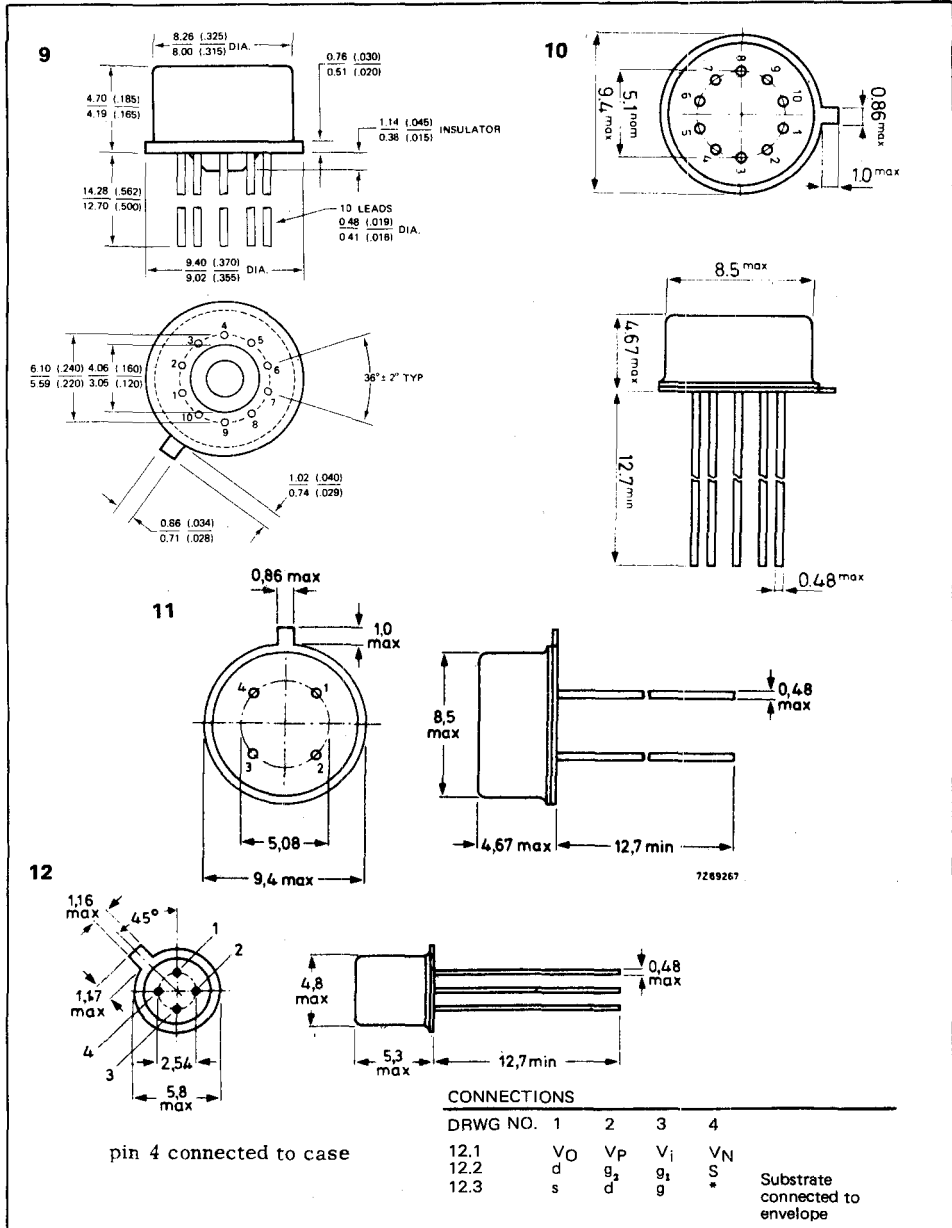
PARAMETER		RATING	UNIT
VCC	Supply voltage	+7	Vdc
VIN	Input voltage	+5.5	Vdc
VOH	Output voltage	+5.5	Vdc
VO	High (82S126) Off-state (82S129)	+5.5	
TA	Temperature range		°C
	Operating	0 to +75	
	N82S126/129	–55 to +125	
	S82S126/129	–65 to +150	
TSTG	Storage		

REFERENCE TABLE

TYPE NO.	STOCK NO.	OUTLINE DRWG. NO.
N82S126F	56300F	2
N82S126N	56301D	2
N82S129F	56302B	2
N82S129N	56303X	2

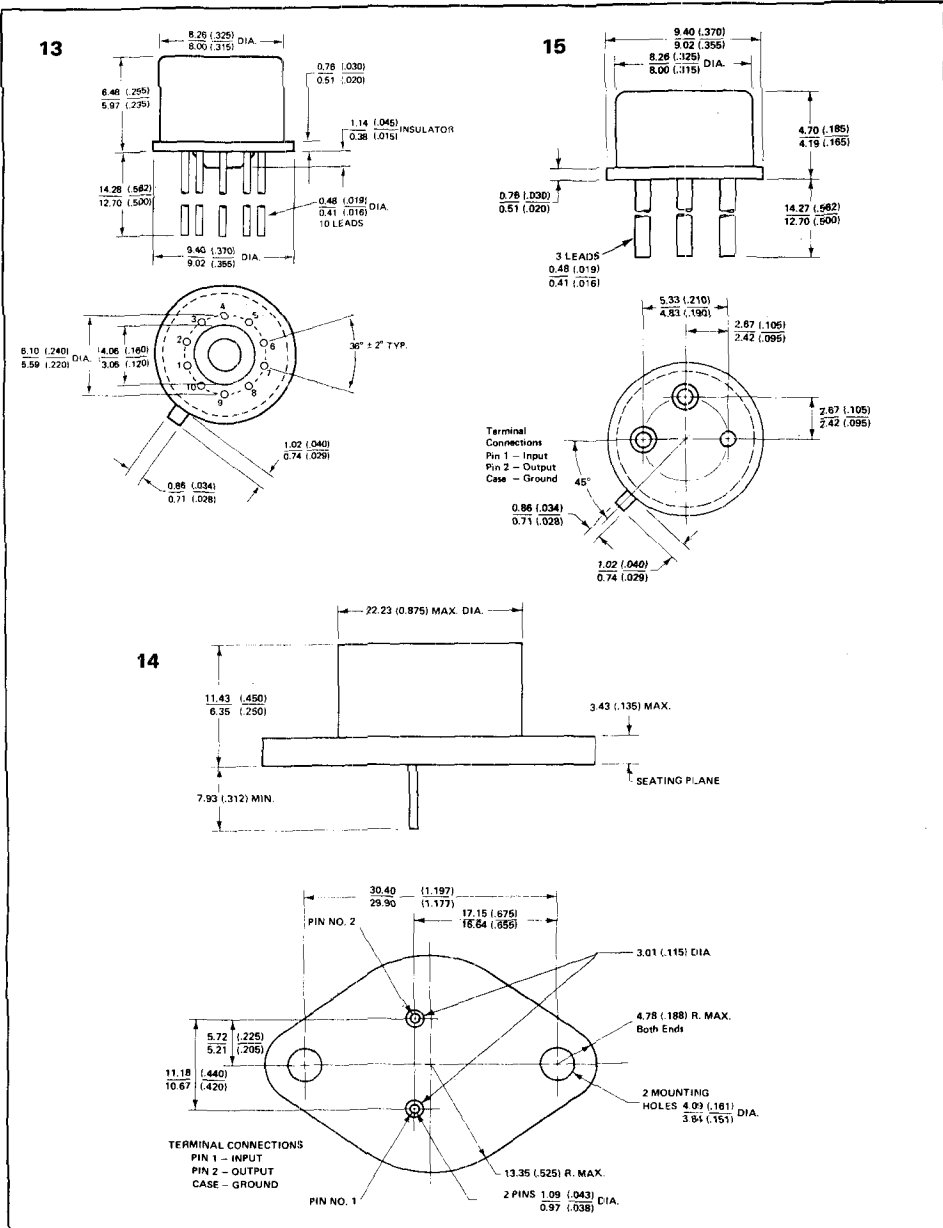
Outline Drawings



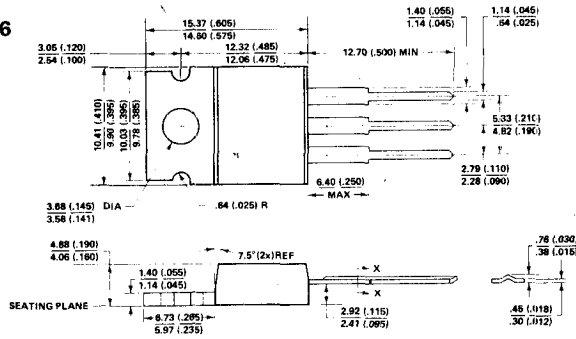


PLEASE QUOTE STOCK NO.
AND MANUFACTURERS PART NO. WHEN ORDERING

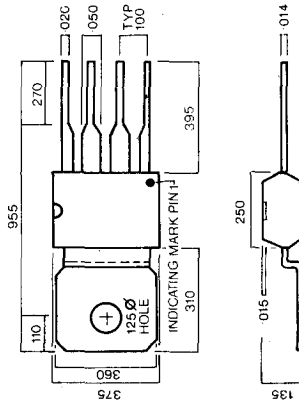
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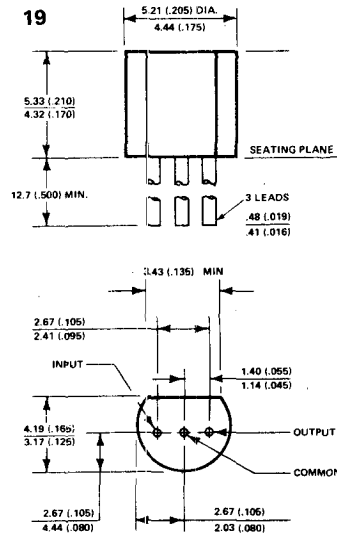
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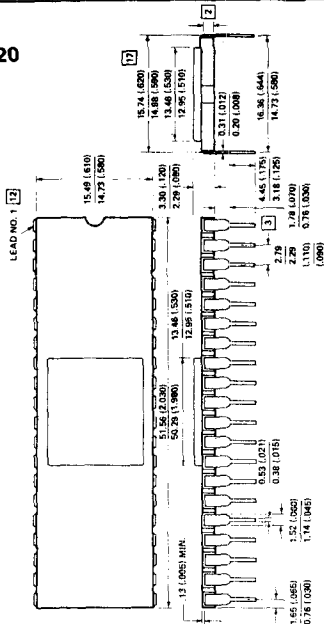
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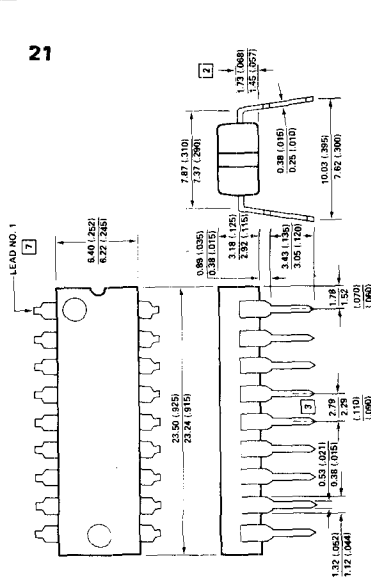
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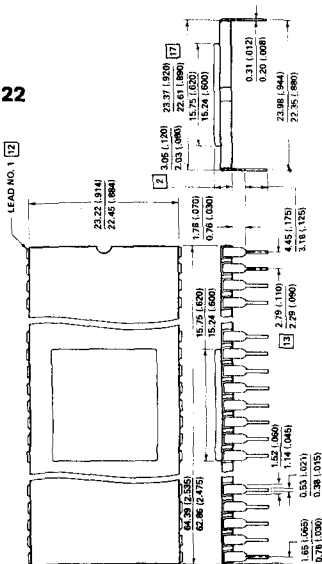
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