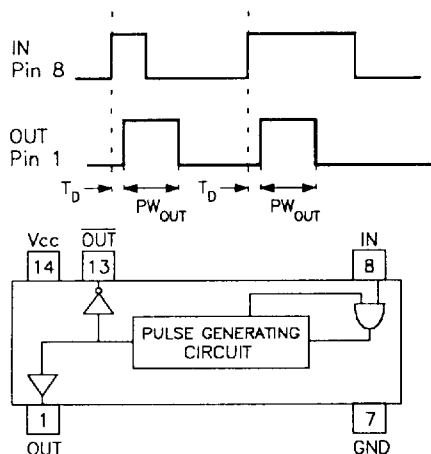


TTL Pulse Width Generator Modules

Triggered by the input's rising edge (input pulse width 10 ns, min.), a pulse of specified width will be generated at the output with a propagation delay of 5 ± 2 ns (7 ± 2 ns, for inverted output). High-to-low transitions will not trigger the unit. Designed for output duty-cycle less than 50%.

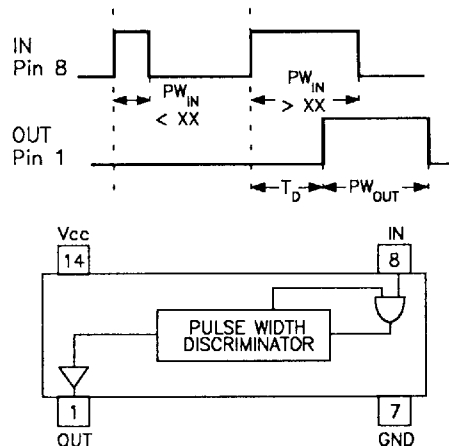


Electrical Specifications at 25°C

Output Pulse Width (ns)	TTL Buffered Pulse Width Generator Modules Maximum Freq. (MHz)	Part Number
5±1.00	63	TTLPWG-5
6±1.00	56	TTLPWG-6
7±1.00	53	TTLPWG-7
8±1.00	50	TTLPWG-8
9±1.00	45	TTLPWG-9
10±1.50	42	TTLPWG-10
15±2.00	32	TTLPWG-15
20±2.00	22	TTLPWG-20
25±2.00	19	TTLPWG-25
30±2.00	15	TTLPWG-30
35±2.00	13	TTLPWG-35
40±2.00	11	TTLPWG-40
45±2.25	10	TTLPWG-45
50±2.50	9	TTLPWG-50
60±3.00	8	TTLPWG-60
70±3.50	7	TTLPWG-70
80±4.00	6	TTLPWG-80
100±5.00	5	TTLPWG-100

TTL Pulse Width Discriminators

Input pulse widths greater than the Nominal value (XX in ns from P/N TTLPD-XX) of the module, will propagate with delay of $(XX + 5\text{ns}) \pm 5\%$ or 2 ns, whichever is greater. Output pulse width will follow the input width $\pm 7\%$ or 4 ns, whichever is greater. Input pulse widths less than the Nominal value will be suppressed.

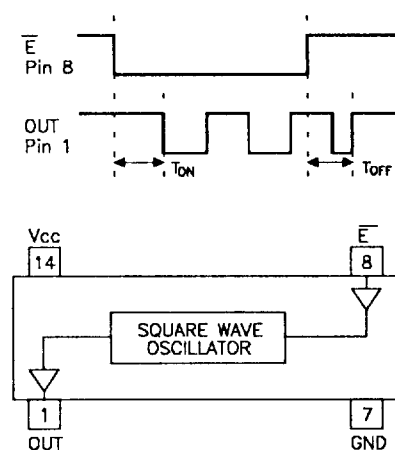


Electrical Specifications at 25°C

Part Number	Suppressed Pulse Width, Max. (ns)	Passed Pulse Width, Min. (ns)
TTLPD-10	< 8.5	> 11.5
TTLPD-15	< 13.5	> 16.5
TTLPD-20	< 18.5	> 21.5
TTLPD-25	< 23.5	> 26.5
TTLPD-30	< 28.5	> 31.5
TTLPD-35	< 33.0	> 37.0
TTLPD-40	< 38.0	> 42.0
TTLPD-45	< 42.7	> 47.3
TTLPD-50	< 47.5	> 52.5
TTLPD-60	< 57.0	> 63.0
TTLPD-75	< 71.0	> 79.0
TTLPD-80	< 76.0	> 84.0
TTLPD-100	< 95.0	> 105.0
TTLPD-120	< 114.0	> 126.0
TTLPD-125	< 118.7	> 131.3
TTLPD-150	< 142.5	> 157.5
TTLPD-200	< 190.0	> 210.0
TTLPD-250	< 237.5	> 262.5

TTL Gated Oscillators

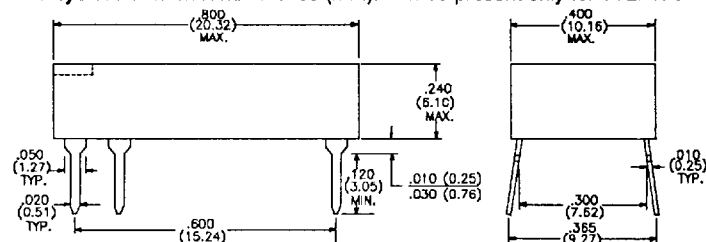
These gated oscillators permit synchronization of the output square wave with the high-to-low transition of the enable input. When the enable is high, the output is held high. The output will start with a high-to-low transition one half-cycle after the input trigger. The output frequency tolerance is $\pm 3\%$.



Electrical Specifications at 25°C

Output Frequency	Part Number
2 MHz	TTLOS-2
5 MHz	TTLOS-5
7.5 MHz	TTLOS-7.5
10 MHz	TTLOS-10
12.5 MHz	TTLOS-12.5
15 MHz	TTLOS-15
20 MHz	TTLOS-20
25 MHz	TTLOS-25
30 MHz	TTLOS-30
33 MHz	TTLOS-33
35 MHz	TTLOS-35
40 MHz	TTLOS-40
45 MHz	TTLOS-45
50 MHz	TTLOS-50
55 MHz	TTLOS-55
60 MHz	TTLOS-60
70 MHz	TTLOS-70
80 MHz	TTLOS-80

Physical Dimensions inches (mm). Pin 13 present only for TTLPWG



GENERAL: For Operating Specifications and Test Conditions, see Tables I and VII on page 7 of this catalog. Delays specified for the Leading Edge.

Temperature Coefficient 500ppm/°C typical
Supply Current, I_{cc}

TTLPWG 35 mA typ., 55 mA max.
TTLPD 42 mA typ., 60 mA max.
TTLOS 15 mA typ., 30 mA max.

VARIATIONS AVAILABLE. FOR INTERMEDIATE VALUES AND/OR CUSTOM DESIGNS PLEASE CONSULT THE FACTORY.

Specifications subject to change without notice.

TTL_PW-7/93

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