

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)} \text{ max}$	$I_D \text{ max}$ $T_A = +25^\circ\text{C}$
-20V	150m Ω @ $V_{GS} = -4.5\text{V}$	-1.9A
	200m Ω @ $V_{GS} = -2.5\text{V}$	-1.7A

Description

This MOSFET is designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

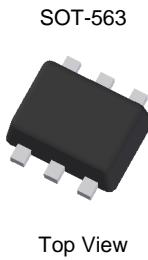
- Backlighting
- Power Management Functions
- DC-DC Converters
- Motor Control

Features

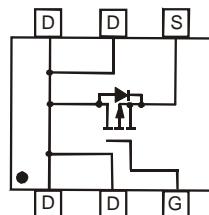
- Very Low On-Resistance
- Very Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 
- Weight: 0.006 grams (Approximate)



Top View


 Top View
 Internal Schematic

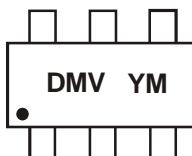
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2104V-7	SOT-563	3000/Tape & Reel

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



DMV = Marking Code
 YM = Date Code Marking
 Y = Year (ex: T = 2006)
 M = Month (ex: 9 = September)

Date Code Key

Year	2006	2007	...	2014	2015	2016	2017	2018	2019	2020	2021
Code	T	U	...	B	C	D	E	F	G	H	I

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@ $T_A = +25^\circ\text{C}$ unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	-20	V
Gate-Source Voltage			V_{GSS}	± 12	V
Continuous Drain Current (Note 5) $V_{GS} = -4.5\text{V}$	Steady State	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	-1.9 -1.5	A
Continuous Drain Current (Note 5) $V_{GS} = -4.5\text{V}$	$t \leq 5\text{s}$	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	-2.1 -1.65	A
Continuous Drain Current (Note 5) $V_{GS} = -2.5\text{V}$	Steady State	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	-1.7 -1.3	A
Pulsed Drain Current		$t_p = 10\mu\text{s}$	I_{DM}	-4.0	A

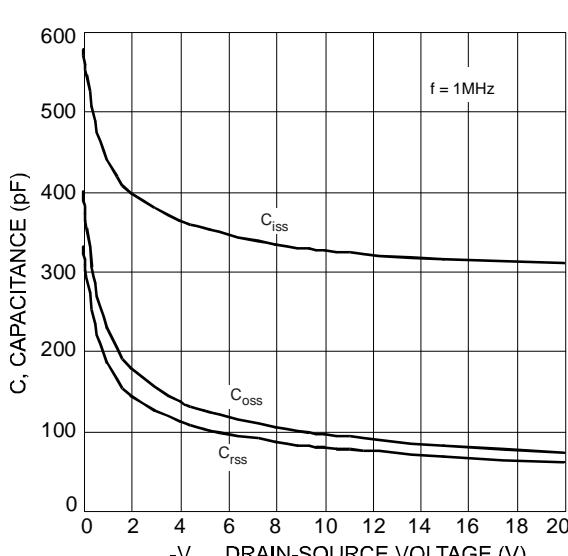
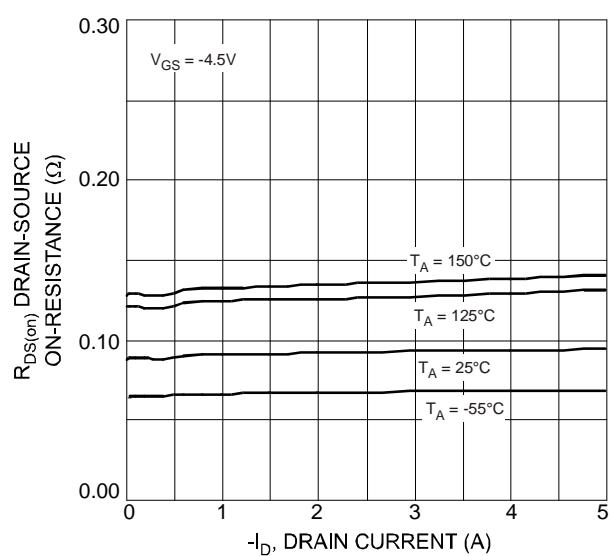
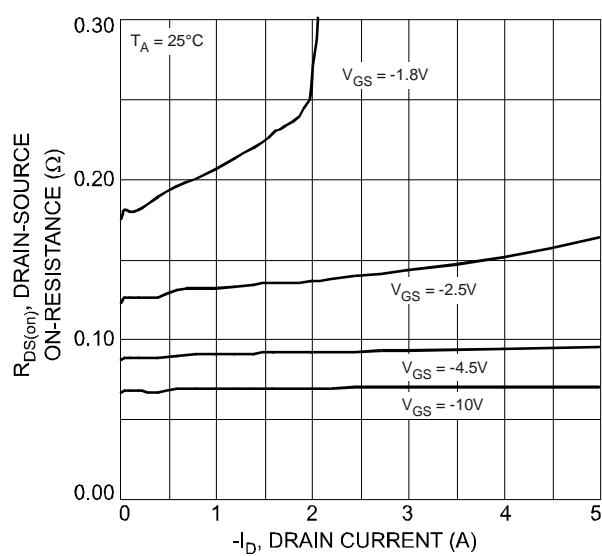
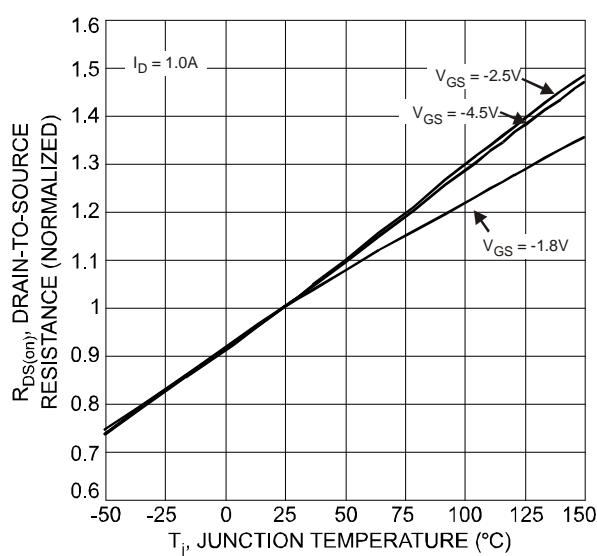
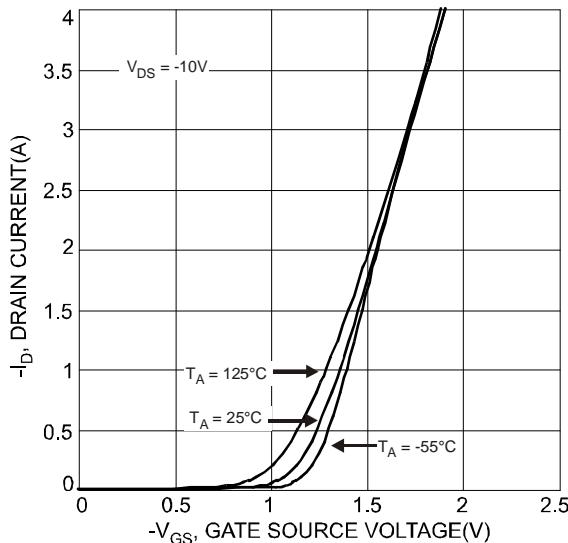
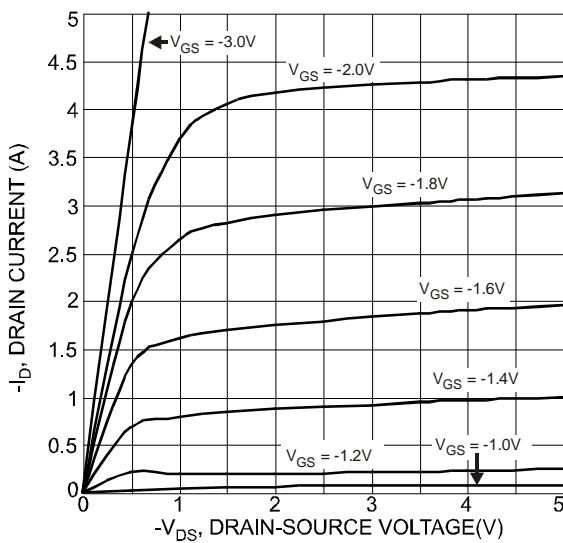
Thermal Characteristics

Characteristic	Symbol	Value	Units
Power Dissipation (Note 5)	P_D	0.85	W
Thermal Resistance, Junction to Ambient @ $T_A = +25^\circ\text{C}$ (Note 5)	$R_{\theta JA}$	146	$^\circ\text{C/W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$ unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV_{DSS}	-20	—	—	V	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-1.0 -5.0	μA	$V_{DS} = -20\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 100	nA	$V_{GS} = \pm 12\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.45	—	-1.0	V	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(\text{ON})}$	—	92 134 180	150 200 240	$\text{m}\Omega$	$V_{GS} = -4.5\text{V}, I_D = -950\text{mA}$ $V_{GS} = -2.5\text{V}, I_D = -670\text{mA}$ $V_{GS} = -1.8\text{V}, I_D = -200\text{mA}$
Forward Transconductance	g_{FS}	—	3.1	—	S	$V_{DS} = -10\text{V}, I_D = -810\text{mA}$
Diode Forward Voltage (Note 6)	V_{SD}	—	—	-0.9	V	$V_{GS} = 0\text{V}, I_S = -360\text{mA}$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	—	320	—	pF	$V_{DS} = -16\text{V}, V_{GS} = 0\text{V}$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	80	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	60	—	pF	

Notes:
5. Device mounted on FR-4 PCB with 1 inch square pads.
6. Short duration pulse test used to minimize self-heating effect.



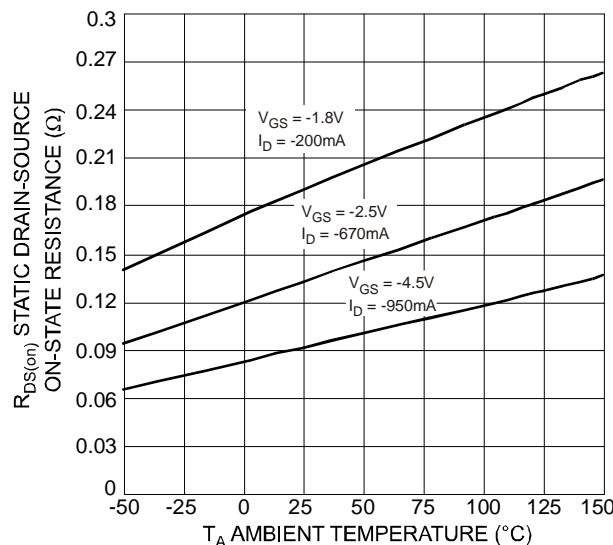


Fig. 7 Static Drain-Source On-State Resistance vs. Ambient Temperature

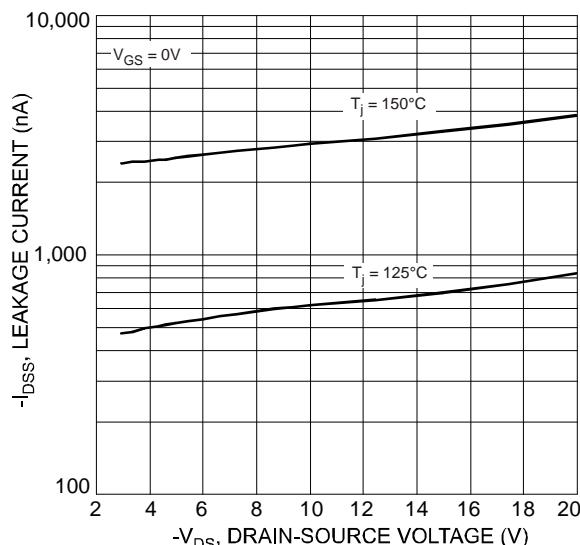


Fig. 8 Drain-Source Leakage Current vs. Voltage

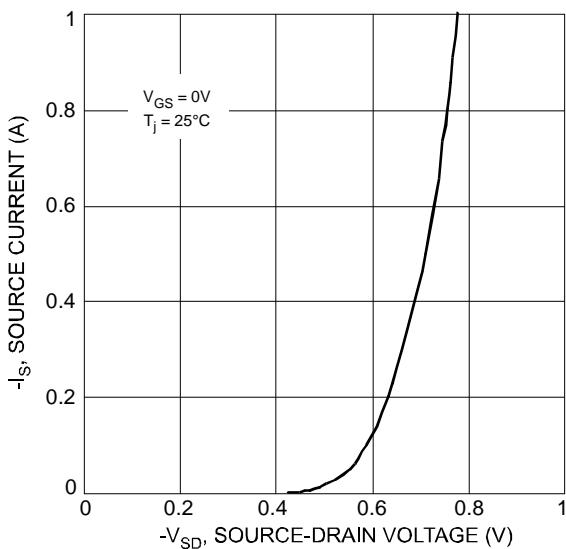


Fig. 9 Diode Forward Voltage vs. Current

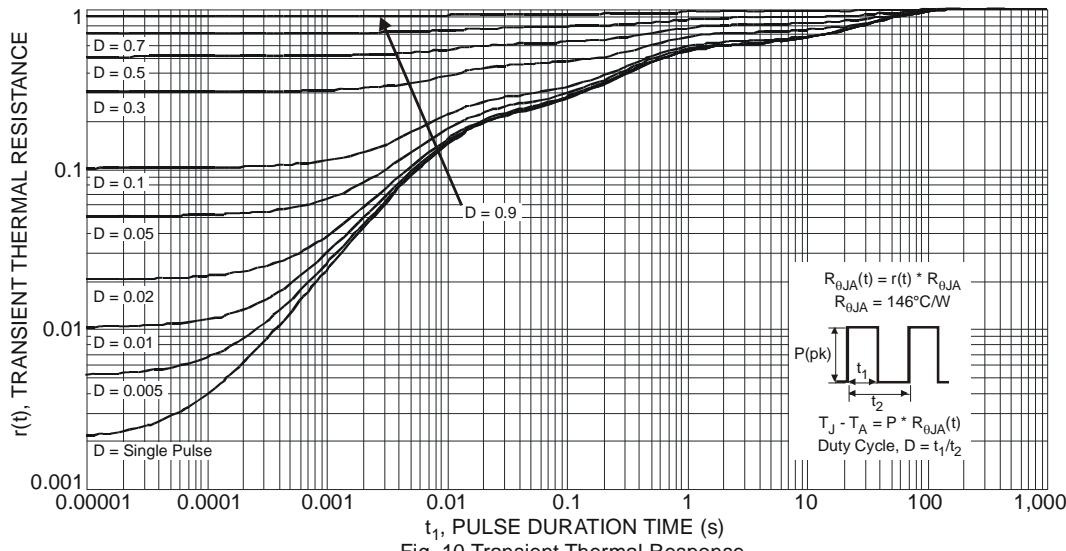
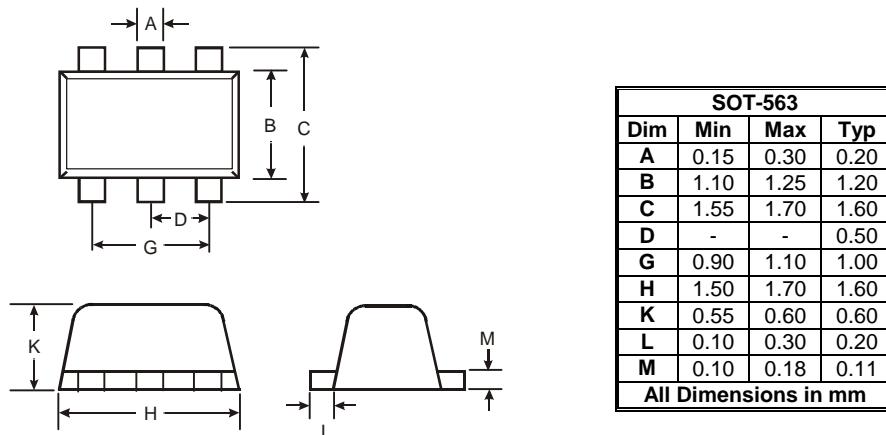


Fig. 10 Transient Thermal Response

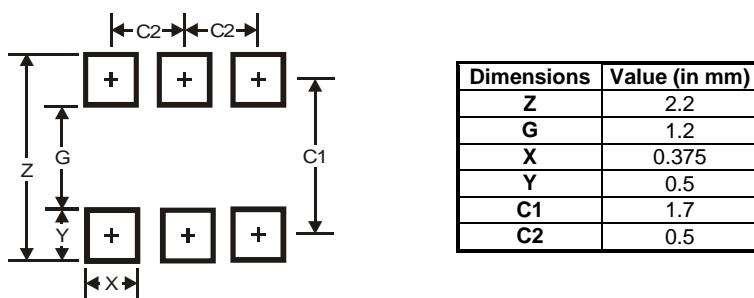
Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for latest version.



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