

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)} \text{ max}$	$I_D \text{ max}$ $T_A = +25^\circ\text{C}$
-20V	150mΩ @ $V_{GS} = -4.5\text{V}$	-1.9A
	200mΩ @ $V_{GS} = -2.5\text{V}$	-1.7A

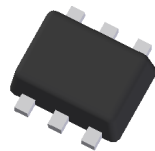
Description

This MOSFET is designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

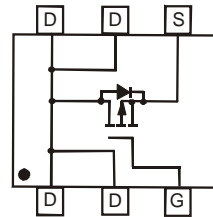
Applications

- Backlighting
- Power Management Functions
- DC-DC Converters
- Motor Control

SOT-563



Top View


 Top View
 Internal Schematic

Features

- Very Low On-Resistance
- Very Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

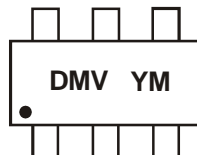
- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2104V-7	SOT-563	3000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



DMV = Marking Code
 YM = Date Code Marking
 Y = Year (ex: T = 2006)
 M = Month (ex: 9 = September)

Date Code Key

Year	2006	2007	...	2014	2015	2016	2017	2018	2019	2020	2021
Code	T	U	...	B	C	D	E	F	G	H	I

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 5) V _{GS} = -4.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	-1.9 -1.5	A
Continuous Drain Current (Note 5) V _{GS} = -4.5V	t ≤ 5s	T _A = +25°C T _A = +70°C	I _D	-2.1 -1.65	A
Continuous Drain Current (Note 5) V _{GS} = -2.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	-1.7 -1.3	A
Pulsed Drain Current	t _p = 10μs		I _{DM}	-4.0	A

Thermal Characteristics

Characteristic	Symbol	Value	Units
Power Dissipation (Note 5)	P _D	0.85	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	R _{θJA}	146	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current T _J = +25°C T _J = +125°C	I _{DSS}	—	—	-1.0 -5.0	μA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±12V, V _{DS} = 0V
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(th)}	-0.45	—	-1.0	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	92	150	mΩ	V _{GS} = -4.5V, I _D = -950mA
			134	200		V _{GS} = -2.5V, I _D = -670mA
			180	240		V _{GS} = -1.8V, I _D = -200mA
Forward Transconductance	g _{FS}	—	3.1	—	S	V _{DS} = -10V, I _D = -810mA
Diode Forward Voltage (Note 6)	V _{SD}	—	—	-0.9	V	V _{GS} = 0V, I _S = -360mA
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	—	320	—	pF	V _{DS} = -16V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	80	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	60	—	pF	

Notes: 5. Device mounted on FR-4 PCB with 1 inch square pads.
6. Short duration pulse test used to minimize self-heating effect.

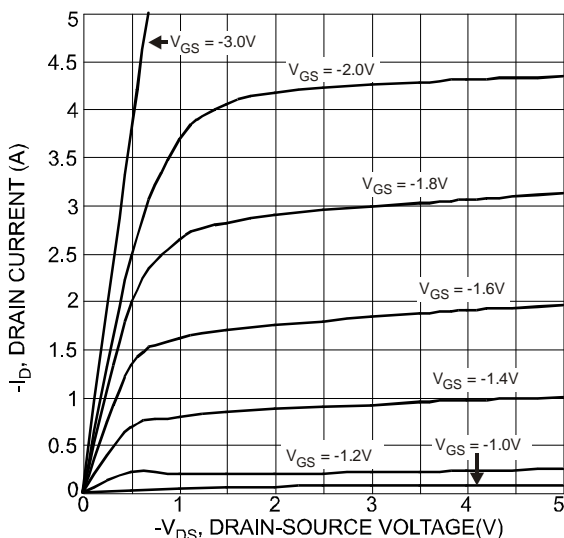


Fig. 1 Typical Output Characteristics

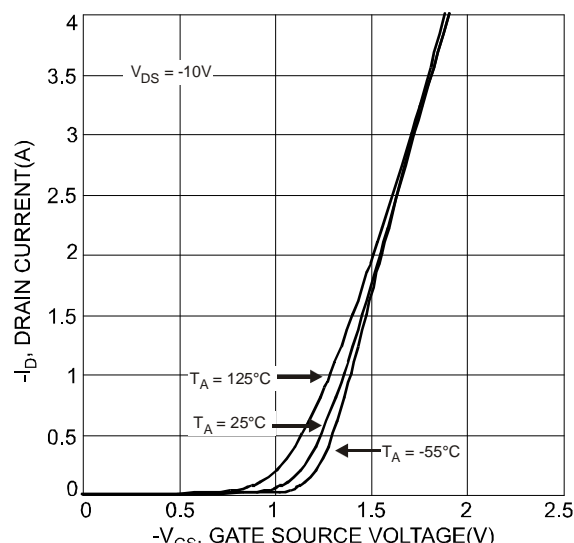


Fig. 2 Typical Transfer Characteristics

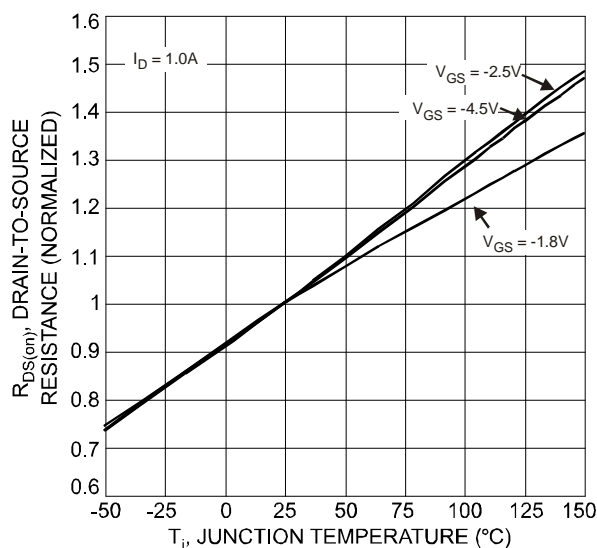


Fig. 3 On-Resistance Variation with Temperature

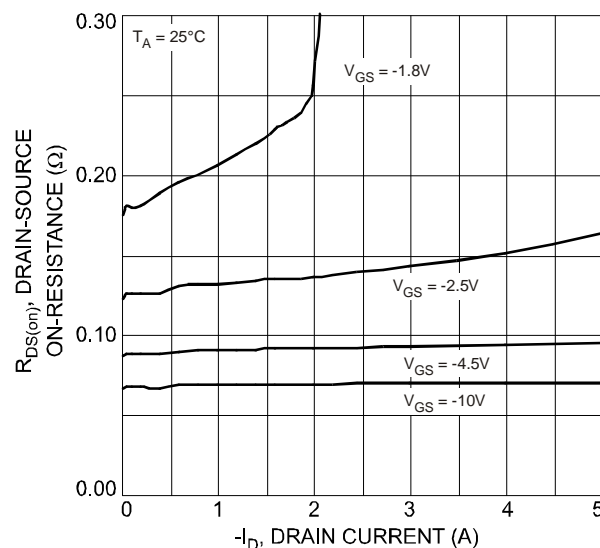


Fig. 4 On-Resistance vs. Drain Current and Gate Voltage

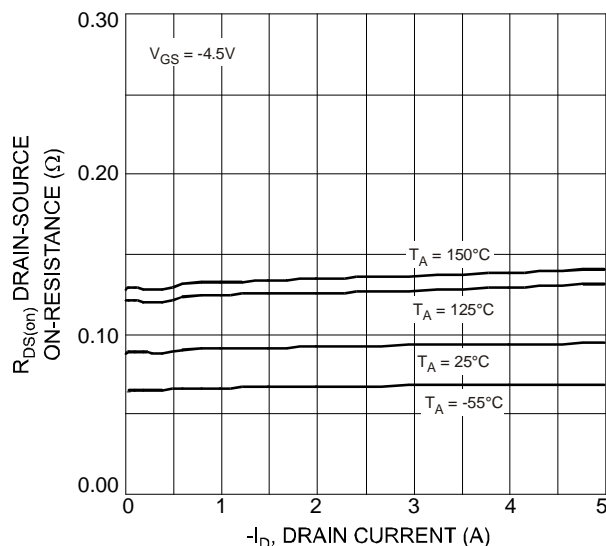


Fig. 5 Drain-Source On-Resistance vs. Drain Current and Temperature

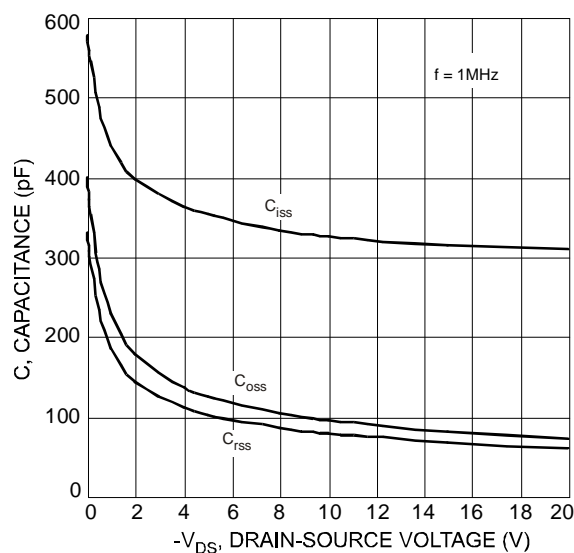


Fig. 6 Typical Capacitance

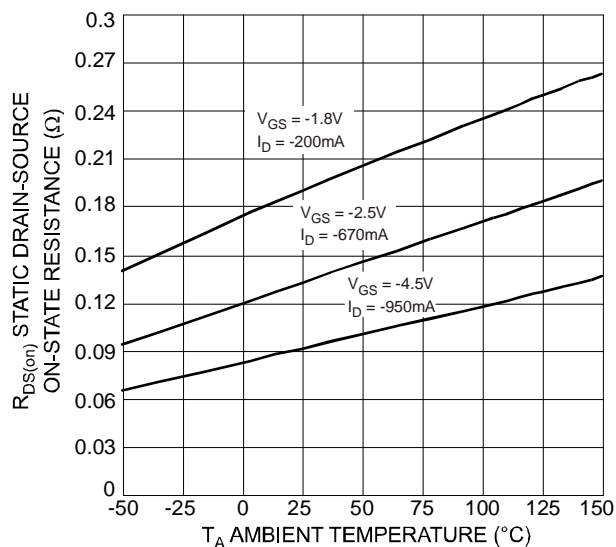


Fig. 7 Static Drain-Source On-State Resistance vs. Ambient Temperature

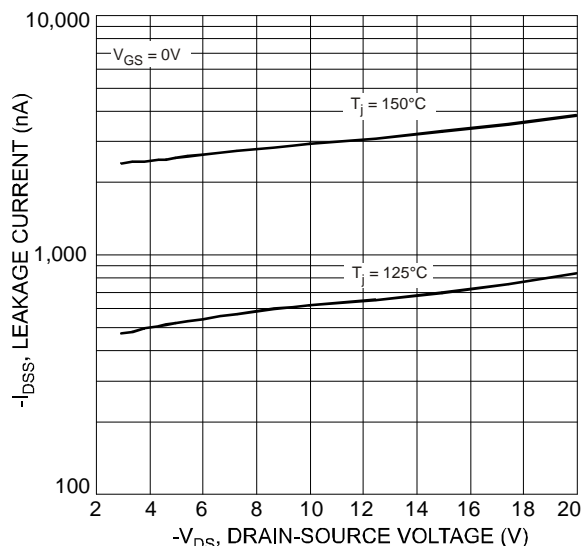


Fig. 8 Drain-Source Leakage Current vs. Voltage

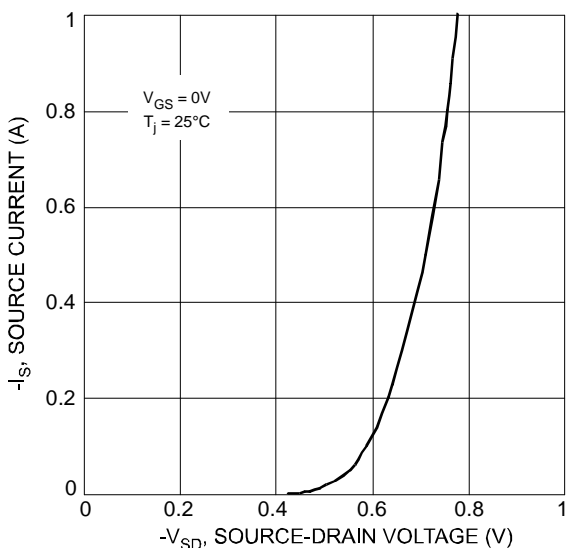


Fig. 9 Diode Forward Voltage vs. Current

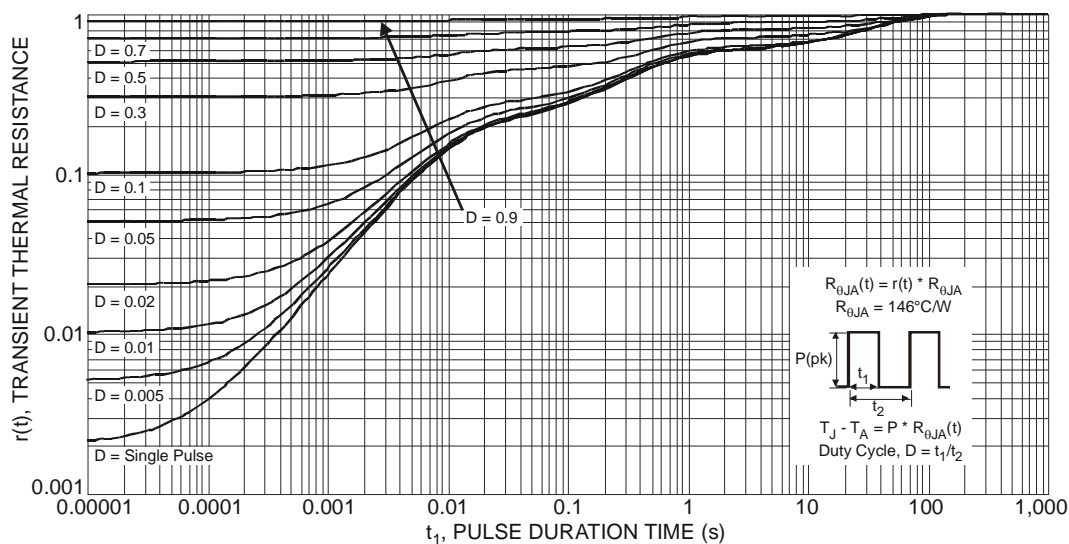
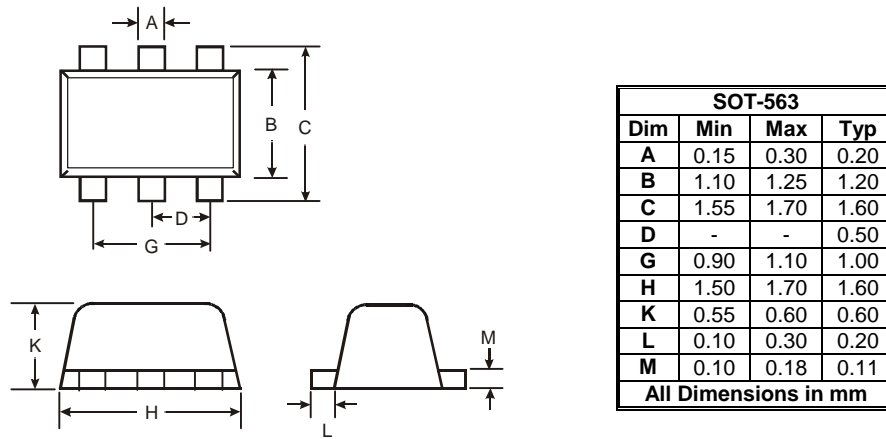


Fig. 10 Transient Thermal Response

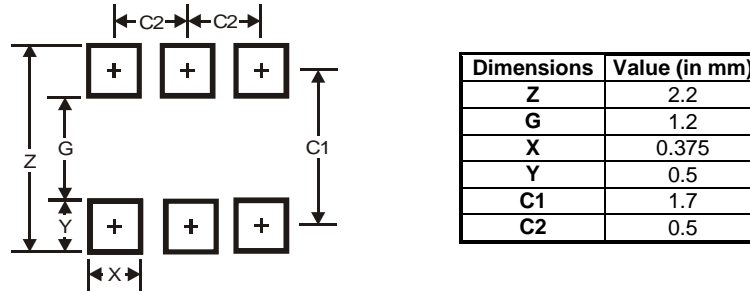
Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for latest version.



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