

EZ-OTG / EZ-Host Development Kit CY3663

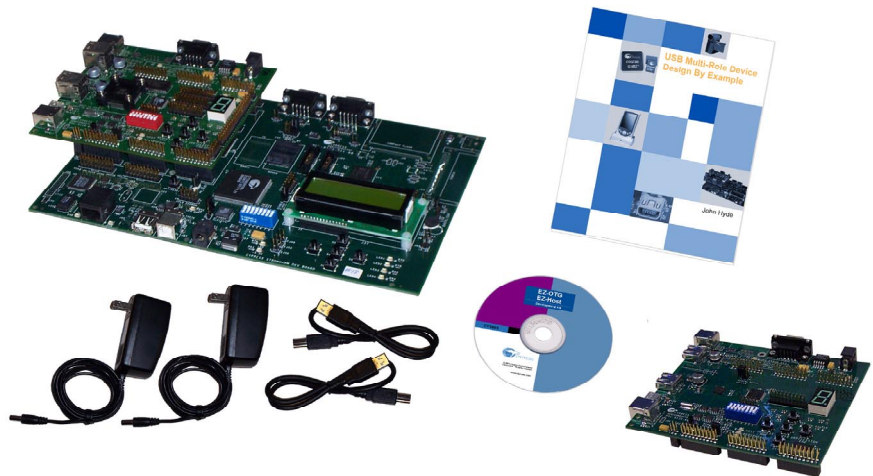
USB Embedded Host and On-The-Go Development Suite

EZ-OTG and EZ-Host provide a powerful architecture for development of embedded host and USB On-The-Go applications. They include a 16-bit embedded RISC microprocessor, 16KB RAM, 8KB BIOS ROM, full OTG support, two Serial Interface Engines, between two to four dual-role USB ports, and a configurable I/O block.

With all of these capabilities comes a need for resources to support design development. You don't want to sacrifice time to market to get the features you want. Cypress understands this and so we put our USB experience to work for you, supplying a robust development kit with ready to go code and design examples – including a compliance tested USB On-The-Go example!

Highlights

- **EZ-OTG and EZ-Host Development Boards**
- **“USB Multi-Role Device Design By Example” book**
- **Co-processor board**
- **Linux OTG drivers**
- **EZ-OTG/EZ-Host Navigator**
 - Tutorial
 - Control Application
- **FrameWorks**
 - Common code for all applications
- **Design Examples**
 - OTG
 - Multi-Port Host
 - Peripheral
 - Simultaneous Host / Peripheral
- **Development Tools**
 - GNUPro environment
 - IBM Eclipse IDE
 - Binary Utilities



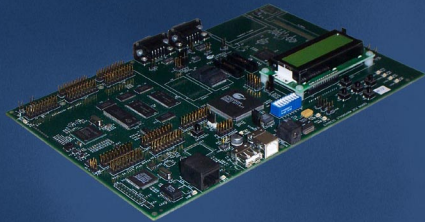
Hardware

At the heart of the development kit are the EZ-OTG and EZ-Host development boards. These boards can be used stand-alone, with the internal processor handling all of the system function, or in a co-processor mode with an external system CPU. The included co-processor board (a StrongArm running the Linux operating system) demonstrates the operation the chips in co-processor mode, using all three of the CPU hardware interfaces: Host Processor Interface (HPI), High Speed Serial (HSS), and Serial Peripheral Interface (SPI).



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The Right Tools for the Job

To help you in the development of your OTG or embedded host product, the CY3663 Development Kit includes a wealth of information from tutorials to technical documents to tested source code.

Documents



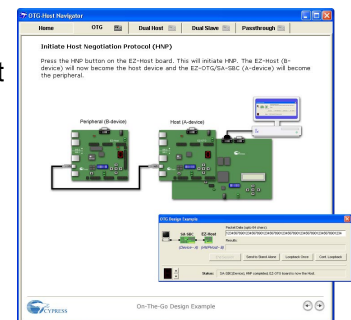
Working with an industry leading author, Cypress brings you "USB Multi-Role Device Design By Example" by John Hyde. Providing a series of increasingly capable examples, this book will guide you through the development of a host or OTG enabled device in record time.

The CY3663 also includes all of the detailed technical documentation that you will need for hardware and software development on the EZ-OTG or EZ-Host. Resources include the Technical Reference Manual, Hardware Design Notes, and User Guides for all components.



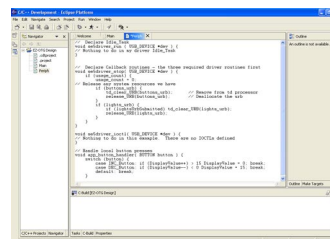
Tutorial and Software

The EZ-OTG / EZ-Host Navigator performs as both a complete tutorial for the kit as well as the control panel to operate the included Design Examples. These Design Examples show functional implementations of the common host usage models and are also ready-to-use code templates. The FrameWorks, common routines for all applications, is the base upon which the Design Examples and your product are built. For supporting the co-processor mode of operation, a complete host/peripheral/OTG stack solution for Linux is also included.



Development Tools

Cypress provides a set of industry standard tools so code development on the EZ-OTG and EZ-Host is a snap. Red Hat has ported their GNU development environment to Windows for the Cypress parts, and the Eclipse Integrated Development Environment is also provided.



- GNUPro Tools
 - C Compiler
 - Assembler
 - Linker
 - Debugger
- Eclipse IDE
- Binary Utilities

Ordering Information

Kit Order Number	Parts Supported
CY3663	EZ-OTG (CY7C67200), EZ-Host (CY7C67300)