TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA4303F

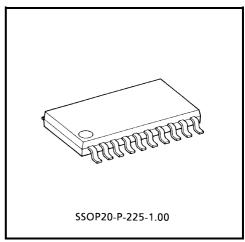
Down Converter for BS/CS Tuner IC

Description

The TA4303F is a monolithic IC to down-convert the L-band (900~2150 MHz) signal for the satellite tuners. It's integrated circuits that perform the mixer/oscillator function. They have double-balanced mixer, local oscillator, IF amplifier, OSC buffer amplifier and prescaler buffer amplifier circuits.

Features

- Single chip full band solution, compatible with digital and analog transmissions.
- Single 5 V power supply operation
- Full band constant convertion Gain and Noise Figure
- Local oscillator output circuit for PLL
- Low Phase Noise local oscillator



Weight: 0.17 g (typ.)

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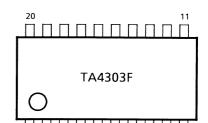
Pin Connection Function Block Diagram

GND (IF AMP) (1 20) GND (OSC Buff) vco IFout (2 19) OSC Buff_RF short V_{CC1} (MIX + IF AMP) (3) 18) GND (OSC) OSC' Buff. 17) OSC_b RFin-1 (4 AMP 16) OSC_e RFin-2 (5 15) OSC_c NC (6 PSC Buff. AMP 14) GND (PSC Buff) NC (7 NC (8 13) V_{CC}2 (OSC Buff + PSC Buff)

12) VCOout

11) GND (MIX)

Top view



Marking

Package: SSOP20-P-225, 1.0mm pitch

Caution

This devices is electrostatic sensitivity.

NC (9

NC (10



Explanation

Pin No.	Pin Name	Pin Voltage	Explanation	Equivalent Circuit
1	GND (IF Amp)	0	GND pin for IF Amp.	_
2	IF out	2.2	IF output pin.	2
3	V _{CC1} (MIX + IF Amp)	5.0	Supply voltage for MIX + IF Amp.	_
4	RFin-1	1.7	RF input pin.	osc
5	RFin-2	1.7	RF input pin.	(4) (5)
6	NC	_	_	-
7	NC	_	_	-
8	NC	_	_	_
9	NC	_	_	_
10	NC	_	_	_
11	GND (MIX)	0	GND pin for MIX.	I
12	VCOout	2.0	OSC output pin.	12)
13	V _{CC2} (OSC Buff + PSC Buff)	5.0	Supply voltage pin for OSC Buff + PSC Buff.	_
14	GND (PSC Buff)	0	GND pin for PSC Buff.	
15	OSC-c	5.0	OSC collector pin, supply voltage pin.	(1)
16	OSC-e	1.8	Emitter pin for OSC.	(16)
17	OSC-b	2.6	Base pin for OSC.	



Pin No.	Pin Name	Pin Voltage	Explanation	Equivalent Circuit		
18	GND (OSC)	0	GND pin for OSC.	ı		
19	RF GND (OSC Buff)	1.4	RF GND pin.	\$ CD XIE		
20	GND (OSC Buff)	0	GND pin for OSC Buff.	_		



Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Supply voltage	V _{CC}	6	V
Total power dissipation	P _D (Note1)	1100	mW
Operating temperature	T _{opr}	-20~85	°C
Storage temperature	T _{stg}	-45~150	°C

Note 1: 100 cm² × 1.6 t (Cu layer area: 36%) on glass epoxy resins.

Recommended Operating Range

Characteristic	Symbol	Rating	Unit
Supply voltage	V _{CC} (Amp)	4.75~5.25	٧
Supply voltage	V _{CC} (OSC)	4.75~5.25	V
Input frequency range	fin	900~2150	MHz
IF Input frequency range	fIF	350~550	MHz

Electrical Characteristics (Ta = 25°C) (Note 2)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Supply current	I _{CC} (Total)	1	V _{CC} = 5 V, No RF input	61	74	87	mA
Conversion gain	Gc	1	fin = 1.6 GHz, fIF = 400 MHz	14	18	25	dB
Noise figure	NF	1	fin = 1.6 GHz, fIF = 400 MHz	_	15	22	dB
Saturation output	P _{O (sat)}	1	fin = 1.6 GHz, fIF = 400 MHz	5	10	_	dBmW
Third-order intercept	IP3	1	fin = 1.596 GHz, 1.6 GHz fIF = 400 MHz, 404 MHz	12	17	24	dBmW
Posc	Posc	1	fLO = 2.0 GHz	-14	-8	_	dBmW

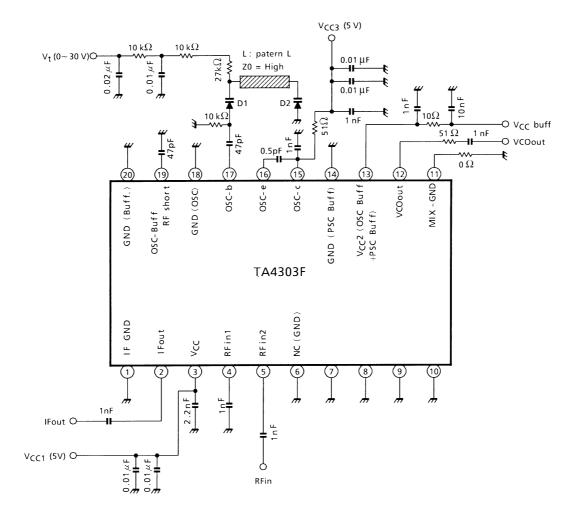
Note 2: All electrical characteristics measured in Supply Voltage 5.0 V (Amp, OSC, OSC Buffer)

Reference Characteristic (Note 3)

Characteristics	Symbol	Test Circuit	Test Condition	Тур.	Unit
Supply current	I _{CC} (Amp)	1	V _{CC} = 5 V, No RF input	36	mA
Supply current	I _{CC} (OSC)	1	V _{CC} = 5 V, No RF input	38	mA

Note 3: All electrical characteristics measured in Supply Voltage 5.0 V (Amp, OSC, OSC Buffer)

Test Circuit



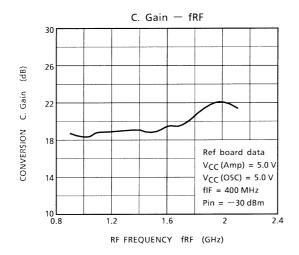
Notice

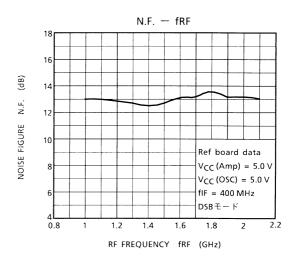
The circuits and measurements contained in this document are given only in the context of as examples of applications for these products.

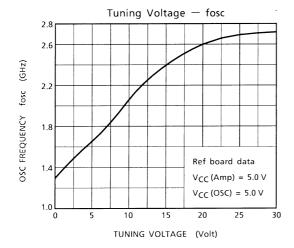
Moreover, these example application circuits are not intended for mass production, since the high-frequency characteristics (the AC characteristics) of these devices will be affected by the external components which the customer uses, by the design of the circuit and by various other conditions.

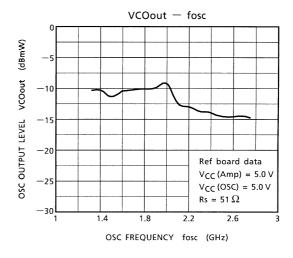
It is the responsibility of the customer to design external circuits which correctly implement the intended application, and to check the characteristics of the design.

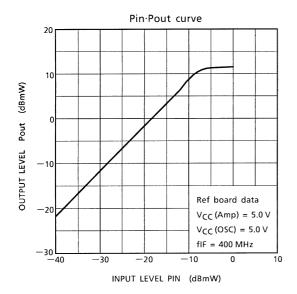
TOSHIBA assume no responsibility for the integrity of customer circuit designs or applications.

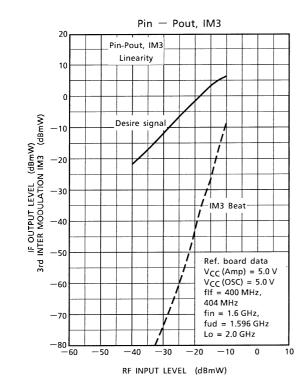










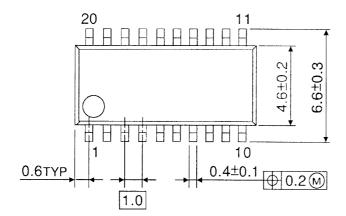


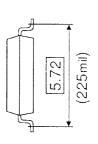
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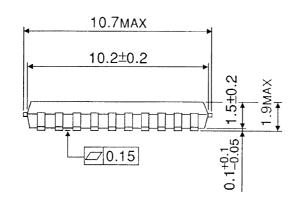


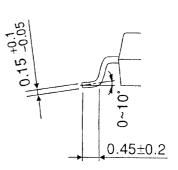
Package Dimensions

SSOP20-P-225-1.00









Weight: 0.17 g (Typ.)