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ADC1112D125F2 ADC1112D125F2 demo board; LVDS/DDR

version;

IDT's dual-channel ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVDS/DDR output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance.



Features

- SMA connector for clock and analog inputs signal
- LVDS/DDR output

Product Options

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Sample & Buy
ADC1112D125F2-DB	Obsolete	BD0	BOARD	0	Ĩ	No	Check Availability

Technical Documentation

	Title	Туре	Format	File Size	Date
User Gui	des & Manuals	<u> </u>			<u></u>
	Demo Board Software ADC_1_r04	Manual - Software	ZIP	2.33 MB	07/19/2012

Related Products

Product ID	Title
ADC1112D125HN	Dual 11-bit ADC; CMOS or LVDS DDR digital outputs

