

# EC2500TS-1.8432M TR

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## REGULATORY COMPLIANCE (Data Sheet downloaded on Dec 2, 2017)


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## ITEM DESCRIPTION

Quartz Crystal Clock Oscillators XO (SPXO) HCMOS/TTL (CMOS) 5.0Vdc 4 Pad 5.0mm x 7.0mm Ceramic Surface Mount (SMD) 1.8432MHz  $\pm 100$ ppm -10°C to +70°C

## ELECTRICAL SPECIFICATIONS

|                                       |                                                                                                                                                                                                                                         |
|---------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Nominal Frequency                     | 1.8432MHz                                                                                                                                                                                                                               |
| Frequency Tolerance/Stability         | $\pm 100$ ppm Maximum (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, Shock, and Vibration) |
| Aging at 25°C                         | $\pm 5$ ppm/year Maximum                                                                                                                                                                                                                |
| Operating Temperature Range           | -10°C to +70°C                                                                                                                                                                                                                          |
| Supply Voltage                        | 5.0Vdc $\pm 10\%$                                                                                                                                                                                                                       |
| Input Current                         | 10mA Maximum (No Load)                                                                                                                                                                                                                  |
| Output Voltage Logic High (Voh)       | 2.4Vdc Minimum with (LS)TTL Load, Vdd-0.5Vdc Minimum with CMOS Load                                                                                                                                                                     |
| Input Current Logic High (Ioh)        | -4mA                                                                                                                                                                                                                                    |
| Output Voltage Logic Low (Vol)        | 0.4Vdc Maximum with (LS)TTL Load, 0.5Vdc Maximum with CMOS Load                                                                                                                                                                         |
| Input Current Logic Low (Iol)         | 4mA                                                                                                                                                                                                                                     |
| Rise/Fall Time                        | 10nSec Maximum (Measured at 0.4Vdc to 2.4Vdc with (LS)TTL Load; Measured at 10% to 90% of waveform with CMOS Load)                                                                                                                      |
| Duty Cycle                            | 50 $\pm 10$ (%) (Measured at 50% of waveform with CMOS Load or at 1.4Vdc with (LS)TTL Load)                                                                                                                                             |
| Load Drive Capability                 | Low Drive (10LSTTL Load or 30pF CMOS Load Maximum)                                                                                                                                                                                      |
| Output Logic Type                     | CMOS                                                                                                                                                                                                                                    |
| Pin 1 Connection                      | Tri-State (High Impedance)                                                                                                                                                                                                              |
| Tri-State Input Voltage (Vih and Vil) | +2.0Vdc Minimum to enable output, +0.8Vdc Maximum to disable output (High Impedance), No Connect to enable output.                                                                                                                      |
| RMS Phase Jitter                      | 1pSec Maximum (12kHz to 20MHz offset frequency)                                                                                                                                                                                         |
| Start Up Time                         | 10mSec Maximum                                                                                                                                                                                                                          |
| Storage Temperature Range             | -55°C to +125°C                                                                                                                                                                                                                         |

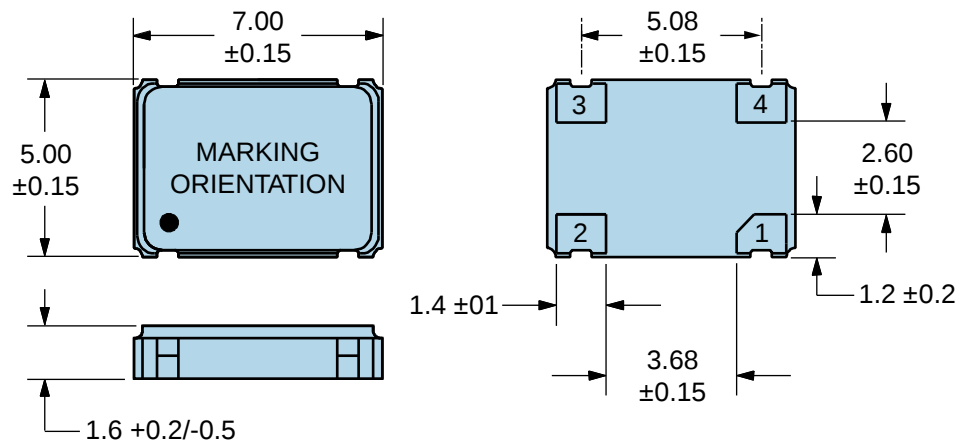
## ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

|                              |                                               |
|------------------------------|-----------------------------------------------|
| ESD Susceptibility           | MIL-STD-883, Method 3015, Class 1, HBM: 1500V |
| Fine Leak Test               | MIL-STD-883, Method 1014, Condition A         |
| Flammability                 | UL94-V0                                       |
| Gross Leak Test              | MIL-STD-883, Method 1014, Condition C         |
| Mechanical Shock             | MIL-STD-883, Method 2002, Condition B         |
| Moisture Resistance          | MIL-STD-883, Method 1004                      |
| Moisture Sensitivity         | J-STD-020, MSL 1                              |
| Resistance to Soldering Heat | MIL-STD-202, Method 210, Condition K          |
| Resistance to Solvents       | MIL-STD-202, Method 215                       |
| Solderability                | MIL-STD-883, Method 2003                      |
| Temperature Cycling          | MIL-STD-883, Method 1010, Condition B         |
| Vibration                    | MIL-STD-883, Method 2007, Condition A         |

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MECHANICAL DIMENSIONS (all dimensions in millimeters)



| PIN | CONNECTION     |
|-----|----------------|
| 1   | Tri-State      |
| 2   | Ground         |
| 3   | Output         |
| 4   | Supply Voltage |

| LINE | MARKING                                               |
|------|-------------------------------------------------------|
| 1    | ECLIPTEK                                              |
| 2    | 1.8432M                                               |
| 3    | XXXXXX<br>XXXXXX=Ecliptek<br>Manufacturing Identifier |

Suggested Solder Pad Layout

All Dimensions in Millimeters



All Tolerances are ±0.1

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OUTPUT WAVEFORM & TIMING DIAGRAM



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Test Circuit for TTL Output

| Output Load Drive Capability | R <sub>L</sub> Value (Ohms) | C <sub>L</sub> Value (pF) |
|------------------------------|-----------------------------|---------------------------|
| 10TTL                        | 390                         | 15                        |
| 5TTL                         | 780                         | 15                        |
| 2TTL                         | 1100                        | 6                         |
| 10LSTTL                      | 2000                        | 15                        |
| 1TTL                         | 2200                        | 3                         |

Table 1: R<sub>L</sub> Resistance Value and C<sub>L</sub> Capacitance Value Vs. Output Load Drive Capability



- Note 1: An external 0.1μF low frequency tantalum bypass capacitor in parallel with a 0.01μF high frequency ceramic bypass capacitor close to the package ground and V<sub>DD</sub> pin is required.
- Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.
- Note 3: Capacitance value C<sub>L</sub> includes sum of all probe and fixture capacitance.
- Note 4: Resistance value R<sub>L</sub> is shown in Table 1. See applicable specification sheet for 'Load Drive Capability'.
- Note 5: All diodes are MM74VHC00, MM74VHC14, or equivalent.

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## Test Circuit for CMOS Output



Note 1: An external 0.1µF low frequency tantalum bypass capacitor in parallel with a 0.01µF high frequency ceramic bypass capacitor close to the package ground and  $V_{DD}$  pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.

Note 3: Capacitance value  $C_L$  includes sum of all probe and fixture capacitance.

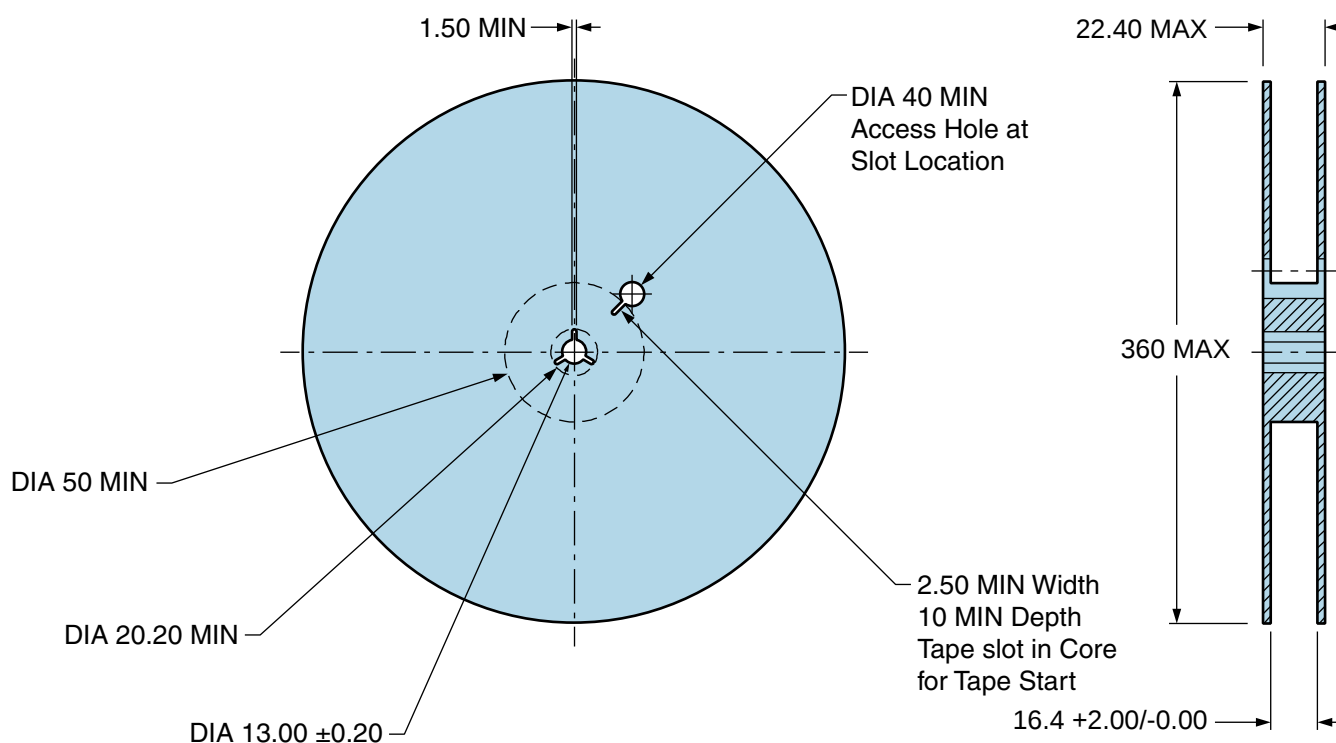
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## Tape & Reel Dimensions

Quantity Per Reel: 1,000 units

All Dimensions in Millimeters

Compliant to EIA-481



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Recommended Solder Reflow Methods



High Temperature Infrared/Convection

|                                     |                                                   |
|-------------------------------------|---------------------------------------------------|
| Ts MAX to TL (Ramp-up Rate)         | 3°C/Second Maximum                                |
| Preheat                             |                                                   |
| - Temperature Minimum (Ts MIN)      | 150°C                                             |
| - Temperature Typical (Ts TYP)      | 175°C                                             |
| - Temperature Maximum (Ts MAX)      | 200°C                                             |
| - Time (ts MIN)                     | 60 - 180 Seconds                                  |
| Ramp-up Rate (TL to TP)             | 3°C/Second Maximum                                |
| Time Maintained Above:              |                                                   |
| - Temperature (TL)                  | 217°C                                             |
| - Time (tL)                         | 60 - 150 Seconds                                  |
| Peak Temperature (TP)               | 260°C Maximum for 10 Seconds Maximum              |
| Target Peak Temperature (TP Target) | 250°C +0/-5°C                                     |
| Time within 5°C of actual peak (tp) | 20 - 40 Seconds                                   |
| Ramp-down Rate                      | 6°C/Second Maximum                                |
| Time 25°C to Peak Temperature (t)   | 8 Minutes Maximum                                 |
| Moisture Sensitivity Level          | Level 1                                           |
| Additional Notes                    | Temperatures shown are applied to body of device. |

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Recommended Solder Reflow Methods



Low Temperature Infrared/Convection 240°C

|                                     |                                                        |
|-------------------------------------|--------------------------------------------------------|
| TS MAX to TL (Ramp-up Rate)         | 5°C/Second Maximum                                     |
| Preheat                             |                                                        |
| - Temperature Minimum (TS MIN)      | N/A                                                    |
| - Temperature Typical (TS TYP)      | 150°C                                                  |
| - Temperature Maximum (TS MAX)      | N/A                                                    |
| - Time (ts MIN)                     | 60 - 120 Seconds                                       |
| Ramp-up Rate (TL to TP)             | 5°C/Second Maximum                                     |
| Time Maintained Above:              |                                                        |
| - Temperature (TL)                  | 150°C                                                  |
| - Time (tL)                         | 200 Seconds Maximum                                    |
| Peak Temperature (TP)               | 240°C Maximum                                          |
| Target Peak Temperature (TP Target) | 240°C Maximum 2 Times / 230°C Maximum 1 Time           |
| Time within 5°C of actual peak (tP) | 10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time |
| Ramp-down Rate                      | 5°C/Second Maximum                                     |
| Time 25°C to Peak Temperature (t)   | N/A                                                    |
| Moisture Sensitivity Level          | Level 1                                                |
| Additional Notes                    | Temperatures shown are applied to body of device.      |

Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)

High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)