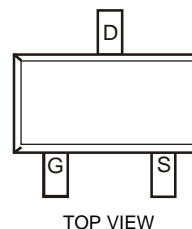
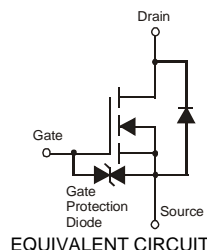


## Features

- Low On-Resistance:
  - 3.0  $\Omega$  @ 4.5V
  - 4.0  $\Omega$  @ 2.5V
  - 6.0  $\Omega$  @ 1.8V
  - 10  $\Omega$  @ 1.5V
- Very Low Gate Threshold Voltage, 1.0V max
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected Gate
- Lead, Halogen, and Antimony Free By Design/RoHS Compliant (Note 2)**
- "Green" Device (Note 3)**
- Qualified to AEC-Q101 Standards for High Reliability**

## Mechanical Data

- Case: SOT-523
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.002 grams (approximate)



## Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V <sub>DSS</sub>	20	V
Gate-Source Voltage	V <sub>GSS</sub>	±10	V
Drain Current (Note 1)	I <sub>D</sub>	230	mA
Pulsed Drain Current T <sub>P</sub> = 10μs	I <sub>DM</sub>	805	mA

## Thermal Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Total Power Dissipation (Note 1)	P <sub>D</sub>	300	mW
Thermal Resistance, Junction to Ambient (Note 1)	R <sub>θJA</sub>	417	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

- Notes:
- Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.
  - No purposefully added lead.
  - Diodes Inc.'s "Green" policy can be found on our website at [http://www.diodes.com/products/lead\\_free/index.php](http://www.diodes.com/products/lead_free/index.php).

**Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 100μA
Zero Gate Voltage Drain Current @ T <sub>C</sub> = 25°C	I <sub>DSS</sub>	—	—	500	nA	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V
Gate-Body Leakage	I <sub>GSS</sub>	—	—	±1 ±500 ±100	μA nA nA	V <sub>GS</sub> = ±10V, V <sub>DS</sub> = 0V V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V V <sub>GS</sub> = ±5V, V <sub>DS</sub> = 0V
ON CHARACTERISTICS (Note 4)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5	—	1.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>	— — — — —	1.8 2.4 2.9 3.7 5.4	3.0 4.0 6.0 10.0 15.0	Ω	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 100mA V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 50mA V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 20mA V <sub>GS</sub> = 1.5V, I <sub>D</sub> = 10mA V <sub>GS</sub> = 1.2V, I <sub>D</sub> = 1mA
Forward Transconductance	Y <sub>fs</sub>	—	242	—	mS	V <sub>DS</sub> =10V, I <sub>D</sub> = 0.1A
Source-Drain Diode Forward Voltage	V <sub>SD</sub>	0.5	—	1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 115mA
DYNAMIC CHARACTERISTICS						
Input Capacitance	C <sub>iss</sub>	—	14.1	—	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	2.9	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	1.6	—	pF	
SWITCHING CHARACTERISTICS, V <sub>GS</sub> = 4.5V (Note 5)						
Turn-On Delay Time	t <sub>d(on)</sub>	—	3.8	—	ns	V <sub>GS</sub> = 4.5V, V <sub>DD</sub> = 10V I <sub>D</sub> = 200mA, R <sub>G</sub> = 2.0Ω
Rise Time	t <sub>r</sub>	—	7.9	—		
Turn-Off Delay Time	t <sub>d(off)</sub>	—	13.4	—		
Fall Time	t <sub>f</sub>	—	15.2	—		

- Notes: 4. Short duration pulse test used to minimize self-heating effect.  
5. Switching characteristics are independent of operating junction temperature.

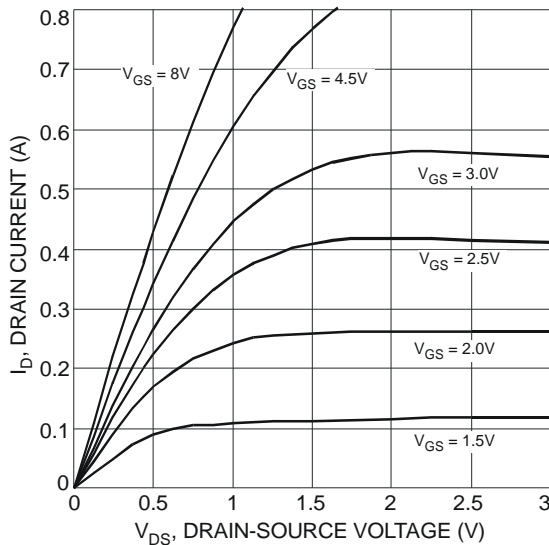


Fig. 1 Typical Output Characteristic

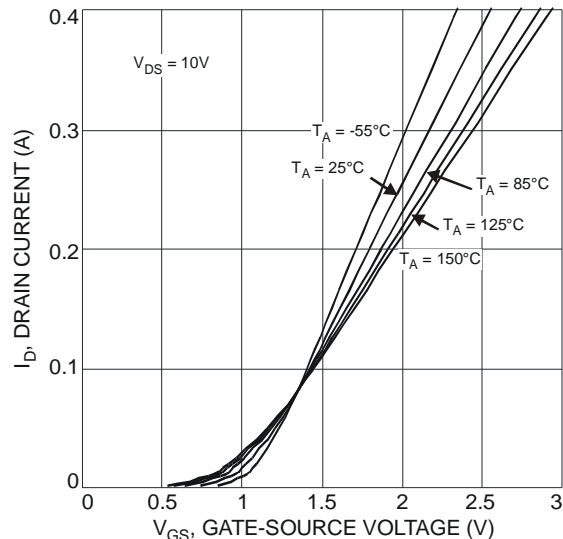


Fig. 2 Typical Transfer Characteristic

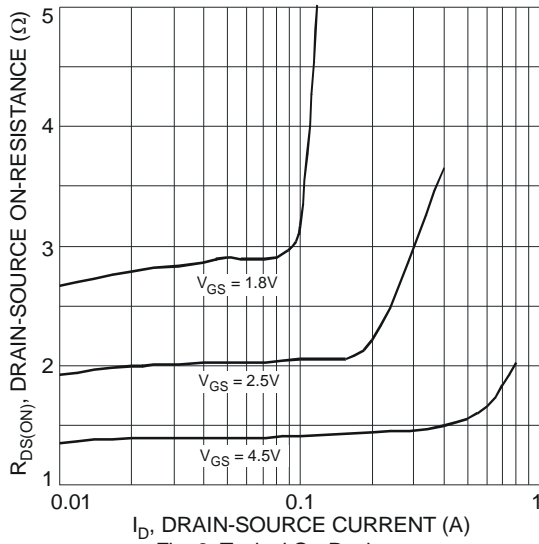


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

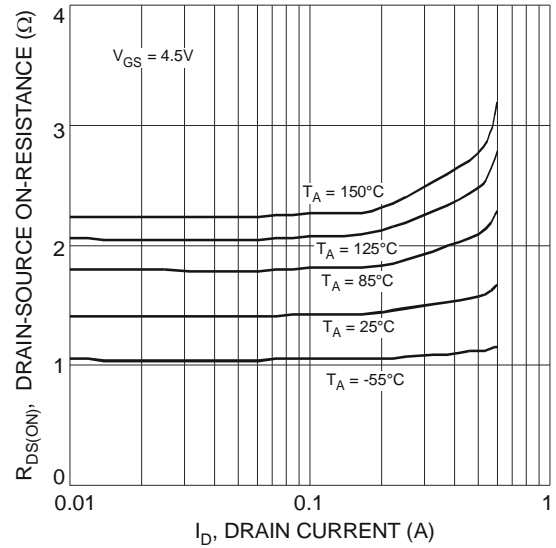


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

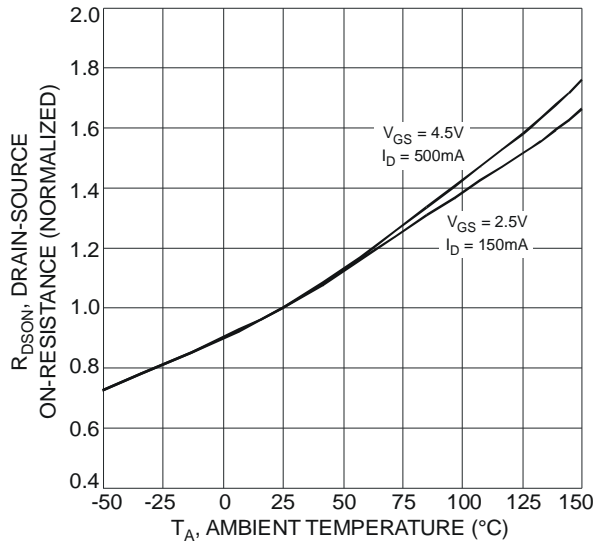


Fig. 5 On-Resistance Variation with Temperature

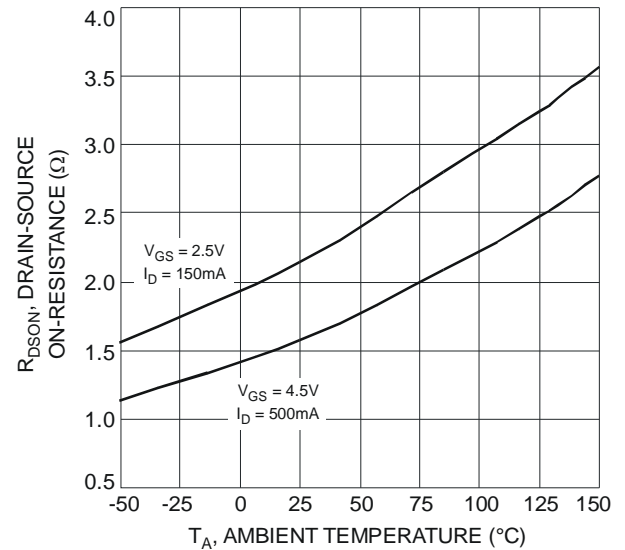


Fig. 6 On-Resistance Variation with Temperature

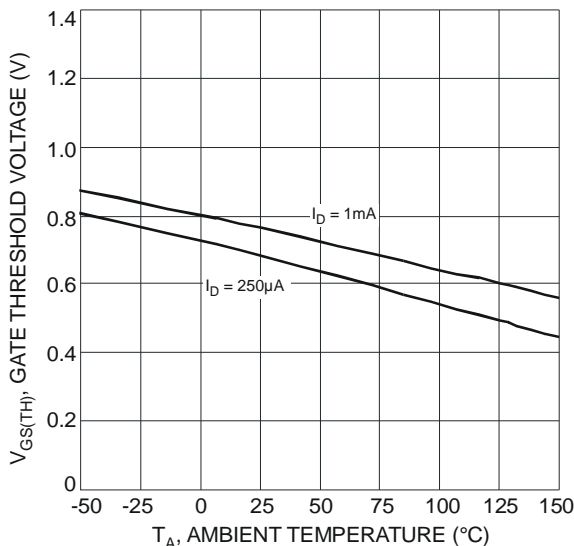


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

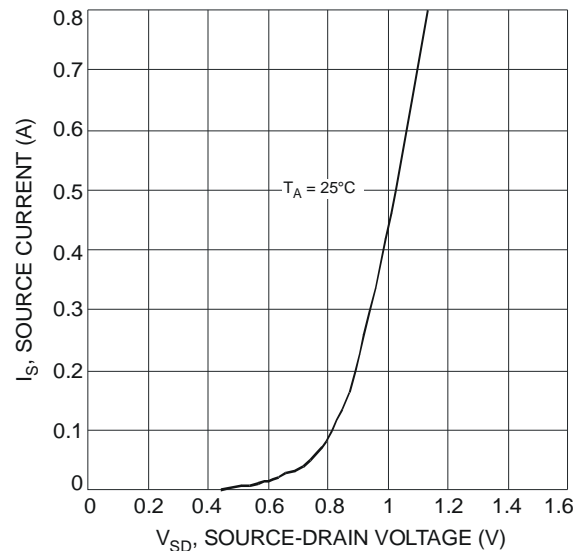
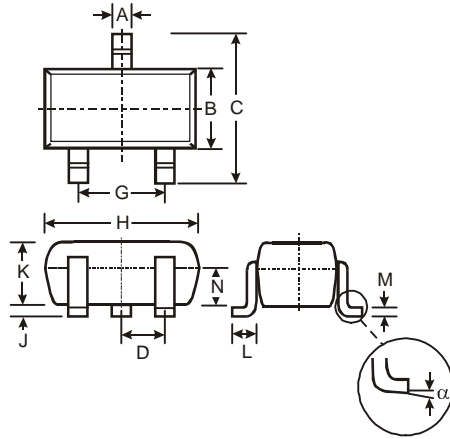


Fig. 8 Diode Forward Voltage vs. Current

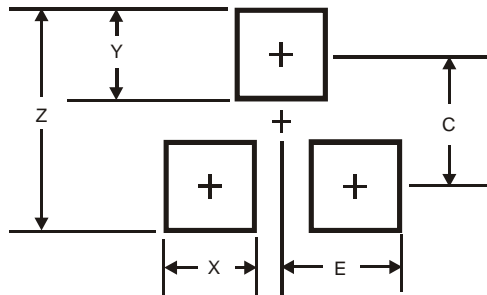


## Package Outline Dimensions



SOT-523			
Dim	Min	Max	Typ
A	0.15	0.30	0.22
B	0.75	0.85	0.80
C	1.45	1.75	1.60
D	—	—	0.50
G	0.90	1.10	1.00
H	1.50	1.70	1.60
J	0.00	0.10	0.05
K	0.60	0.80	0.75
L	0.10	0.30	0.22
M	0.10	0.20	0.12
N	0.45	0.65	0.50
α	0°	8°	—
All Dimensions in mm			

## Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.8
X	0.4
Y	0.51
C	1.3
E	0.7

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