

To all our customers

Regarding the change of names mentioned in the document, such as Mitsubishi Electric and Mitsubishi XX, to Renesas Technology Corp.

The semiconductor operations of Hitachi and Mitsubishi Electric were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Mitsubishi Electric, Mitsubishi Electric Corporation, Mitsubishi Semiconductors, and other Mitsubishi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Note : Mitsubishi Electric will continue the business operations of high frequency & optical devices and power devices.

Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

PRELIMINARY
 Notice: This is not a final specification.
 Some parametric limits are subject to change.

MITSUBISHI Pch POWER MOSFET

FX20ASJ-2

HIGH-SPEED SWITCHING USE

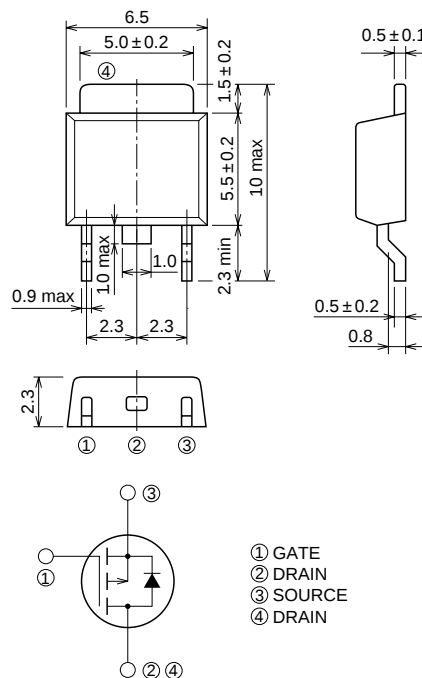
FX20ASJ-2



- 4V DRIVE
- V_{DS} -100V
- $r_{DS(ON)}$ (MAX) 0.26Ω
- I_D -20A
- Integrated Fast Recovery Diode (TYP.) 100ns

OUTLINE DRAWING

Dimensions in mm



MP-3

APPLICATION

Motor control, Lamp control, Solenoid control
 DC-DC converter, etc.

MAXIMUM RATINGS (Tc = 25°C)

Symbol	Parameter	Conditions	Ratings	Unit
V_{DS}	Drain-source voltage	$V_{GS} = 0V$	-100	V
V_{GSS}	Gate-source voltage	$V_{DS} = 0V$	± 20	V
I_D	Drain current		-20	A
I_{DM}	Drain current (Pulsed)		-80	A
I_{DA}	Avalanche drain current (Pulsed)	$L = 50\mu H$	-20	A
I_S	Source current		-20	A
I_{SM}	Source current (Pulsed)		-80	A
P_D	Maximum power dissipation		35	W
T_{ch}	Channel temperature		-55 ~ +150	°C
T_{stg}	Storage temperature		-55 ~ +150	°C
—	Weight	Typical value	0.26	g

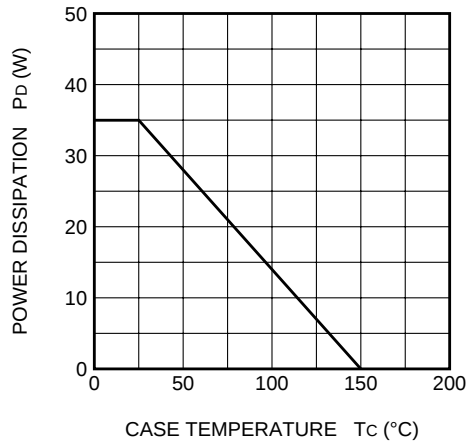
Jan.1999

ELECTRICAL CHARACTERISTICS (Tch = 25°C)

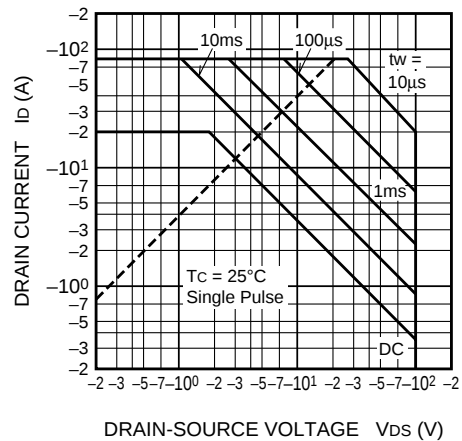
Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V (BR) DSS	Drain-source breakdown voltage	$I_D = -1\text{mA}$, $V_{GS} = 0\text{V}$	-100	—	—	V
I _{GSS}	Gate-source leakage current	$V_{GS} = \pm 20\text{V}$, $V_{DS} = 0\text{V}$	—	—	± 0.1	μA
I _{DSS}	Drain-source leakage current	$V_{DS} = -100\text{V}$, $V_{GS} = 0\text{V}$	—	—	-0.1	mA
V _{GS} (th)	Gate-source threshold voltage	$I_D = -1\text{mA}$, $V_{DS} = -10\text{V}$	-1.0	-1.5	-2.0	V
r _{DS} (ON)	Drain-source on-state resistance	$I_D = -10\text{A}$, $V_{GS} = -10\text{V}$	—	0.20	0.26	Ω
r _{DS} (ON)	Drain-source on-state resistance	$I_D = -10\text{A}$, $V_{GS} = -4\text{V}$	—	0.25	0.32	Ω
V _{DS} (ON)	Drain-source on-state voltage	$I_D = -10\text{A}$, $V_{GS} = -10\text{V}$	—	-2.0	-2.6	V
y _{fs}	Forward transfer admittance	$I_D = -10\text{A}$, $V_{DS} = -10\text{V}$	—	10.3	—	S
C _{iss}	Input capacitance	$V_{DS} = -10\text{V}$, $V_{GS} = 0\text{V}$, $f = 1\text{MHz}$	—	2360	—	pF
C _{oss}	Output capacitance		—	198	—	pF
C _{rss}	Reverse transfer capacitance		—	99	—	pF
t _d (on)	Turn-on delay time	$V_{DD} = -50\text{V}$, $I_D = -10\text{A}$, $V_{GS} = -10\text{V}$, $R_{GEN} = R_{GS} = 50\Omega$	—	13	—	ns
t _r	Rise time		—	30	—	ns
t _d (off)	Turn-off delay time		—	139	—	ns
t _f	Fall time		—	74	—	ns
V _{SD}	Source-drain voltage	$I_S = -10\text{A}$, $V_{GS} = 0\text{V}$	—	-1.0	-1.5	V
R _{th} (ch-c)	Thermal resistance	Channel to case	—	—	3.57	°C/W
t _{rr}	Reverse recovery time	$I_S = -20\text{A}$, $\text{dis}/\text{dt} = 100\text{A}/\mu\text{s}$	—	100	—	ns

PERFORMANCE CURVES

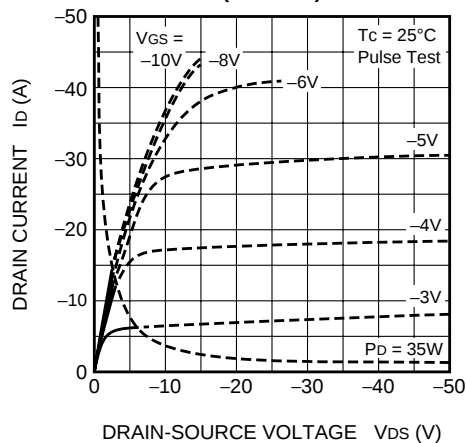
POWER DISSIPATION DERATING CURVE



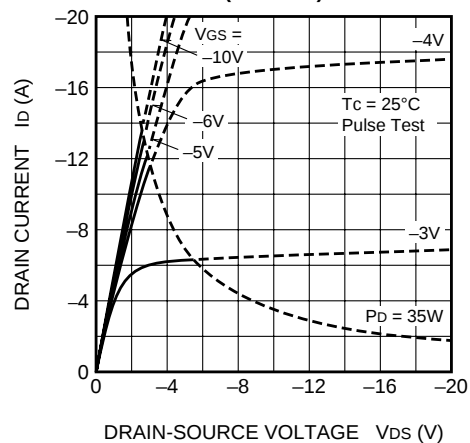
MAXIMUM SAFE OPERATING AREA



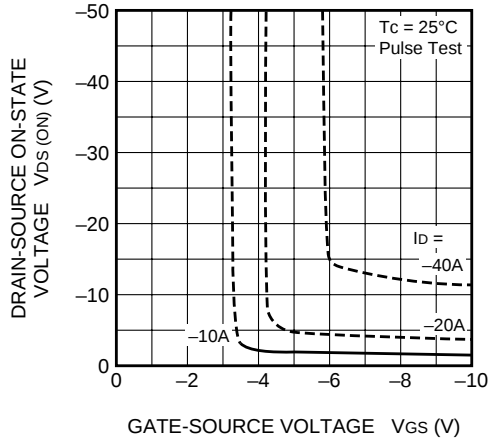
OUTPUT CHARACTERISTICS (TYPICAL)



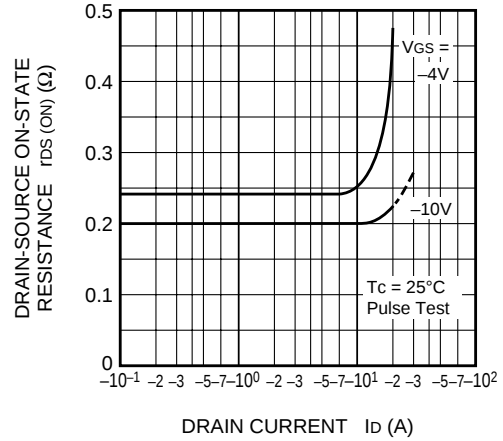
OUTPUT CHARACTERISTICS (TYPICAL)



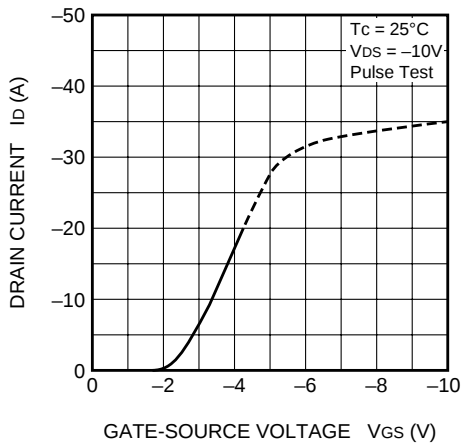
**ON-STATE VOLTAGE VS.
GATE-SOURCE VOLTAGE
(TYPICAL)**



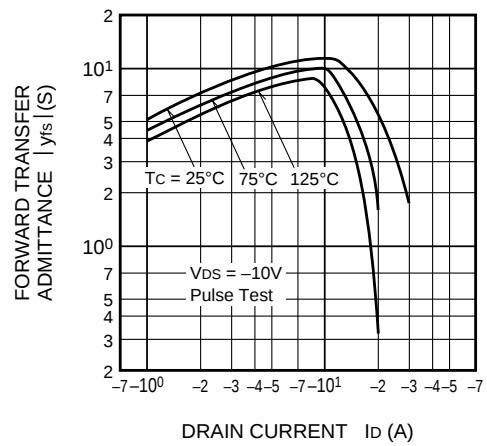
**ON-STATE RESISTANCE VS.
DRAIN CURRENT
(TYPICAL)**



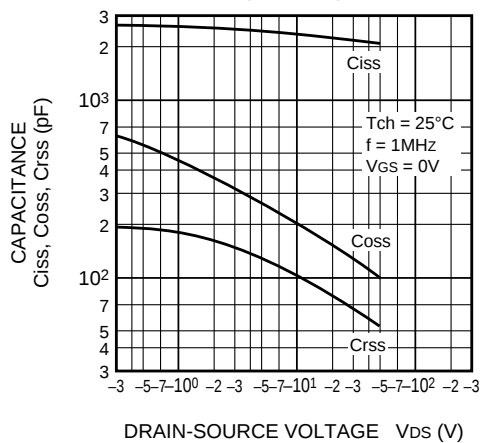
**TRANSFER CHARACTERISTICS
(TYPICAL)**



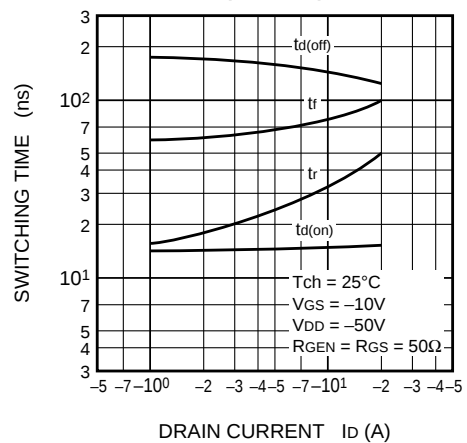
**FORWARD TRANSFER ADMITTANCE
VS. DRAIN CURRENT
(TYPICAL)**



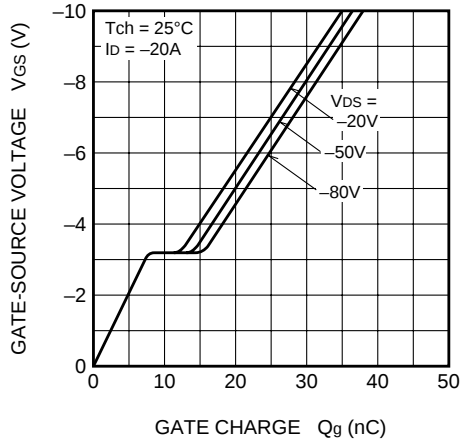
**CAPACITANCE VS.
DRAIN-SOURCE VOLTAGE
(TYPICAL)**



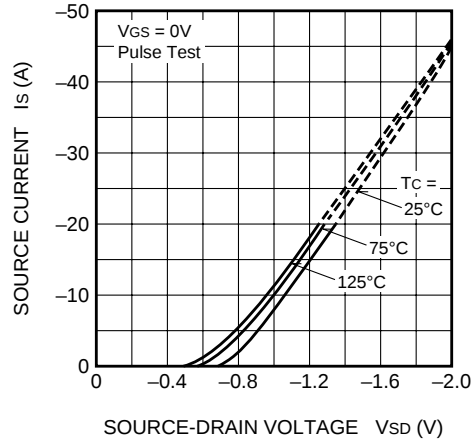
**SWITCHING CHARACTERISTICS
(TYPICAL)**



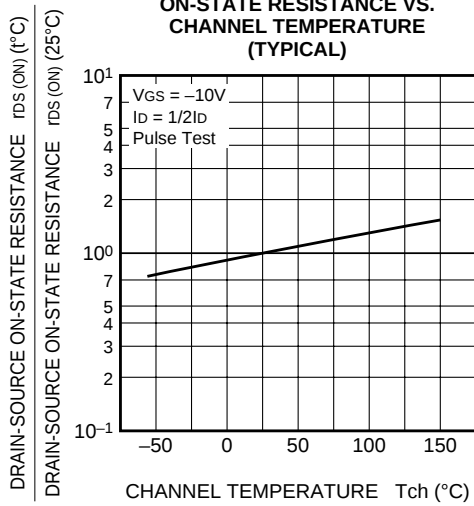
**GATE-SOURCE VOLTAGE
 VS. GATE CHARGE
 (TYPICAL)**



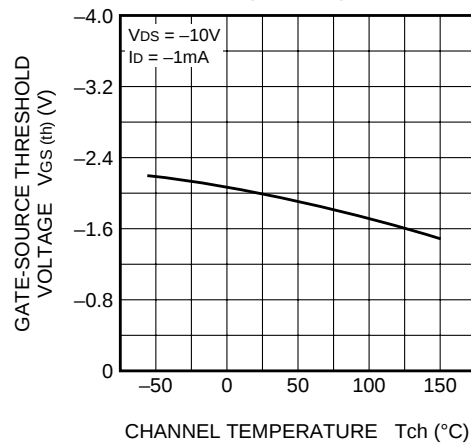
**SOURCE-DRAIN DIODE
 FORWARD CHARACTERISTICS
 (TYPICAL)**



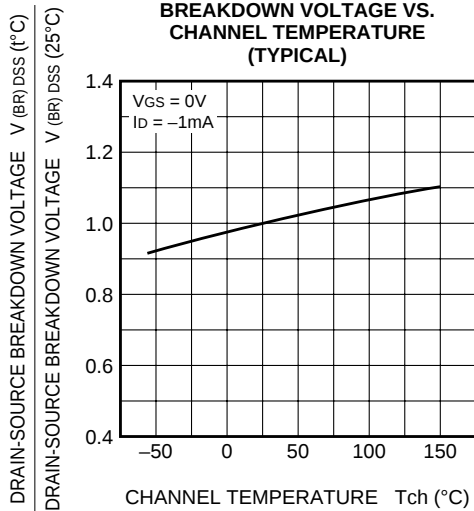
**ON-STATE RESISTANCE VS.
 CHANNEL TEMPERATURE
 (TYPICAL)**



**THRESHOLD VOLTAGE VS.
 CHANNEL TEMPERATURE
 (TYPICAL)**



**BREAKDOWN VOLTAGE VS.
 CHANNEL TEMPERATURE
 (TYPICAL)**



**TRANSIENT THERMAL IMPEDANCE
 CHARACTERISTICS**

