10.3MAX

Unit: mm

 ϕ 3.2 ± 0.2

TOSHIBA THYRISTOR SILICON PLANAR TYPE

SF10GZ47, SF10JZ47

MEDIUM POWER CONTROL APPLICATIONS

- Repetitive Peak Off-State Voltage: VDRM = 400V,600V
 Repetitive Peak Reverse Voltage: VRRM = 400V,600V
- Average On-State Current: IT (AV) = 10A
- Isolation Voltage: $V_{Isol} = 1500V AC$

MAXIMUM RATINGS

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Repetitive Peak Off-State Voltage	SF10GZ47	V_{DRM}	400	(\nearrow)	
and Repetitive Peak Reverse Voltage	SF10JZ47	V_{RRM}	600		
Non-Repetitive Peak Reverse Voltage	SF10GZ47	V_{RSM}	500	\ \ \ \	
(Non-Repetitive<5ms, $T_j = 0\sim125^{\circ}C$)	SF10JZ47	V KSM	720	V	
Average On-State Current (Half Sine Waveform Tc = 66°C)		I _{T (AV)}	10	A	
R.M.S. On-State Current		I _{T (RMS)}	16	< <a>A	
Peak One Cycle Surge On-State Current (Non-Repetitive)			160 (50Hz)	A	
		ITSM	176 (60Hz)		
I ² t Limit Value		(I ² t \	125	A^2 s	
Critical Rate of Rise of On-State Current (Note 1)		di / dt	100	ALus	
Peak Gate Power Dissipation		P _{GM}	5	\rightarrow_{W}	
Average Gate Power Dissipation		P _G (AV)	(0.5/))	W	
Peak Forward Gate Voltage		V _{FGM}	10	V	
Peak Reverse Gate Voltage		V _{RGM}	-5	V	
Peak Forward Gate Current		I _{GM}	2	Α	
Junction Temperature		<i>F</i> 1>	-40~125	°C	
Storage Temperature Range		Tstg	-40~125	°C	
Isolation Voltage (AC, t = 1min.)		VIsol	1500	V	

2.54±0.25

1. CATHODE
2. ANODE
3. GATE

JEDEC —

JEITA —

TOSHIBA 13-10H1B

Weight: 1.7 g (typ.)

Note 1: di / dt test condition

V_{DRM} = 0.5 × Rated

1<u>7M</u>≤ 30A

tgw≥ 10µs

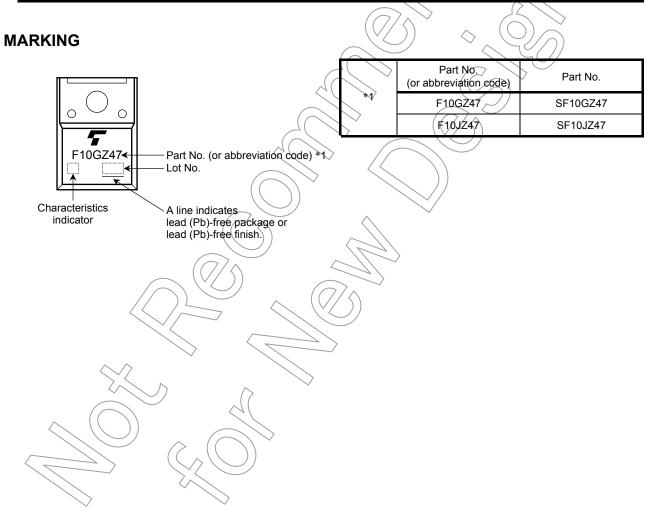
t_{gr} ≤ 250ns

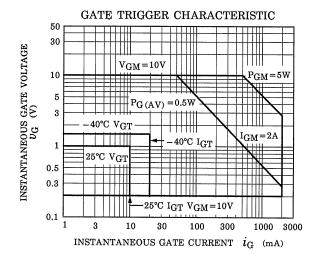
 $i_{gp} = I_{GT} \times 2.0$

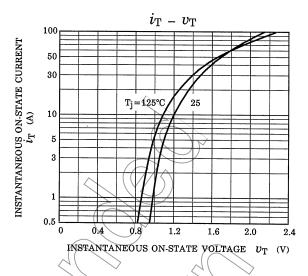


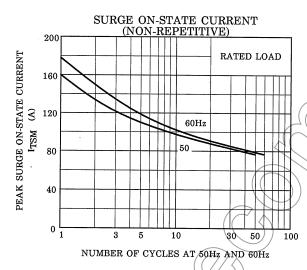
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

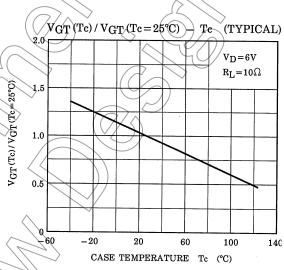
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Repetitive Peak Off-State Current and Repetitive Peak Reverse Current	I _{DRM} I _{RRM}	V _{DRM} = V _{RRM} = Rated	_	_	10	μΑ
Peak On-State Voltage	V _{TM}	I _{TM} = 30A	_	_	1.5	V
Gate Trigger Voltage	V_{GT}	$V_D = 6V, R_1 = 10\Omega$	Á	_	1.0	٧
Gate Trigger Current	I _{GT}	VD - 0V, NL - 1012	(-)	4	10	mA
Gate Non-Trigger Voltage	$V_{\sf GD}$	V _D = Rated × 2 / 3, Tc = 125°C	0.2) <u> </u>	_	V
Critical Rate of Rise of Off-State Voltage	dv / dt	V _{DRM} = Rated, Tc = 125°C Exponential Rise	$\langle \rangle$	50	_	V / µs
Holding Current	lΗ	V _D = 6V, I _{TM} = 1A		_	40	mA
Latching current	ΙL	V _D = 6V, f = 50Hz, t _{gw} = 50µs i _G = 30mA	_](50	mA
Thermal Resistance	R _{th (j-c)}	Junction to Case	_	4	3.4	°C / W

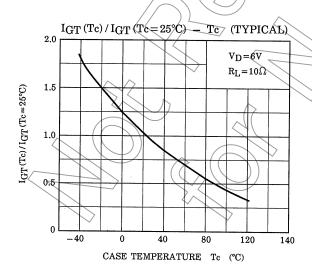


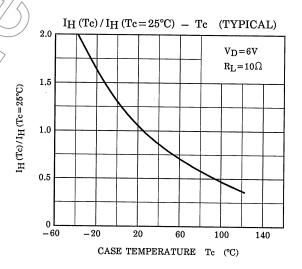




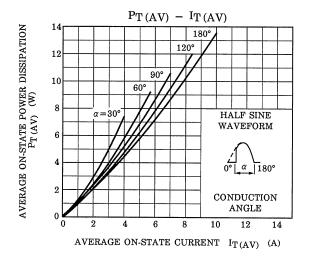


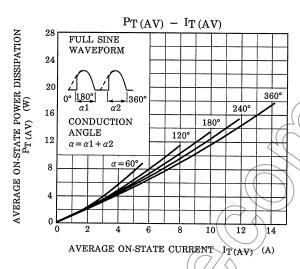


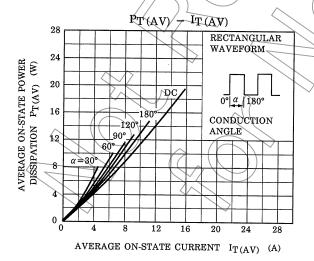


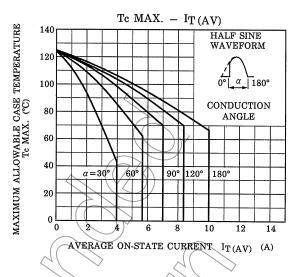


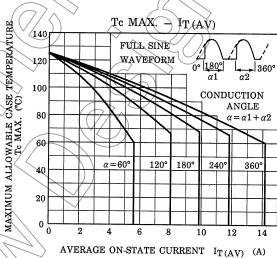
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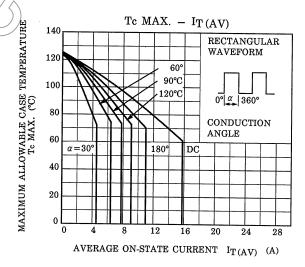




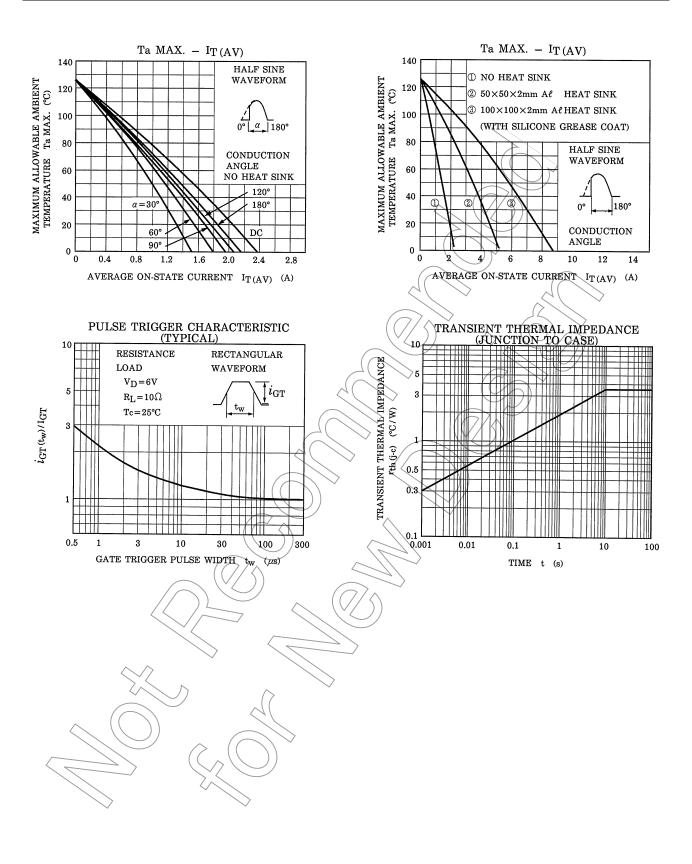








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