

Description

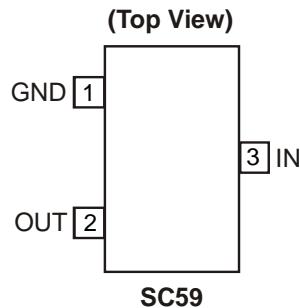
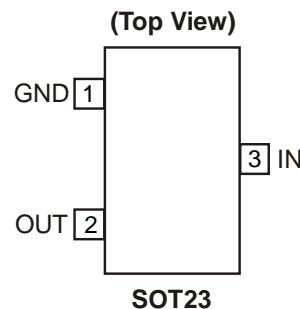
The AP2331TD is a single channel current-limited integrated high-side power switcher optimized for hot-swap applications. The devices have fast short-circuit response time for improved overall system robustness and provide a complete protection solution for application subject to heavy capacitive loads and the prospect of short circuit. It offers reverse-current blocking, over-current, over-temperature and short-circuit protection, as well as controlled rise time and under-voltage lockout functionality.

The device is available in SOT23 and SC59 packages.

Features

- Quick Turn-off of Output Discharge Path at Power Down
- Input Voltage Range: 2.7V to 5.2V
- Fast Short-Circuit Response Time
- 0.4A Accurate Current Limiting
- 250mΩ On-Resistance
- Reverse-Current Blocking
- Built-In Soft-Start with 0.7ms Typical Turn-On Time
- Over-Current Protection
- Over-Voltage Protection
- Short-Circuit and Thermal Protection
- ESD Protection: 3kV HBM, 300V MM
- Ambient Temperature Range: -40°C to +85°C
- Available in "Green" Molding Compound (No Br, Sb)
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- UL Recognized, File Number E322375
- IEC60950-1 CB Scheme Certified

Pin Assignments



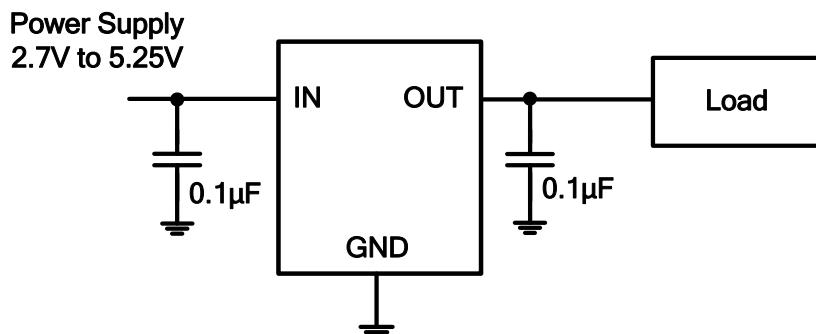
Applications

- LCD TVs & Monitors
- Set-Top Boxes, Residential Gateways
- Laptops, Desktops, Servers
- Printers, Docking Stations, HUBs
- Smart Phones, e-Readers

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Typical Application Circuit



Pin Descriptions

Pin Name	Pin Number	Functions
GND	1	GND
OUT	2	Switch Output Pin
IN	3	Voltage Input Pin

Absolute Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Symbol	Parameter	Ratings	Unit
ESD HBM	Human Body Model ESD Protection	3	kV
ESD MM	Machine Model ESD Protection	300	V
V_{IN}	Input Voltage Relative to GND	6.5	V
V_{OUT}	Output Voltage Relative to GND	$V_{IN} + 0.3$	V
I_{LOAD}	Maximum Continuous Load Current	Internal Limited	A
T_{JMAX}	Maximum Junction Temperature	+150	$^\circ\text{C}$
T_{ST}	Storage Temperature Range (Note 4)	-65 to +150	$^\circ\text{C}$

Note: 4. UL Recognized Rating from -30°C to $+70^\circ\text{C}$ (Diodes Incorporated qualified T_{ST} from -65°C to $+150^\circ\text{C}$).

Recommended Operating Conditions (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
V_{IN}	Input Voltage Relative to GND	2.7	5.2	V
I_{OUT}	Output Current	0	0.2	A
T_A	Operating Ambient Temperature	-40	+85	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, $V_{IN} = 5\text{V}$, unless otherwise specified.)

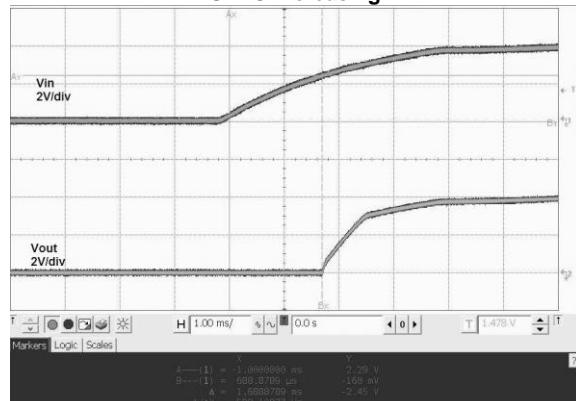
Symbol	Parameter	Test Conditions (Note 5)	Min	Typ	Max	Unit
V_{UVLO}	Input UVLO	V_{IN} rising	2.35	—	2.65	V
I_Q	Input Quiescent Current	Above UVLO, $I_{OUT} = 0$	—	85	125	μA
I_{REV}	Reverse Leakage Current	$V_{IN} = 0\text{V}$, $V_{OUT} = 5\text{V}$, I_{REV} at V_{IN}	—	0.01	0.10	μA
$R_{DS(ON)}$	Switch On-resistance	$V_{IN} = 5\text{V}$, $I_{OUT} = 0.2\text{A}$	100	250	350	$\text{m}\Omega$
I_{LIMIT}	Over-load Current Limit	$V_{IN} = 5\text{V}$, $V_{OUT} = 4\text{V}$	0.3	0.4	0.5	A
I_{os}	Short-circuit Current	OUT shorted to ground	0.3	0.4	0.5	A
I_{ROCP}	Reverse-current Trigger Point	$V_{IN} = 5.0\text{V}$, $V_{OUT} = 5.2\text{V}$	—	0.20	0.25	A
t_{TRIG}	Deglitch Time from Reverse Current Trigger to MOSFET Turn Off	(Note 6)	0.5	0.7	1.0	ms
V_{OVP}	Output Over-voltage Trip Point	(Note 7)	5.3	—	5.6	V
t_{OVP}	Debounce Time from Output Over-voltage to MOSFET Turn Off	—	—	15	—	μs
V_{REC}	Recovery after Turn-off from ROCP and OVP	—	—	101%* V_{IN}	—	V
t_{ON}	Output Turn-on Time (Note 8)	$C_L = 0.1\mu\text{F}$, $R_{LOAD} = 20\Omega$ (UVLO to 90% $V_{OUT-NOM}$)	—	0.7	—	ms
T_{SHDN}	Thermal Shutdown Threshold	$V_{IN} = 2.7\text{V}$ to 5.25V	—	+150	—	$^\circ\text{C}$
T_{HYS}	Thermal Shutdown Hysteresis	—	—	+20	—	$^\circ\text{C}$
θ_{JA}	Thermal Resistance Junction-to-Ambient (Note 9)	SOT23	—	215	—	$^\circ\text{C}/\text{W}$
		SC59	—	255	—	$^\circ\text{C}/\text{W}$

Notes:

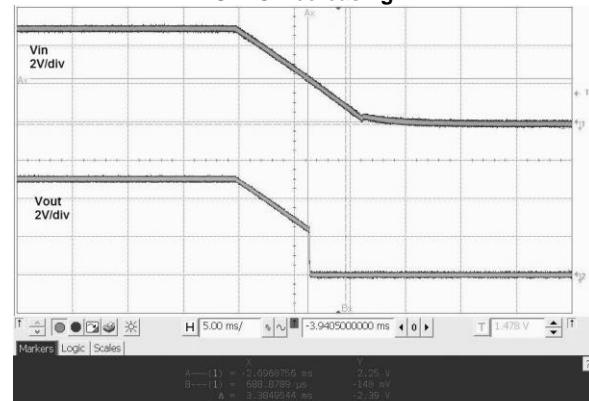
5. Pulse-testing techniques maintain junction temperature close to ambient temperature; thermal effects must be taken into account separately.
6. When reverse current triggers at $I_{ROCP} = 0.20\text{A}$, the reverse current is continuously clamped at I_{ROCP} for 0.7ms deglitch time until MOSFET is turned off.
7. During output over-voltage protection, the output draws approximately $60\mu\text{A}$ current.
8. Since the output turn-on slew rate is dependent on input supply slew rate, this limit is only applicable for input supply slew rate between $V_{IN}/0.2\text{ms}$ to $V_{IN}/1\text{ms}$.
9. Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.

Typical Performance Characteristics

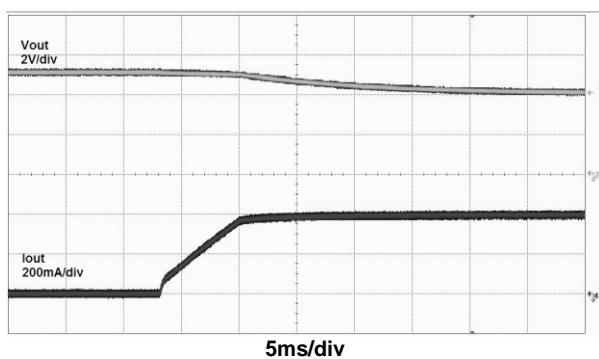
UVLO Increasing



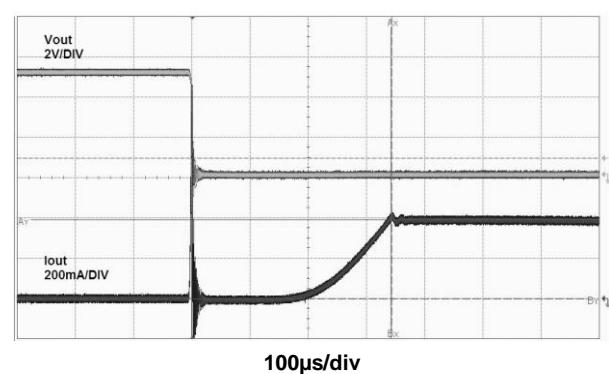
UVLO Decreasing



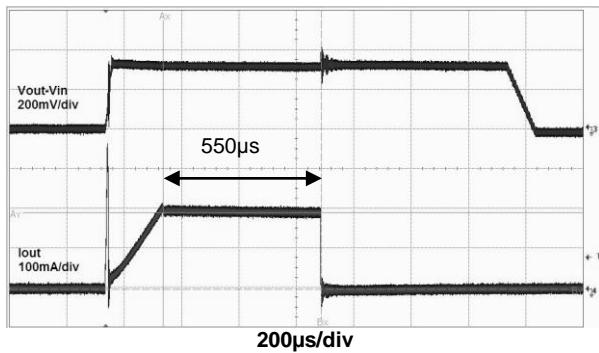
Over-Load Current Limit



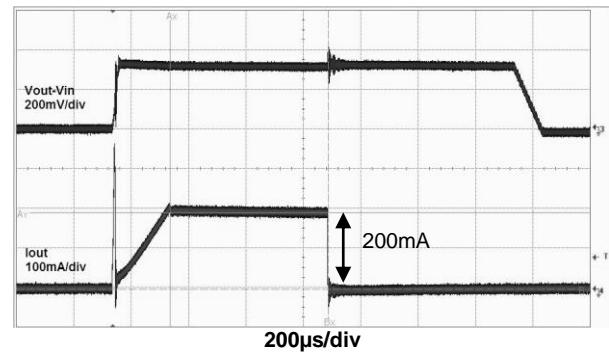
Short-Circuit Current Limit



Deglitch Time from Reverse-Current Trigger to MOSFET Turn-Off

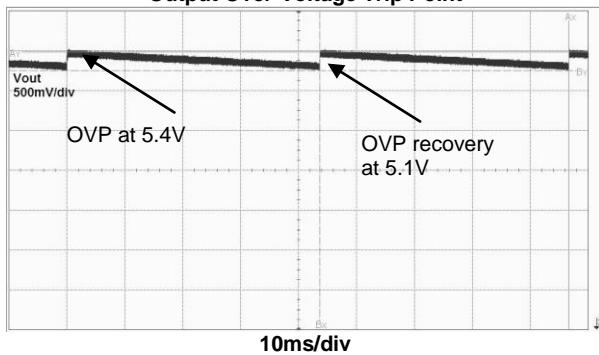


Reverse-Current Limit

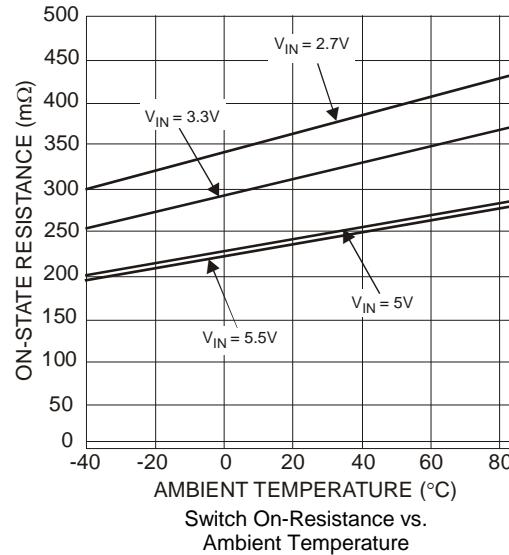
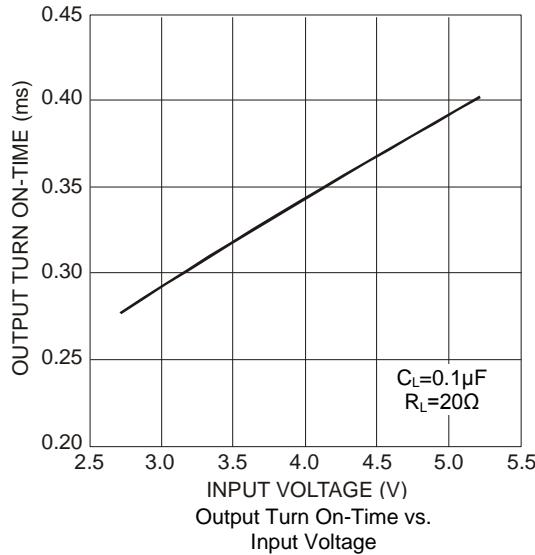
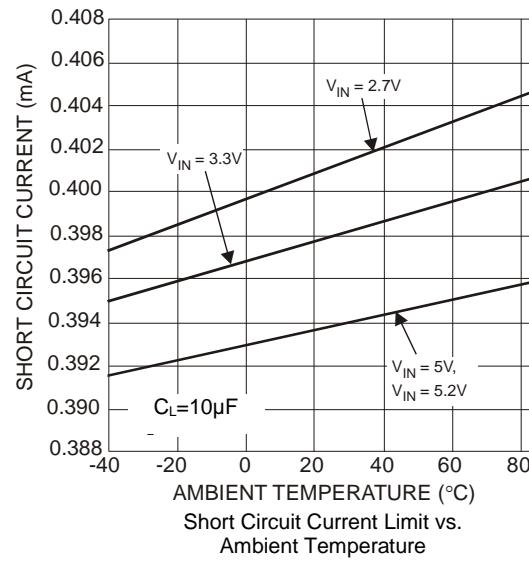
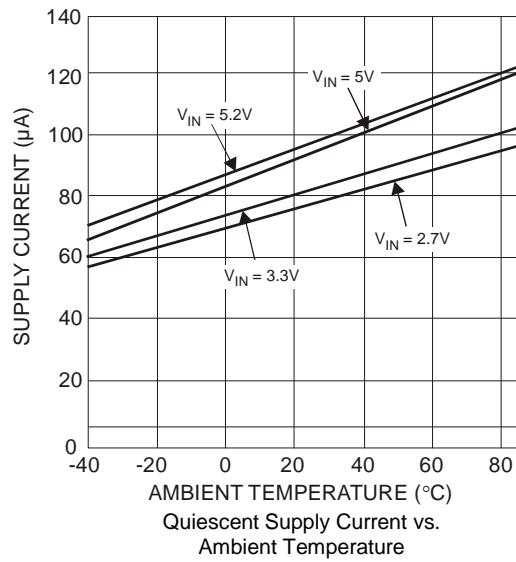
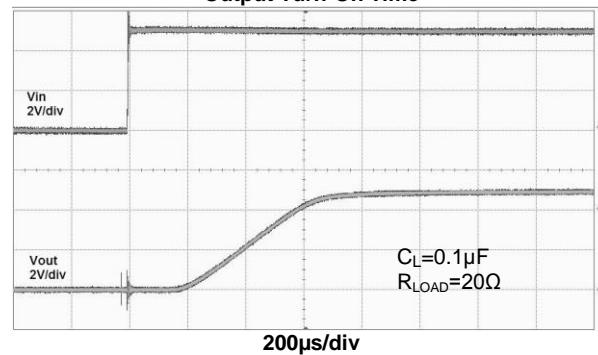


Typical Performance Characteristics (Cont.)

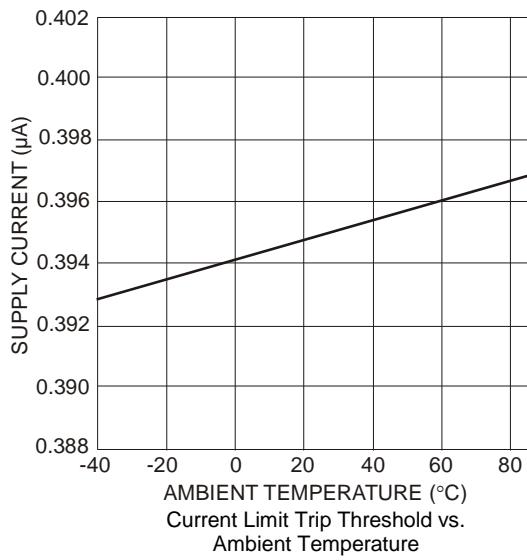
Output Over-Voltage Trip Point



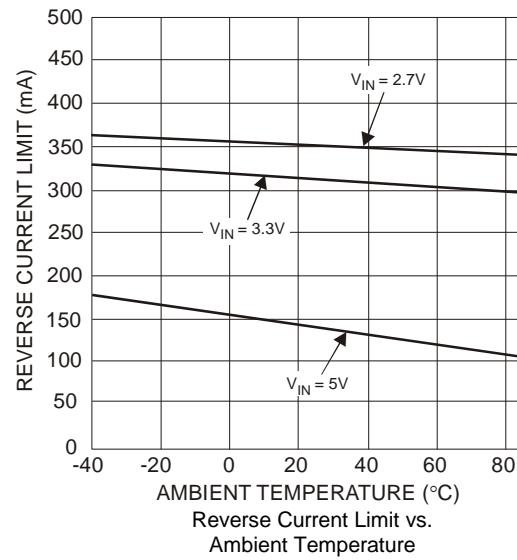
Output Turn-On Time



Typical Performance Characteristics (Cont.)



Current Limit Trip Threshold vs.
Ambient Temperature



Reverse Current Limit vs.
Ambient Temperature

Application Information

Input and Output Capacitors

Minimum 0.1 μ F ceramic bypass capacitor from VIN to GND, located near the device, is strongly recommended to suppress the ringing during short circuit condition. 0.1 μ F close to device output pin is also required to reduce output transient voltage on a short event.

Under-Voltage Lockout (UVLO)

Under-voltage lockout function (UVLO) guarantees that the internal power switch is initially off during start-up. The UVLO functions only when the power supply has reached at least 2.5V (TYP). Whenever the input voltage falls below approximately 2.5V, the power switch is turned off. This facilitates the design of hot-insertion systems where it is not possible to turn off the power switch before input power is removed.

Over-Current and Short-Circuit Protection

An internal sensing FET is employed to check for over current conditions. Unlike current-sense resistors, sense FETs do not increase the series resistance of the current path. When an over current condition is detected, the device maintains a constant output current and reduces the output voltage accordingly. Complete shutdown occurs only if the fault stays long enough to activate thermal limiting.

The different overload conditions and the corresponding response of the AP2331TD are outlined below:

S.NO.	Conditions	Explanation	Behavior of the AP2331TD
1	Short-circuit condition at start-up	Output is shorted before input voltage is applied or before the part is powered up.	The IC senses the short circuit and immediately clamps output current to a certain safe level namely I_{LIMIT} .
2	Short-circuit or Over current condition	Short-Circuit or Overload condition that occurs when the part is powered up and above UVLO.	At the instance the overload occurs, higher current may flow for a very short period of time before the current limit function can react. After the current limit function has tripped (reached the over-current trip threshold), the device switches into current limiting mode and the current is clamped at I_{LIMIT} .
3	Gradual increase from nominal operating current to I_{LIMIT}	Load increases gradually until the current-limit threshold.	The current rises until I_{LIMIT} . Once the threshold has been reached, the device switches into its current limiting mode and is clamped at I_{LIMIT} .

Reverse-Current Protection

The USB specification does not allow an output device to source current back into the USB port. In a normal MOSFET switch, current will flow in reverse direction (from the output side to the input side) when the output side voltage is higher than the input side. A reverse current limit feature is implemented in the AP2331TD to limit such back currents. Reverse current limit is always active in AP2331TD. Reverse current is limited at I_{ROCP} level and when the fault exists for more than 700 μ s, output device is disabled and shut down. This is called the "Deglitch time from reverse current trigger to MOSFET turn off." Recovery from I_{ROCP} occurs when the output voltage falls to 101% of input voltage.

Over-Voltage Protection

The device has an output over-voltage protection that triggers when the output voltage reaches 5.3V (MIN). When this fault condition stays on for longer than 15 μ s, (This is called the "Debounce time from output over voltage to MOSFET turn off") output device is disabled and shut down.

Recovery from ROVP occurs when the output voltage falls to 101% of input voltage.

Thermal Protection

Thermal protection prevents the IC from damage when the die temperature exceeds safe margins. This mainly occurs when heavy-overload or short-circuit faults are present for extended periods of time. The AP2331TD implements a thermal sensing to monitor the operating junction temperature of the power distribution switch. Once the die temperature rises to approximately +150°C, the Thermal protection feature gets activated as follows: The internal thermal sense circuitry turns the power switch off thus preventing the power switch from damage. Hysteresis in the thermal sense circuit allows the device to cool down to approximately +20°C before the output is turned back on. This built-in thermal hysteresis feature is an excellent feature, as it avoids undesirable oscillations of the thermal protection circuit. The switch continues to cycle in this manner until the load fault is removed, resulting in a pulsed output.

Discharge Function

When input voltage falls below UVLO, the discharge function is active. The output capacitor is discharged through an internal NMOS that has a discharge resistance of 1.8k Ω . Hence, the output voltage drops down. The internal discharge NMOS is turned off right away to avoid leakage current from output. The transient output discharge feature makes it suitable for HDMI applications.

Application Information (Cont.)

Power Dissipation and Junction Temperature

The low on-resistance of the internal MOSFET allows the small surface-mount packages to pass large current. Using the maximum operating ambient temperature (T_A) and $R_{DS(ON)}$, the power dissipation can be calculated by:

$$P_D = R_{DS(ON)} \times I^2$$

Finally, calculate the junction temperature:

$$T_J = P_D \times R_{\theta JA} + T_A$$

Where:

T_A = Ambient Temperature °C

$R_{\theta JA}$ = Thermal Resistance

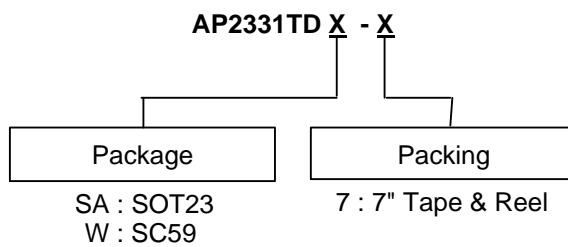
P_D = Total Power Dissipation

Layout Consideration

Good PCB layout is important for improving the thermal performance of the device. All trace lengths should be kept as short as possible. The input (IN) and output (OUT) PCB traces should be as wide as possible to reduce stray impedance. Use a ground plane to enhance the power dissipation capability of the device if applicable.

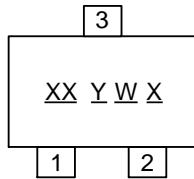
1. Please place the input capacitors near the IN pin as close as possible to minimize the effects of parasitic inductance.
2. Output decoupling capacitors must be placed near the device as close as possible for decoupling transient noises.
3. The negative pins of the input and output capacitors and the GND pin must be connected to the ground plane of the load.
4. Keep VIN and VOUT traces as wide and short as possible.

Ordering Information



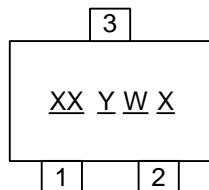
Part Number	Package Code	Packaging (Note 10)	7" Tape and Reel	
			Quantity	Part Number Suffix
AP2331TDSA-7	SA	SOT23	3000/Tape & Reel	-7
AP2331TDW-7	W	SC59	3000/Tape & Reel	-7

Note: 10. Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at <http://www.diodes.com/package-outlines.html>.

Marking Information**(1) SOT23****(Top View)**

XX : Identification code
Y : Year 0~9
W : Week : A~Z : 1~26 week;
a~z : 27~52 week; z represents
52 and 53 week
X : A~Z : Internal code

Part Number	Package	Identification Code
AP2331TDSA-7	SOT23	KP

(2) SC59**(Top View)**

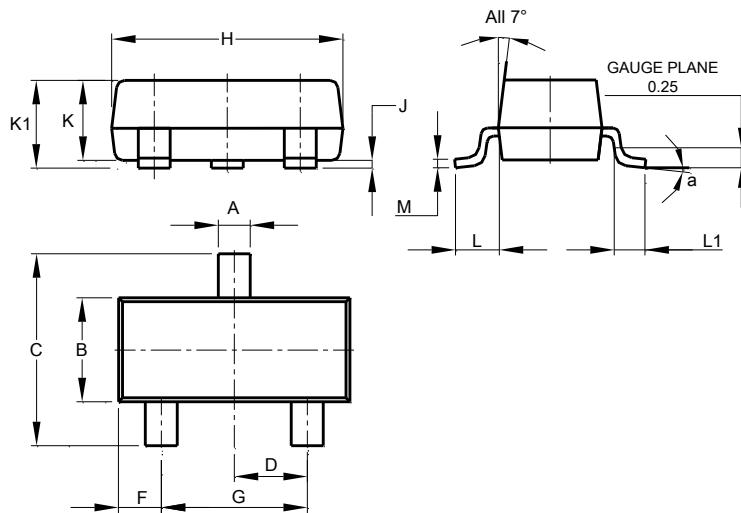
XX : Identification code
Y : Year 0~9
W : Week : A~Z : 1~26 week;
a~z : 27~52 week; z represents
52 and 53 week
X : A~Z : Internal code

Part Number	Package	Identification Code
AP2331TDW-7	SC59	KR

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

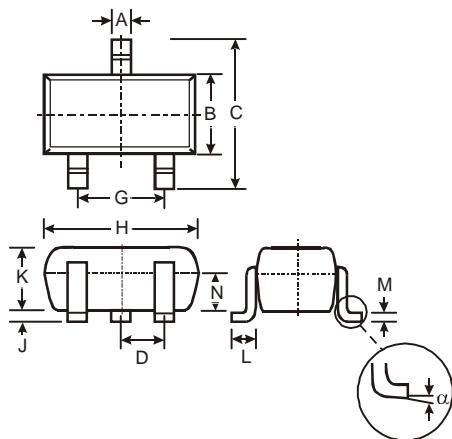
(1) SOT23



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--

All Dimensions in mm

(2) SC59



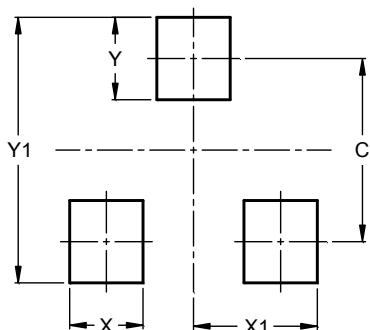
SC59			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	-	-	0.95
G	-	-	1.90
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
a	0°	8°	-

All Dimensions in mm

Suggested Pad Layout

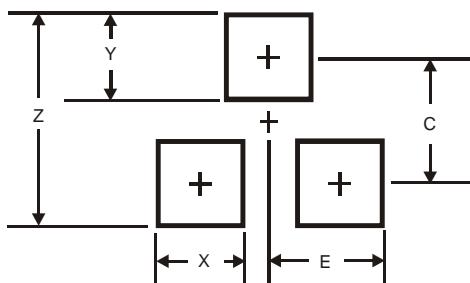
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

(1) SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

(2) SC59



Dimensions	SC59
Z	3.4
X	0.8
Y	1.0
C	2.4
E	1.35

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