

2SK0198 (2SK198)

Silicon N-Channel Junction FET

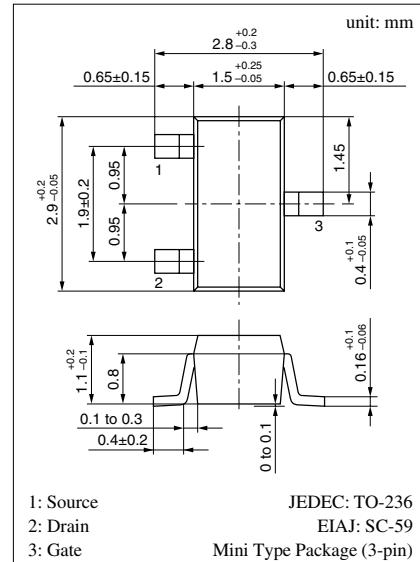
For low-frequency amplification

■ Features

- High mutual conductance g_m
- Low noise type
- Mini-type package, allowing downsizing of the sets and automatic insertion through the tape/magazine packing.

■ Absolute Maximum Ratings (Ta = 25°C)

Parameter	Symbol	Ratings	Unit
Drain to Source voltage	V _{DSX}	30	V
Gate to Drain voltage	V _{GDO}	-30	V
Drain current	I _D	20	mA
Gate current	I _G	10	mA
Allowable power dissipation	P _D	150	mW
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C



Marking Symbol (Example): 1O

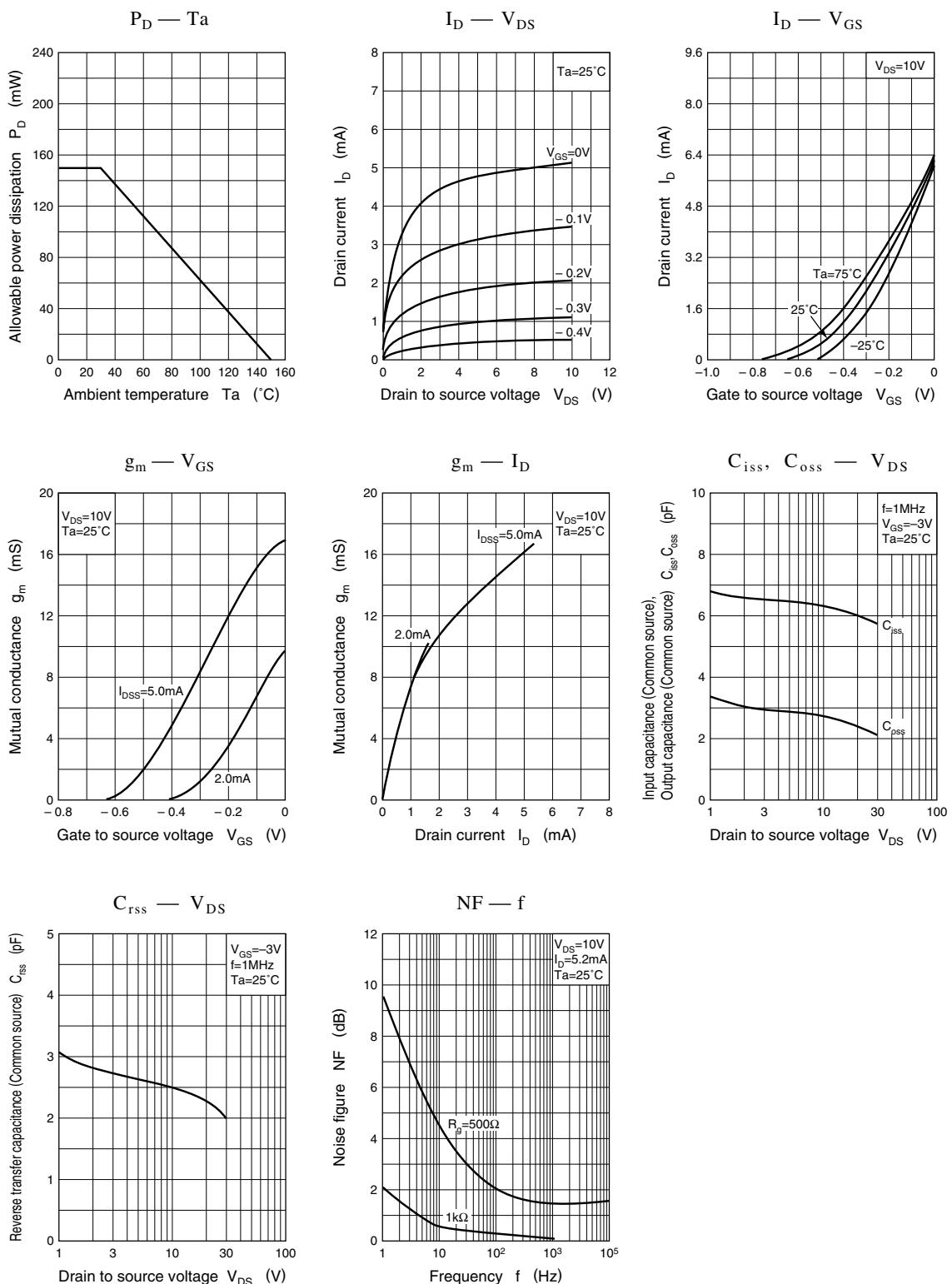
■ Electrical Characteristics (Ta = 25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source cut-off current	I _{DSS} *	V _{DS} = 10V, V _{GS} = 0	0.5		12	mA
Gate to Source leakage current	I _{GSS}	V _{GS} = -30V, V _{DS} = 0			-100	nA
Gate to Source cut-off voltage	V _{GSC}	V _{DS} = 10V, I _D = 10μA	-0.1		-1.5	V
Mutual conductance	g _m	V _{DS} = 10V, I _D = 0.5mA, f = 1kHz	4			mS
		V _{DS} = 10V, V _{GS} = 0, f = 1kHz		13		
Input capacitance (Common Source)	C _{iss}	V _{DS} = 10V, V _{GS} = 0, f = 1MHz		14		pF
Reverse transfer capacitance (Common Source)	C _{rss}			3.5		pF
Noise figure	NV	V _{DS} = 30V, I _D = 1mA, G _V = 80dB R _g = 100kΩ, Function = FLAT		60		mV

* I_{DSS} rank classification

Runk	P	Q	R
I _{DSS} (mA)	0.5 to 3	2 to 6	4 to 12
Marking Symbol	1OP	1OQ	1OR

Note) The part number in the parenthesis shows conventional part number.



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