

N - CHANNEL ENHANCEMENT MODE " SINGLE FEATURE SIZE™ " POWER MOSFET

TYPE	V _{DSS}	R _{DS(on)}	I _D
STP16NE06	60 V	< 0.12 Ω	16 A
STP16NE06FI	60V	< 0.12 Ω	11 A

- TYPICAL R_{DS(on)} = 0.09 Ω
- AVALANCHE RUGGED TECHNOLOGY
- 100% AVALANCHE TESTED
- 175°C OPERATING TEMPERATURE
- HIGH dV/dt CAPABILITY
- APPLICATION ORIENTED CHARACTERIZATION

DESCRIPTION

This Power Mosfet is the latest development of SGS-THOMSON unique "Single Feature Size" process whereby a single body is implanted on a strip layout structure. The resulting transistor shows extremely high packing density for low on-resistance, rugged avalanche characteristics and less critical alignment steps therefore a remarkable manufacturing reproducibility.

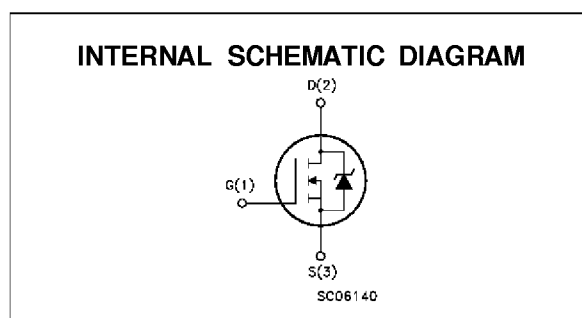
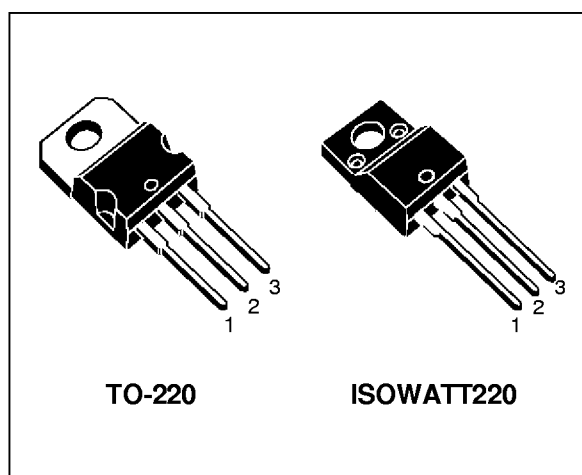
APPLICATIONS

- DC MOTOR CONTROL
- DC-DC & DC-AC CONVERTERS
- SYNCHRONOUS RECTIFICATION

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value		Unit
		STP16NE06	STP16NE06FI	
V _{DS}	Drain-source Voltage (V _{GS} = 0)	60		V
V _{DGR}	Drain- gate Voltage (R _{GS} = 20 kΩ)	60		V
V _{GS}	Gate-source Voltage	± 20		V
I _D	Drain Current (continuous) at T _C = 25 °C	16	11	A
I _D	Drain Current (continuous) at T _C = 100 °C	10	7	A
I _{DM} (•)	Drain Current (pulsed)	64	64	A
P _{tot}	Total Dissipation at T _C = 25 °C	60	30	W
	Derating Factor	0.4	0.2	W/°C
V _{ISO}	Insulation Withstand Voltage (DC)	—	2000	V
dV/dt	Peak Diode Recovery voltage slope	6		V/ns
T _{sig}	Storage Temperature	-65 to 175		°C
T _j	Max. Operating Junction Temperature	175		°C

(•) Pulse width limited by safe operating area
 June 1997



(1) I_{SD} ≤ 16 A, di/dt ≤ 200 A/μs, V_{DD} ≤ V_{(BR)DSS}, T_j ≤ T_{JMAX}

THERMAL DATA

		TO-220	ISOWATT220	
$R_{thj-case}$	Thermal Resistance Junction-case Max	2.5	5	$^{\circ}\text{C/W}$
$R_{thj-amb}$	Thermal Resistance Junction-ambient Max	62.5		$^{\circ}\text{C/W}$
$R_{thc-sink}$	Thermal Resistance Case-sink Typ	0.5		$^{\circ}\text{C/W}$
T_l	Maximum Lead Temperature For Soldering Purpose	300		$^{\circ}\text{C}$

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I_{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T_j max, $\delta < 1\%$)	16	A
E_{AS}	Single Pulse Avalanche Energy (starting $T_j = 25^{\circ}\text{C}$, $I_D = I_{AR}$, $V_{DD} = 25\text{ V}$)	80	mJ

ELECTRICAL CHARACTERISTICS ($T_{case} = 25^{\circ}\text{C}$ unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source Breakdown Voltage	$I_D = 250\text{ }\mu\text{A}$ $V_{GS} = 0$	60			V
I_{DSS}	Zero Gate Voltage Drain Current ($V_{GS} = 0$)	$V_{DS} = \text{Max Rating}$ $V_{DS} = \text{Max Rating}$ $T_c = 125^{\circ}\text{C}$			1 10	μA μA
I_{GSS}	Gate-body Leakage Current ($V_{DS} = 0$)	$V_{GS} = \pm 20\text{ V}$			± 100	nA

ON (*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ $I_D = 250\text{ }\mu\text{A}$	2	3	4	V
$R_{DS(on)}$	Static Drain-source On Resistance	$V_{GS} = 10\text{ V}$ $I_D = 8\text{ A}$		0.090	0.12	Ω
$I_{D(on)}$	On State Drain Current	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $V_{GS} = 10\text{ V}$	16			A

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$g_{fs} (*)$	Forward Transconductance	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $I_D = 8\text{ A}$		6		S
C_{iss}	Input Capacitance	$V_{DS} = 25\text{ V}$ $f = 1\text{ MHz}$ $V_{GS} = 0$		760	1000	pF
C_{oss}	Output Capacitance			100	140	pF
C_{rss}	Reverse Transfer Capacitance			30	45	pF

ELECTRICAL CHARACTERISTICS (continued)**SWITCHING ON**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$ t_r	Turn-on Time Rise Time	$V_{DD} = 30\text{ V}$ $I_D = 8\text{ A}$ $R_G = 4.7\text{ }\Omega$ $V_{GS} = 10\text{ V}$		10 35	80 40	ns ns
$(di/dt)_{on}$	Turn-on Current Slope	$V_{DD} = 25\text{ V}$ $I_D = 16\text{ A}$ $R_G = 4.7\text{ }\Omega$ $V_{GS} = 10\text{ V}$		200		A/ μ s
Q_g Q_{gs} Q_{gd}	Total Gate Charge Gate-Source Charge Gate-Drain Charge	$V_{DD} = 40\text{ V}$ $I_D = 16\text{ A}$ $V_{GS} = 10\text{ V}$		20 5 7	30	nC nC nC

SWITCHING OFF

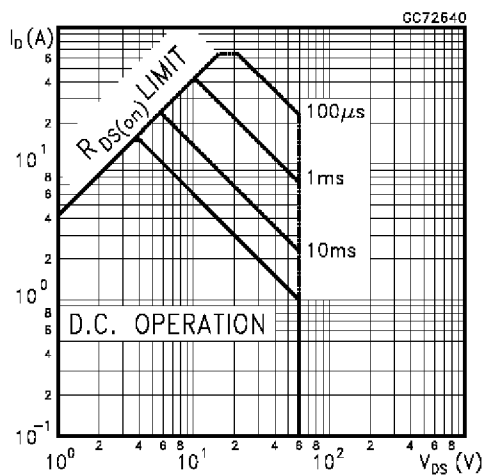
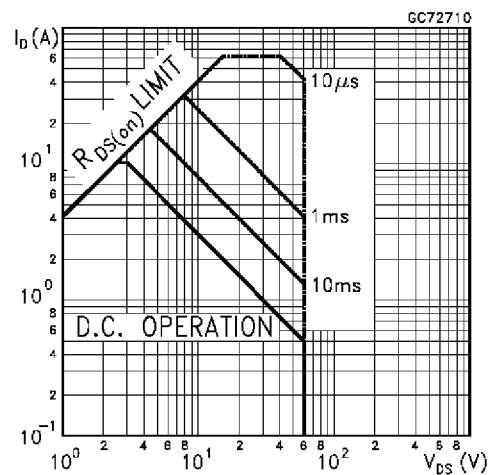
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{r(Voff)}$ t_f t_c	Off-voltage Rise Time Fall Time Cross-over Time	$V_{DD} = 48\text{ V}$ $I_D = 16\text{ A}$ $R_G = 4.7\text{ }\Omega$ $V_{GS} = 10\text{ V}$		7 18 30	10 25 45	ns ns ns

SOURCE DRAIN DIODE

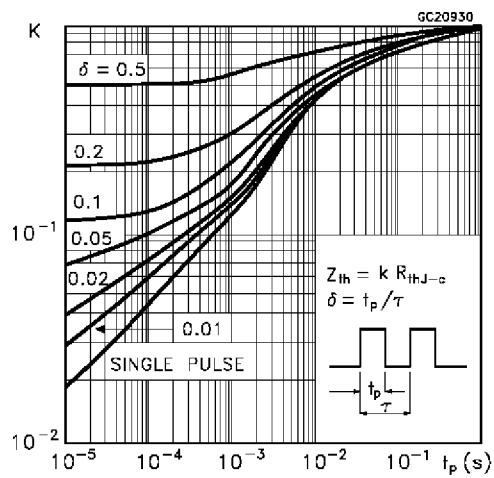
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{SD} $I_{SDM}(\bullet)$	Source-drain Current Source-drain Current (pulsed)				16 64	A A
$V_{SD} (*)$	Forward On Voltage	$I_{SD} = 16\text{ A}$ $V_{GS} = 0$			1.5	V
t_{rr} Q_{rr} I_{RRM}	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	$I_{SD} = 16\text{ A}$ $di/dt = 100\text{ A}/\mu\text{s}$ $V_{DD} = 30\text{ V}$ $T_J = 150\text{ }^\circ\text{C}$		70 0.21 6		ns μ C A

(*) Pulsed: Pulse duration = 300 μ s, duty cycle 1.5 %

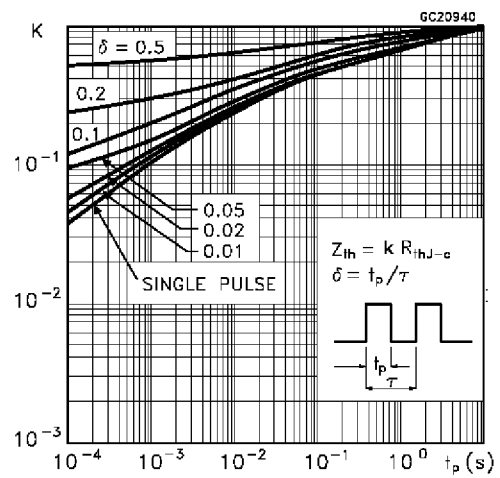
(•) Pulse width limited by safe operating area

Safe Operating Area for TO-220**Safe Operating Area for ISOWATT220**

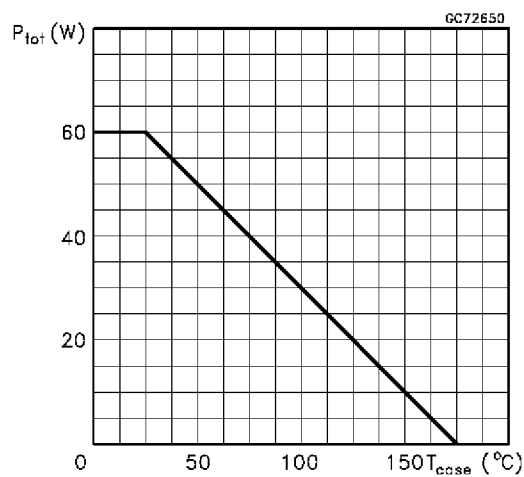
Thermal Impedance for TO-220



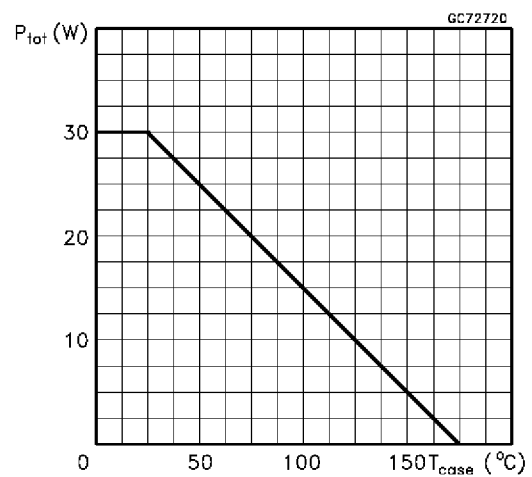
Thermal Impedance for ISOWATT220



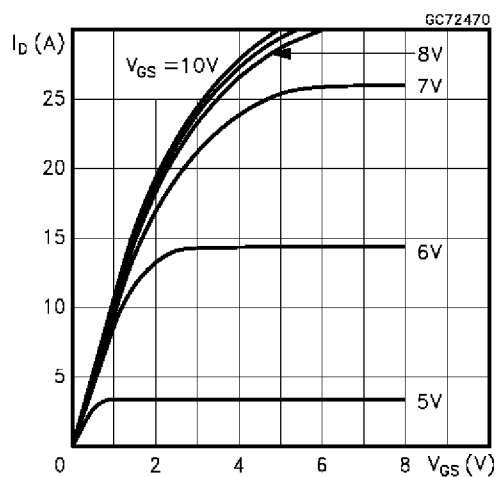
Derating Curve for TO-220



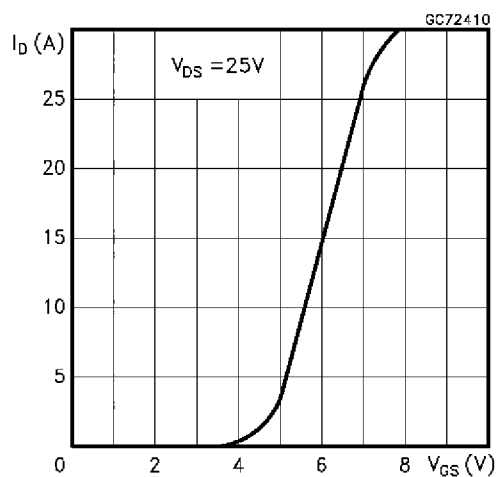
Derating Curve for ISOWATT220



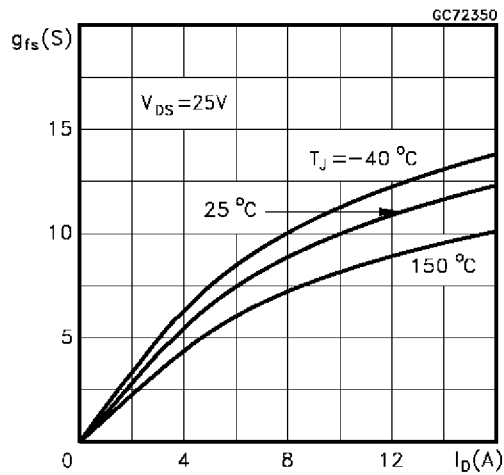
Output Characteristics



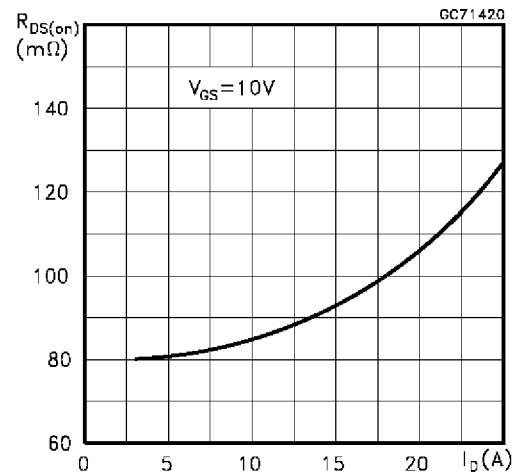
Transfer Characteristics



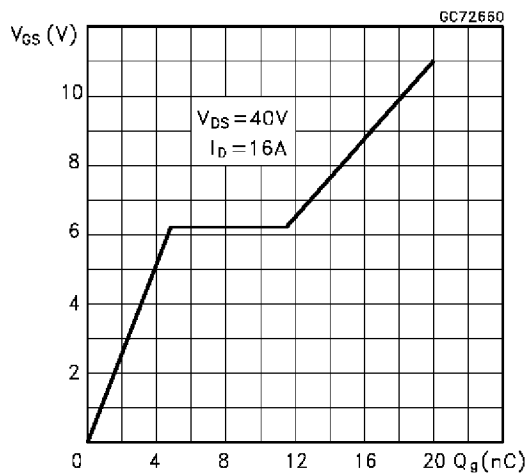
Transconductance



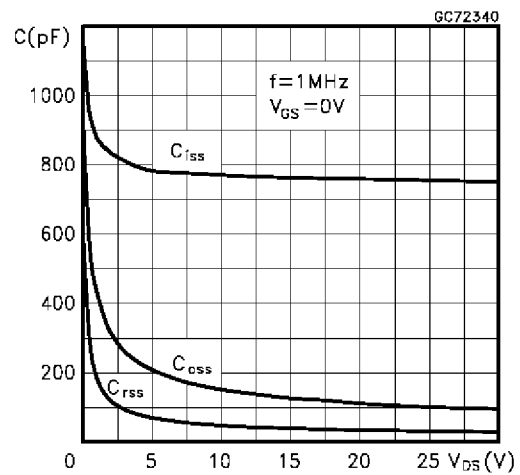
Static Drain-source On Resistance



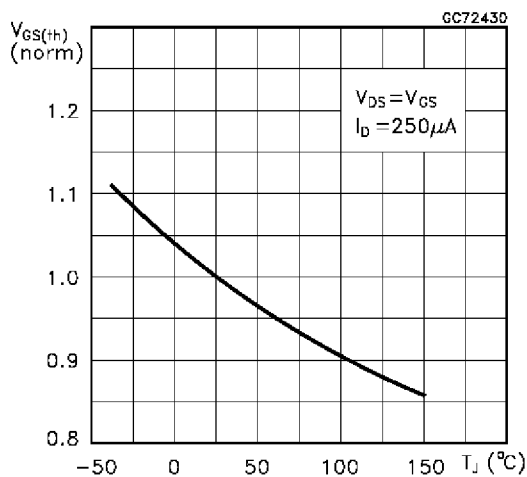
Gate Charge vs Gate-source Voltage



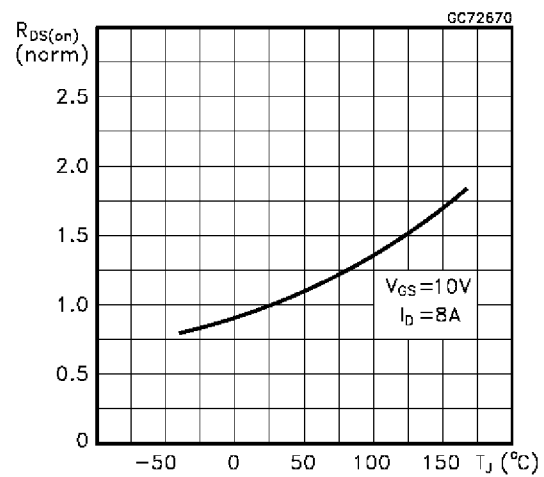
Capacitance Variations



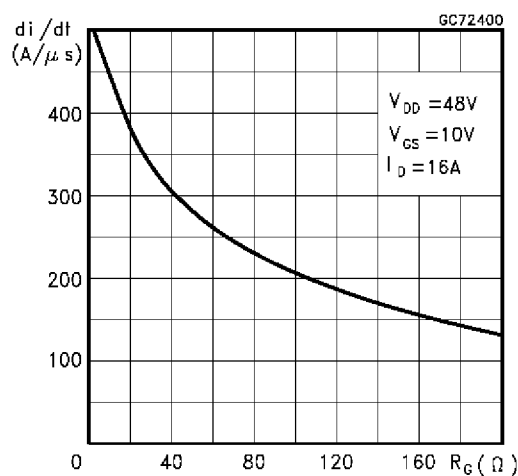
Normalized Gate Threshold Voltage vs Temperature



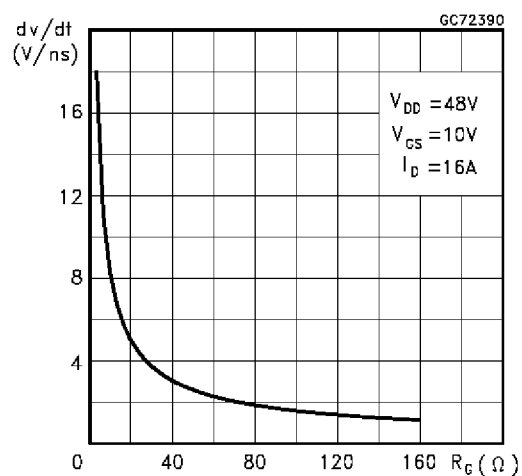
Normalized On Resistance vs Temperature



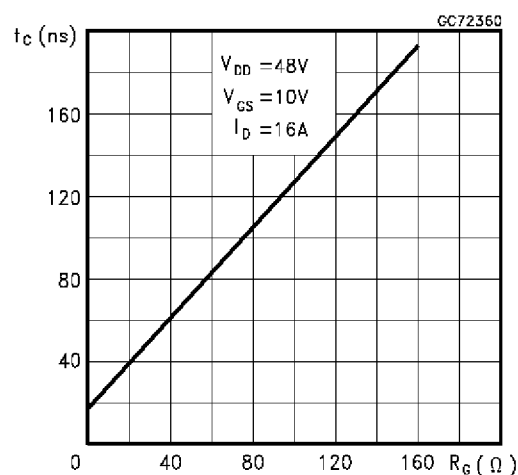
Turn-on Current Slope



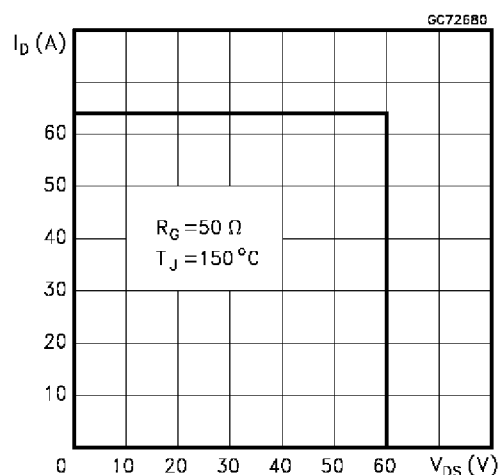
Turn-off Drain-source Voltage Slope



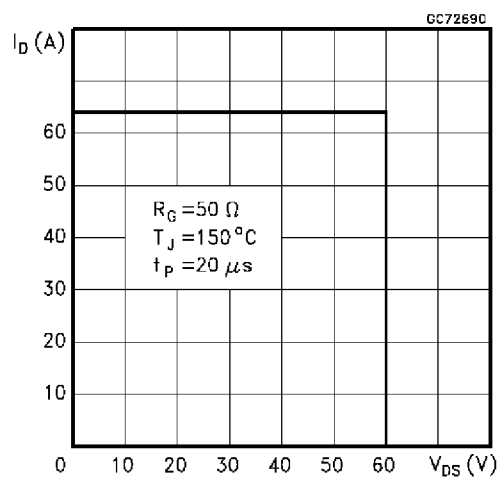
Cross-over Time



Switching Safe Operating Area



Accidental Overload Area



Source-drain Diode Forward Characteristics

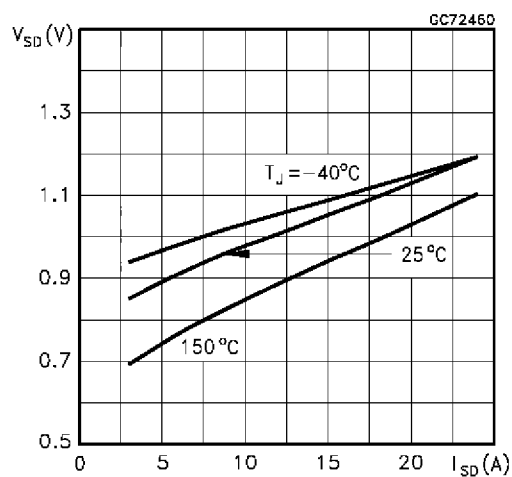
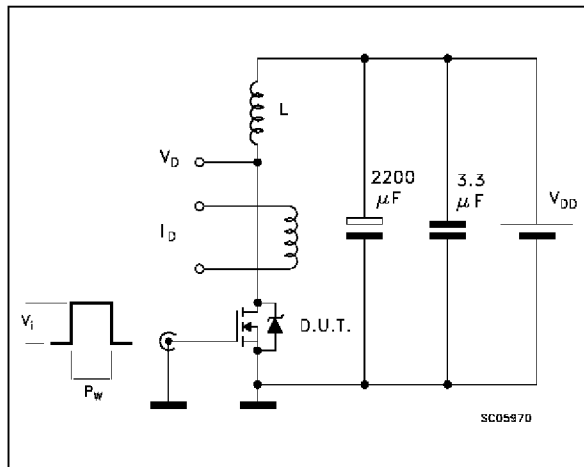
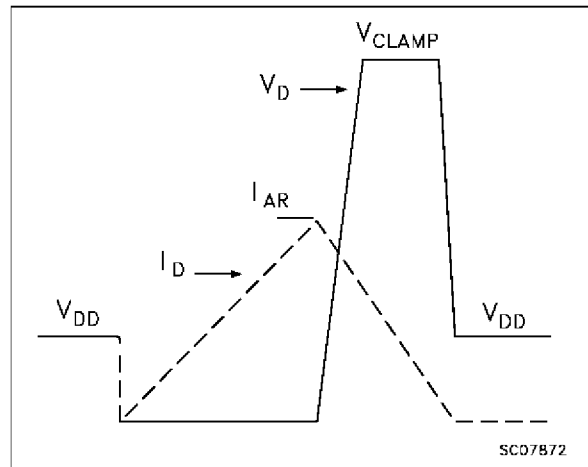
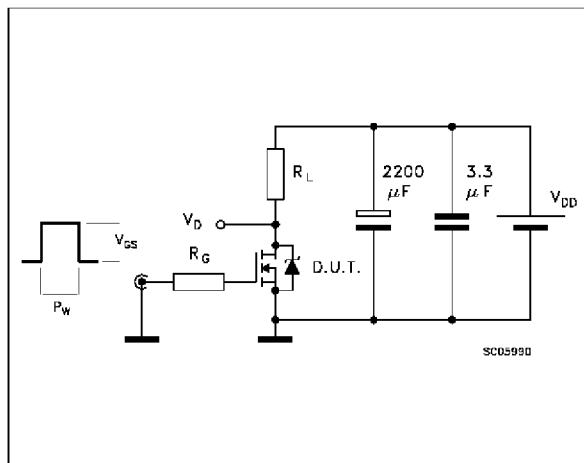
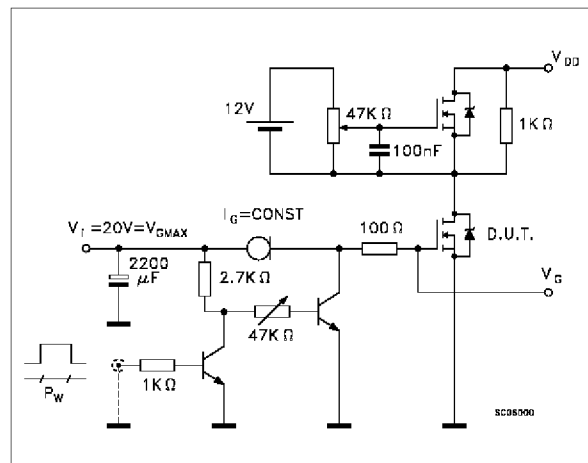
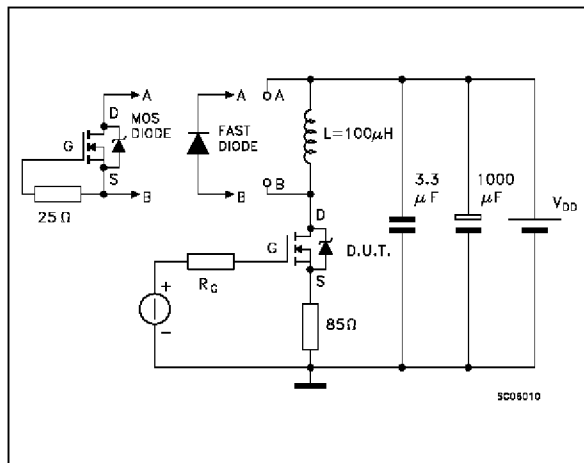
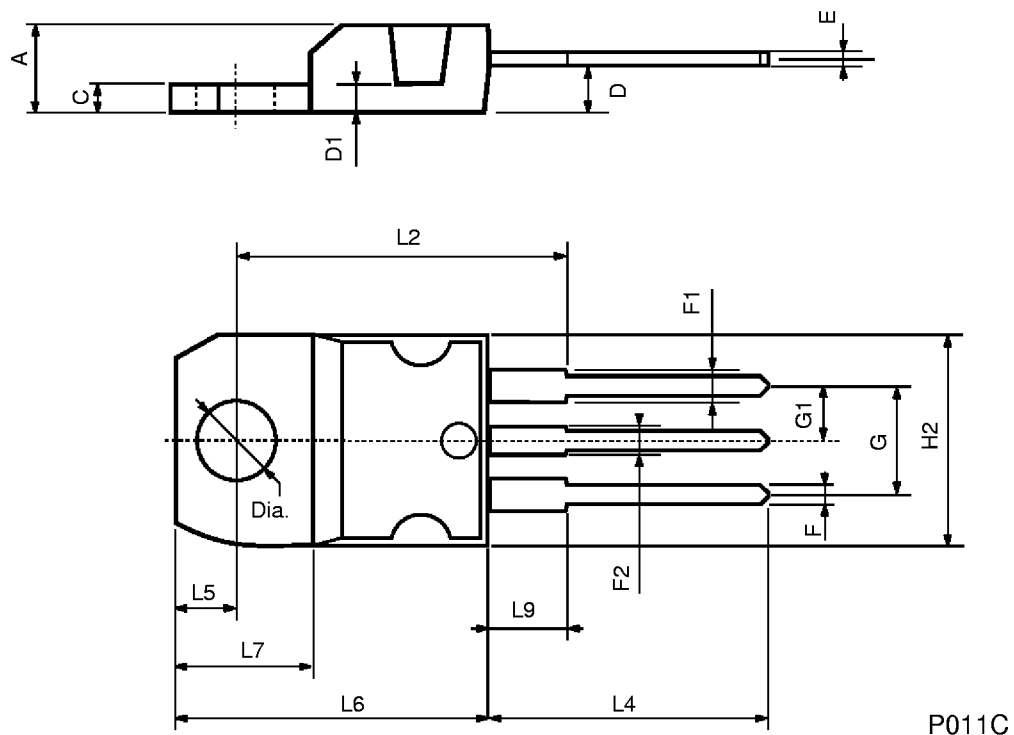


Fig. 1: Unclamped Inductive Load Test Circuit**Fig. 2:** Unclamped Inductive Waveform**Fig. 3:** Switching Times Test Circuits For Resistive Load**Fig. 4:** Gate Charge test Circuit**Fig. 5:** Test Circuit For Inductive Load Switching And Diode Recovery Times

TO-220 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.40		4.60	0.173		0.181
C	1.23		1.32	0.048		0.051
D	2.40		2.72	0.094		0.107
D1		1.27			0.050	
E	0.49		0.70	0.019		0.027
F	0.61		0.88	0.024		0.034
F1	1.14		1.70	0.044		0.067
F2	1.14		1.70	0.044		0.067
G	4.95		5.15	0.194		0.203
G1	2.4		2.7	0.094		0.106
H2	10.0		10.40	0.393		0.409
L2		16.4			0.645	
L4	13.0		14.0	0.511		0.551
L5	2.65		2.95	0.104		0.116
L6	15.25		15.75	0.600		0.620
L7	6.2		6.6	0.244		0.260
L9	3.5		3.93	0.137		0.154
DIA.	3.75		3.85	0.147		0.151



ISOWATT220 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.4		4.6	0.173		0.181
B	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
E	0.4		0.7	0.015		0.027
F	0.75		1	0.030		0.039
F1	1.15		1.7	0.045		0.067
F2	1.15		1.7	0.045		0.067
G	4.95		5.2	0.195		0.204
G1	2.4		2.7	0.094		0.106
H	10		10.4	0.393		0.409
L2		16			0.630	
L3	28.6		30.6	1.126		1.204
L4	9.8		10.6	0.385		0.417
L6	15.9		16.4	0.626		0.645
L7	9		9.3	0.354		0.366
Ø	3		3.2	0.118		0.126

