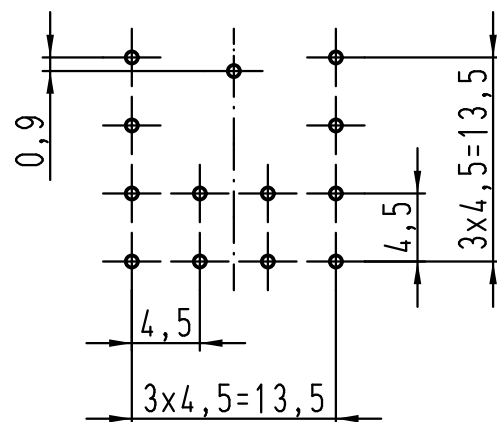
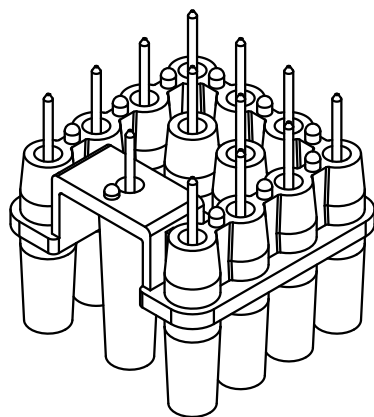
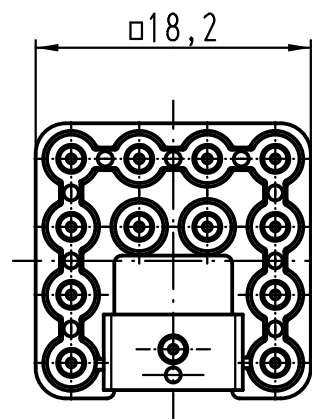
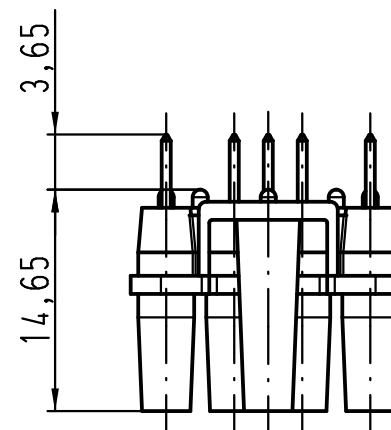


Leiterplatten-Layout Layout of printed circuit boards



Empfohlener Loch-Durchmesser: 0,8mm
recommended hole diameter: 0.8mm



			All Dimensions in mm Original Size DIN A 4				Techn. Character.			Nicht tolerierte Maße/ Free size tolerances			
				Dat.	Name	Maßstab/ Scale 2:1	Leiterplatten-Adapter <i>PCB-adapter</i> Han Q12						
			Detail.	03.03.10	Spr								
			Insp.		Be								
			Stand.										
			HARTING Electric GmbH & Co. KG							TB 09 12 012 9901			Blatt/ page /
A			D-32339 Espelkamp										
Mod.	Dat.	Name							Sub.				