TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

TC75S59AFE, TC75S59AFC

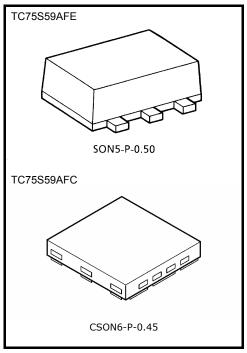
Single Comparator (Open-Drain Output)

The TC75S59AFE and TC75S59AFC are CMOS general-purpose single comparators. The devices can operate from a single supply voltage and are designed for a lower supply-current than conventional general-purpose bipolar comparators. The output is designed for Open-Drain Output and can supply a higher voltage than the power supply. Therefore, it is possible to pull-up the voltage to a level higher than that of the power supply. The Open-Drain Output can be wired-OR with another Open-Drain Output circuit.

* Output voltage should not exceed the maximum rating.

Features

- Low Supply Current: I_{DD} = 100 μA (typ.)
- · Single Power Supply Operation
- Wide Common Mode Input Voltage Range: VSS to VDD 0.9 V
- Open-Drain Output Circuit
- · Low Input Bias Current
- Small Package

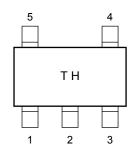


Weight

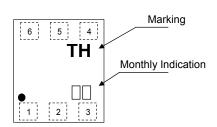
SON5-P-0.50 : 0.003 g (typ.) CSON6-P-0.45 : 0.002 g (typ.)

Marking (top view)

TC75S59AFE

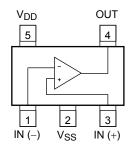


TC75S59AFC

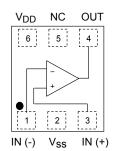


Pin Assignment (top view)

TC75S59AFE



TC75S59AFC



Start of commercial production 2006-06



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating		Unit
Supply Voltage	V _{DD} , V _{SS}	±3.5 or 7		V
Differential Input Voltage	DV _{IN}	±7		V
Input Voltage	V _{IN}	V _{SS} to V _{DD}		V
Output Current	IO	±35		mA
Output Voltage	VO	V _{SS} to V _{SS} + 7		V
Power Dissipation	PD	TC75S59AFE	100	mW
		TC75S59AFC	100 (Note 1)	IIIVV
Operating Temperature	T _{opr}	-40 to 85		°C
Strage Temperature	T _{stg}	-55 to 125		°C

Note: Due to the CMOS structure, this device may be susceptible to latch-up. To prevent latch-up, please take the following precautions;

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- Ensure that no I/O pin's voltage level ever exceeds Vdd or drops below Vss. In addition, check the power-on timing.
- Do not subject the device to excessive noise.

(Note 1) : FR4 in board implementation $(25.4 mm \times 25.4 mm \times 1.6t, \, Cu \, Pad: \, 0.4 mm^2)$



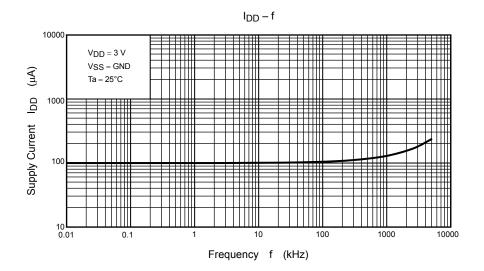
Electrical Characteristics ($V_{DD} = 5 V$, $V_{SS} = GND$, Ta = 25°C)

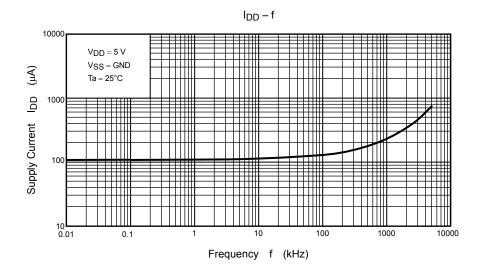
Characteristics	Symbol	Test Circuit	Test Condition	Min.	Тур.	Max.	Unit
Input Offset Voltage	V _{IO}	_	_	_	±1	±7	mV
Input Offset Current	I _{IO}	_	_	_	1	_	pА
Input Bias Current	lı	_	_	_	1	_	pА
Common Mode Input Voltage	CMV _{IN}	_	_	0	_	4.1	V
Supply Current	I _{DD} (Note1)	_	_	_	110	220	μА
Voltage Gain	G _V	_	_	_	94	_	dB
Sink Current	I _{sink}	_	V _{OL} = 0.5 V	13	25	_	mA
Output Leakage Current	I _{LEAK}	_	V _{DD} = 5 V , V _O = 5 V	_	5	_	nA
Off-state Leakage Current	loff	_	V _{DD} = 0 V , V _O = 5 V	_	5	_	nA
Output Low Voltage	V _{OL}	_	I _{sink} = 5.0 mA	_	0.1	0.3	V
Operating Supply Voltage Range	V _{DD}	_	_	1.8	_	7.0	V
Propagation Delay (Turn on)	t _{PLH} (1)	_	Over Drive = 100 mV	_	200	_	ns
	t _{PLH} (2)	_	TTLStep Input	_	140	_	115
Propagation Delay (Turn off)	t _{PHL} (1)	_	Over Drive = 100 mV	_	80	_	ne
	t _{PHL} (2)	_	TTL Step Input	_	60	_	ns
Response Time	t _{TLH}	_	Over Drive = 100 mV	_	160	_	- ns
	t _{THL}		Over Drive = 100 mV	_	3	_	

Electrical Characteristics ($V_{DD} = 3 V$, $V_{SS} = GND$, Ta = 25°C)

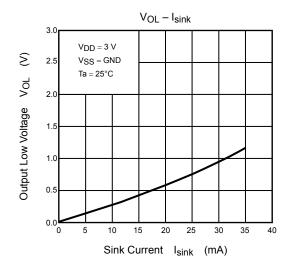
Characteristics	Symbol	Test Circuit	Test Condition	Min.	Тур.	Max.	Unit
Input Offset Voltage	V _{IO}	_	_	_	±1	±7	mV
Input Offset Current	I _{IO}	_	_	_	1	_	pA
Input Bias Current	II	_	_	_	1	_	pА
Common Mode Input Voltage	CMV _{IN}	_	_	0	_	2.1	V
Supply Current	I _{DD} (Note1)	_	_	_	100	200	μА
Sink Current	I _{sink}	_	V _{OL} = 0.5 V	6	18	_	mA
Output Leakage Current	I _{LEAK}	_	$V_{DD} = 3 \text{ V}$, $V_{O} = 3 \text{ V}$	_	5	_	nA
Off-State Leakage Current	I _{OFF}	_	$V_{DD} = 0 \text{ V}$, $V_{O} = 3 \text{ V}$	_	5	_	nA
Output Low Voltage	V _{OL}		I _{sink} = 5.0 mA		0.15	0.35	>
Propagation Delay (Turn On)	t _{PLH}	_	Over Drive = 100 mV	_	160	_	ns
Propagation Delay (Turn Off)	t _{PHL}	_	Over Drive = 100 mV	_	70	_	ns
Response Time	t _{TLH}	_	Over Drive = 100 mV	_	170	_	ns
	t _{THL}	_	Over Drive = 100 mV	_	3	_	115

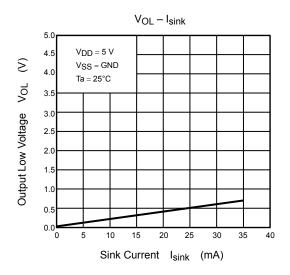
Note: The current consumption of this device increases as its operating frequency increases. Note that the power dissipation should not exceed the allowable power.

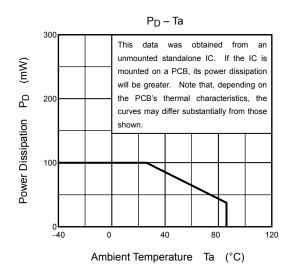




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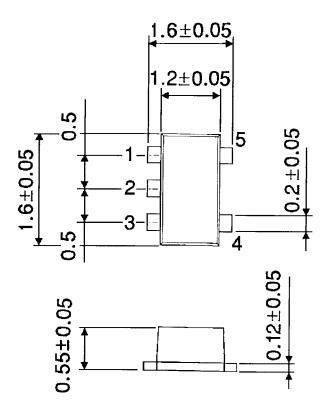




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Package Dimensions

SON5-P-0.50 Unit: mm



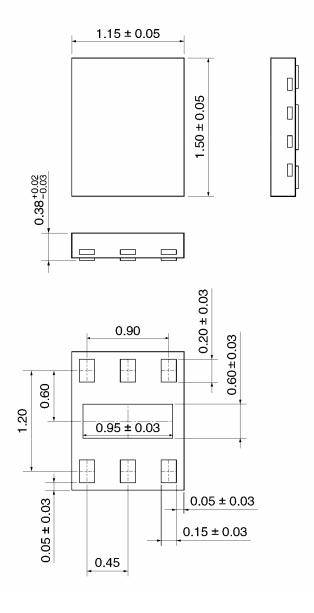
Weight: 0.003 g (Typ.)

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Package Dimensions

CSON6-P-0.45

Unit: mm



Weight: 0.002 g (Typ.)

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