



74HC4511; 74HCT4511

BCD to 7-segment latch/decoder/driver

Rev. 4 — 28 March 2024

Product data sheet

1. General description

The 74HC4511; 74HCT4511 is a BCD to 7-segment latch/decoder/driver with four address inputs (A, B, C, D), a latch enable input (\overline{LE}), a ripple blanking input (\overline{BI}), a lamp test input (\overline{LT}), and seven segment outputs (a to g). When \overline{LE} is LOW, the state of the segment outputs (a to g) is determined by the data on A to D. When \overline{LE} goes HIGH, the last data present on A to D are stored in the latches and the segment outputs remain stable. When \overline{LT} is LOW, all the segment outputs are HIGH independent of all other input conditions. With \overline{LT} HIGH, a LOW on \overline{BI} forces all segment outputs LOW. The inputs \overline{LT} and \overline{BI} do not affect the latch circuit. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC} .

2. Features and benefits

- Complies with JEDEC standard no. 7A
- Wide supply voltage range from 2.0 V to 6.0 V
- CMOS low power dissipation
- High noise immunity
- Input levels:
 - For 74HC4511: CMOS level
 - For 74HCT4511: TTL level
- Latch storage of BCD inputs
- Blanking input
- Lamp test input
- Driving common cathode LED displays
- Guaranteed 10 mA drive capability per output
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level B
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

Type number	Package			Version
	Temperature range	Name	Description	
74HC4511D 74HCT4511D	-40 °C to +125 °C	SO16	plastic small outline package; 16 leads; body width 3.9 mm	SOT109-1

4. Functional diagram

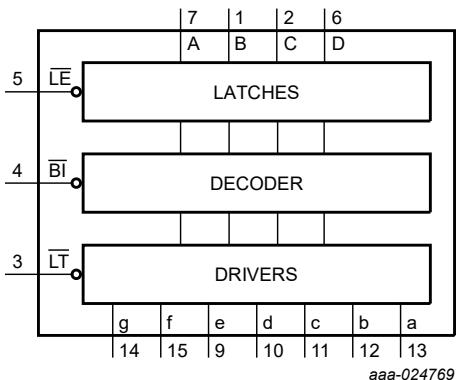


Fig. 1. Functional diagram

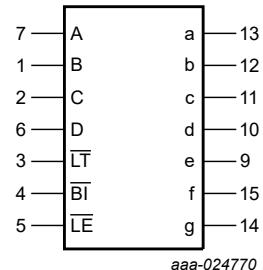


Fig. 2. Logic symbol

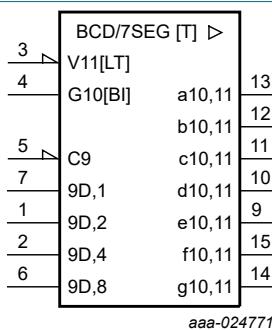
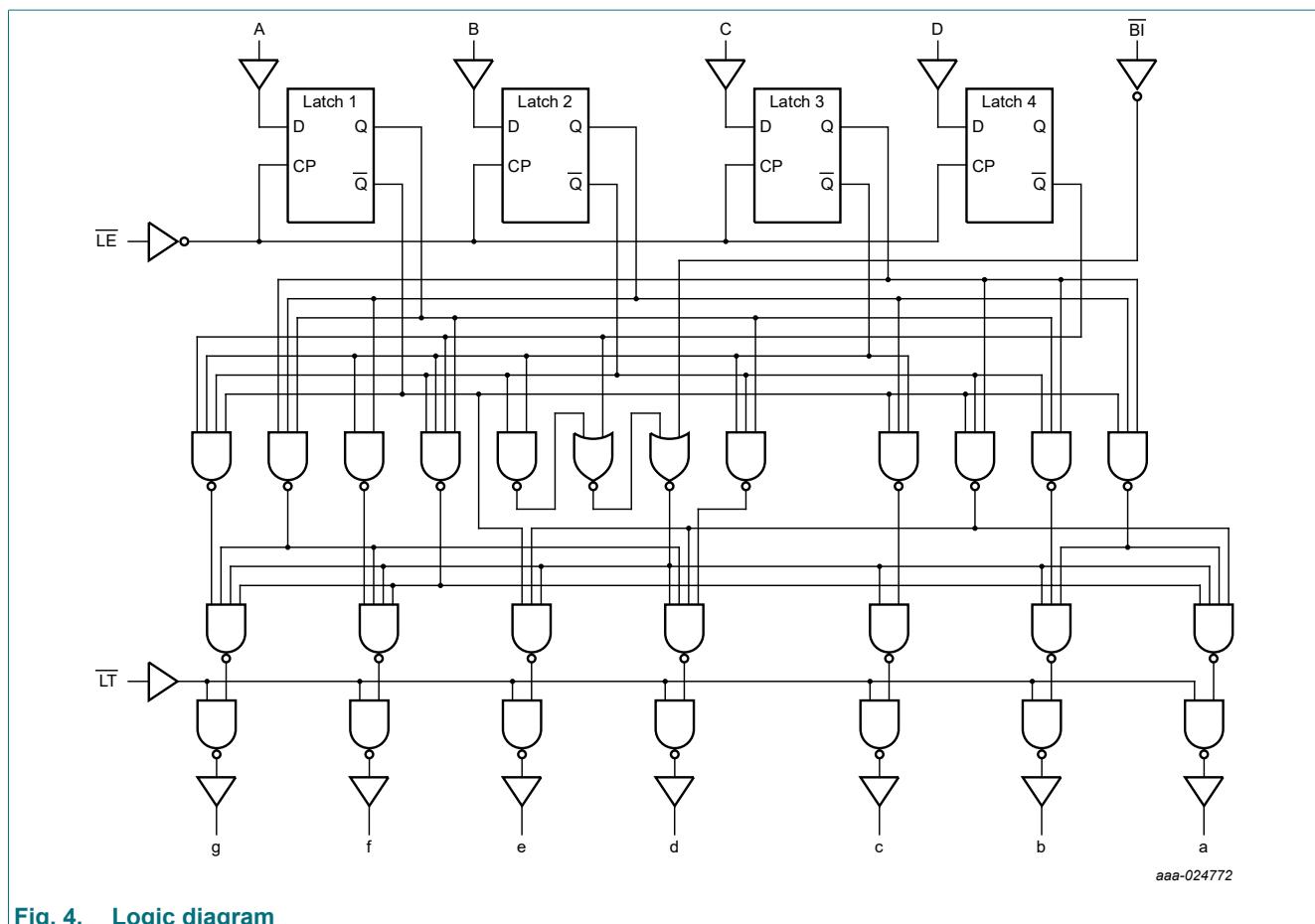
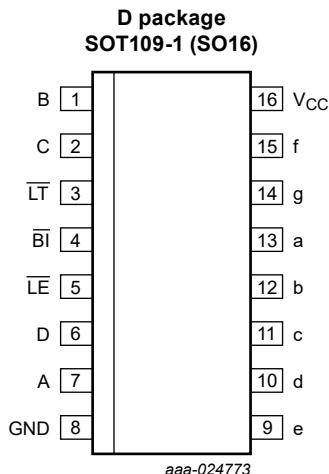


Fig. 3. IEC logic symbol



5. Pinning information

5.1. Pinning



5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
LT	3	lamp test input (active LOW)
BI	4	ripple blanking input (active low)
LE	5	latch enable input (active low)
A, B, C, D	7, 1, 2, 6	BCD address inputs
GND	8	ground (0 V)
a, b, c, d, e, f, g	13, 12, 11, 10, 9, 15, 14	segments outputs
V _{CC}	16	supply voltage

6. Functional description

Table 3. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care.

Inputs							Outputs							Display
LE	BI	LT	D	C	B	A	a	b	c	d	e	f	g	
X	X	L	X	X	X	X	H	H	H	H	H	H	H	8
X	L	H	X	X	X	X	L	L	L	L	L	L	L	blank
L	H	H	L	L	L	L	H	H	H	H	H	H	L	0
L	H	H	L	L	L	H	L	H	H	L	L	L	L	1
L	H	H	L	L	H	L	H	H	L	H	H	L	H	2
L	H	H	L	L	H	H	H	H	H	H	L	L	H	3
L	H	H	L	H	L	L	L	H	H	L	L	H	H	4
L	H	H	L	H	L	H	H	L	H	H	L	H	H	5
L	H	H	L	H	H	L	L	L	H	H	H	H	H	6
L	H	H	L	H	H	H	H	H	H	L	L	L	L	7
L	H	H	H	L	L	L	H	H	H	H	H	H	H	8
L	H	H	H	L	L	H	H	H	H	L	L	H	H	9
L	H	H	H	L	H	L	L	L	L	L	L	L	L	blank
L	H	H	H	H	L	H	L	L	L	L	L	L	L	blank
L	H	H	H	H	H	L	L	L	L	L	L	L	L	blank
L	H	H	H	H	H	H	L	L	L	L	L	L	L	blank
L	H	H	H	H	H	H	L	L	L	L	L	L	L	blank
H	H	H	X	X	X	X	[1]							[1]

[1] Depends upon the BCD-code applied during the LOW-to-HIGH transition of LE.

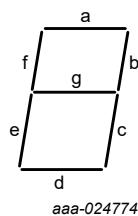


Fig. 5. Segment designation

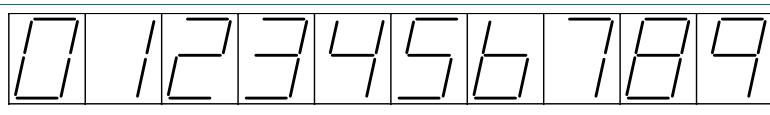


Fig. 6. Display

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+7.0	V
I _{IK}	input clamping current	V _I < -0.5 V or V _I > V _{CC} + 0.5 V	-	±20	mA
I _{OK}	output clamping current	V _O < -0.5 V or V _O > V _{CC} + 0.5 V	-	±20	mA
I _O	output current	-0.5 V < V _O < V _{CC} + 0.5 V	-	±25	mA
I _{CC}	supply current		-	+50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	SO16 package	[1]	-	500 mW

[1] For SOT109-1 (SO16) package: P_{tot} derates linearly with 12.4 mW/K above 110 °C.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V)

Symbol	Parameter	Conditions	74HC4511			74HCT4511			Unit
			Min	Typ	Max	Min	Typ	Max	
V _{CC}	supply voltage		2.0	5.0	6.0	4.5	5.0	5.5	V
V _I	input voltage		0	-	V _{CC}	0	-	V _{CC}	V
V _O	output voltage		0	-	V _{CC}	0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 2.0 V	-	-	625	-	-	-	ns/V
		V _{CC} = 4.5 V	-	1.67	139	-	1.67	139	ns/V
		V _{CC} = 6.0 V	-	-	83	-	-	-	ns/V

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	Min	Max	
74HC4511										
V _{IH}	HIGH-level input voltage	V _{CC} = 2.0 V	1.5	1.2	-	1.5	-	1.5	-	V
		V _{CC} = 4.5 V	3.15	2.4	-	3.15	-	3.15	-	V
		V _{CC} = 6.0 V	4.2	3.2	-	4.2	-	4.2	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 2.0 V	-	0.8	0.5	-	0.5	-	0.5	V
		V _{CC} = 4.5 V	-	2.1	1.35	-	1.35	-	1.35	V
		V _{CC} = 6.0 V	-	2.8	1.8	-	1.8	-	1.8	V

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	Min	Max	
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}								
		I _O = -7.5 mA; V _{CC} = 4.5 V	3.98	-	-	3.84	-	3.7	-	V
		I _O = -10 mA; V _{CC} = 4.5 V	3.6	-	-	3.35	-	3.1	-	V
		I _O = -7.5 mA; V _{CC} = 6.0 V	5.6	-	-	5.45	-	5.35	-	V
		I _O = -10 mA; V _{CC} = 6.0 V	5.48	-	-	5.34	-	5.2	-	V
		I _O = -15 mA; V _{CC} = 6.0 V	4.8	-	-	4.5	-	4.2	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}								
		I _O = 20 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 20 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 20 μA; V _{CC} = 6.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 4.0 mA; V _{CC} = 4.5 V	-	0.15	0.26	-	0.33	-	0.4	V
		I _O = 5.2 mA; V _{CC} = 6.0 V	-	0.16	0.26	-	0.33	-	0.4	V
I _I	input leakage current	V _I = V _{CC} or GND; V _{CC} = 6.0 V	-	-	±0.1	-	±1.0	-	±1.0	μA
I _{CC}	supply current	V _I = V _{CC} or GND; I _O = 0 A; V _{CC} = 6.0 V	-	-	8.0	-	80	-	160	μA
C _I	input capacitance		-	3.5	-	-	-	-	-	pF

74HCT4511

V _{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2.0	1.6	-	2.0	-	2.0	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V	-	1.2	0.8	-	0.8	-	0.8	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL} ; V _{CC} = 4.5 V								
		I _O = -7.5 mA	3.98	-	-	3.84	-	3.7	-	V
		I _O = -10 mA	3.6	-	-	3.35	-	3.1	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL} ; V _{CC} = 4.5 V								
		I _O = 20 μA	-	0	0.1	-	0.1	-	0.1	V
		I _O = 4.0 mA	-	0.15	0.26	-	0.33	-	0.4	V
I _I	input leakage current	V _I = V _{CC} or GND; V _{CC} = 5.5 V	-	-	±0.1	-	±1.0	-	±1.0	μA
I _{CC}	supply current	V _I = V _{CC} or GND; V _{CC} = 5.5 V; I _O = 0 A	-	-	8.0	-	80	-	160	μA
ΔI _{CC}	additional supply current	per input pin; V _{CC} = 4.5 V to 5.5 V; V _I = V _{CC} - 2.1 V; I _O = 0 A; other inputs at V _{CC} or GND								
		LT, LE inputs	-	150	540	-	675	-	735	μA
		BT, A, B, C, D inputs	-	30	108	-	135	-	147	μA
C _I	input capacitance		-	3.5	-	-	-	-	-	pF

10. Dynamic characteristics

Table 7. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); $C_L = 50 \text{ pF}$ unless otherwise specified; for test circuit see [Fig. 11](#).

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	Min	Max	
74HC4511										
t_{pd}	propagation delay	A-D to a-g; see Fig. 7 [1]								
		$V_{CC} = 2.0 \text{ V}$	-	77	300	-	375	-	450	ns
		$V_{CC} = 4.5 \text{ V}$	-	28	60	-	75	-	90	ns
		$V_{CC} = 5.0 \text{ V}; C_L = 15 \text{ pF}$	-	24	-	-	-	-	-	ns
		$V_{CC} = 6.0 \text{ V}$	-	22	51	-	64	-	77	ns
		LE to a-g; see Fig. 8								
		$V_{CC} = 2.0 \text{ V}$	-	74	270	-	330	-	405	ns
		$V_{CC} = 4.5 \text{ V}$	-	27	54	-	68	-	81	ns
		$V_{CC} = 5.0 \text{ V}; C_L = 15 \text{ pF}$	-	23	-	-	-	-	-	ns
		$V_{CC} = 6.0 \text{ V}$	-	22	46	-	58	-	69	ns
		BI to a-g; see Fig. 9								
		$V_{CC} = 2.0 \text{ V}$	-	61	220	-	275	-	330	ns
		$V_{CC} = 4.5 \text{ V}$	-	22	44	-	55	-	66	ns
		$V_{CC} = 5.0 \text{ V}; C_L = 15 \text{ pF}$	-	19	-	-	-	-	-	ns
		$V_{CC} = 6.0 \text{ V}$	-	18	37	-	47	-	56	ns
		LT to a-g; see Fig. 7								
		$V_{CC} = 2.0 \text{ V}$	-	41	150	-	190	-	225	ns
		$V_{CC} = 4.5 \text{ V}$	-	15	30	-	38	-	45	ns
		$V_{CC} = 5.0 \text{ V}; C_L = 15 \text{ pF}$	-	12	-	-	-	-	-	ns
		$V_{CC} = 6.0 \text{ V}$	-	12	26	-	33	-	38	ns
t_t	transition time	see Fig. 7 , Fig. 8 and Fig. 9 [2]								
		$V_{CC} = 2.0 \text{ V}$	-	19	75	-	95	-	110	ns
		$V_{CC} = 4.5 \text{ V}$	-	7	15	-	19	-	22	ns
		$V_{CC} = 6.0 \text{ V}$	-	6	13	-	16	-	19	ns
t_w	pulse width	LE LOW; see Fig. 8								
		$V_{CC} = 2.0 \text{ V}$	80	11	-	100	-	120	-	ns
		$V_{CC} = 4.5 \text{ V}$	16	4	-	20	-	24	-	ns
		$V_{CC} = 6.0 \text{ V}$	14	3	-	17	-	20	-	ns
t_{su}	set-up time	A-D to \overline{LE} ; see Fig. 10								
		$V_{CC} = 2.0 \text{ V}$	60	14	-	75	-	90	-	ns
		$V_{CC} = 4.5 \text{ V}$	12	5	-	15	-	18	-	ns
		$V_{CC} = 6.0 \text{ V}$	10	4	-	13	-	15	-	ns
t_h	hold time	A-D to \overline{LE} ; see Fig. 10								
		$V_{CC} = 2.0 \text{ V}$	0	-11	-	0	-	0	-	ns
		$V_{CC} = 4.5 \text{ V}$	0	-4	-	0	-	0	-	ns
		$V_{CC} = 6.0 \text{ V}$	0	-3	-	0	-	0	-	ns

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ	Max	Min	Max	Min	Max	
C_{PD}	power dissipation capacitance	$V_I = \text{GND to } V_{CC}; V_{CC} = 5 \text{ V}; f_i = 1 \text{ MHz}$ [3]	-	64	-	-	-	-	-	pF
74HCT4511										
t_{pd}	propagation delay	A-D to a-g; see Fig. 7 [1]								
		$V_{CC} = 4.5 \text{ V}$	-	28	60	-	75	-	90	ns
		$V_{CC} = 5.0 \text{ V}; C_L = 15 \text{ pF}$	-	24	-	-	-	-	-	ns
		LE to a-g; see Fig. 8								
		$V_{CC} = 4.5 \text{ V}$	-	27	54	-	68	-	81	ns
		$V_{CC} = 5.0 \text{ V}; C_L = 15 \text{ pF}$	-	24	-	-	-	-	-	ns
		BI to a-g; see Fig. 9								
		$V_{CC} = 4.5 \text{ V}$	-	23	44	-	55	-	66	ns
		$V_{CC} = 5.0 \text{ V}; C_L = 15 \text{ pF}$	-	20	-	-	-	-	-	ns
		LT to a-g; see Fig. 7								
		$V_{CC} = 4.5 \text{ V}$	-	16	30	-	38	-	45	ns
		$V_{CC} = 5.0 \text{ V}; C_L = 15 \text{ pF}$	-	13	-	-	-	-	-	ns
t_t	transition time	see Fig. 7 , Fig. 8 and Fig. 9 [2]								
		$V_{CC} = 4.5 \text{ V}$	-	7	15	-	19	-	22	ns
t_w	pulse width	LE LOW; see Fig. 8								
		$V_{CC} = 4.5 \text{ V}$	16	5	-	20	-	24	-	ns
t_{su}	set-up time	A-D to LE; see Fig. 10								
		$V_{CC} = 4.5 \text{ V}$	12	5	-	15	-	18	-	ns
t_h	hold time	A-D to LE; see Fig. 10								
		$V_{CC} = 4.5 \text{ V}$	0	-4	-	0	-	0	-	ns
C_{PD}	power dissipation capacitance	$V_I = \text{GND to } V_{CC} - 1.5 \text{ V}; V_{CC} = 5 \text{ V}; f_i = 1 \text{ MHz}$ [3]	-	64	-	-	-	-	-	pF

[1] t_{pd} is the same as t_{PHL} and t_{PLH} .

[2] t_t is the same as t_{THL} and t_{TLH} .

[3] C_{PD} is used to determine the dynamic power dissipation (P_D in μW):

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum(C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

f_i = input frequency in MHz;

f_o = output frequency in MHz;

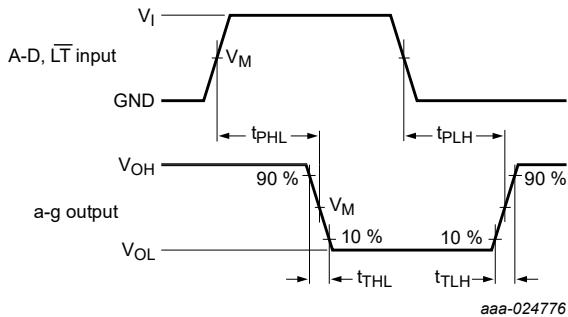
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

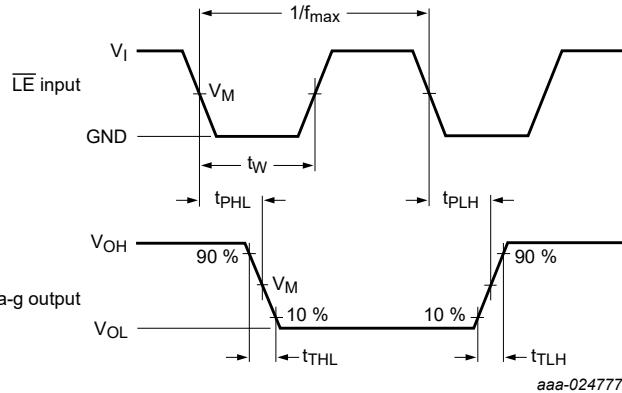
$$\sum(C_L \times V_{CC}^2 \times f_o) = \text{sum of outputs.}$$

10.1. Waveforms and test circuit



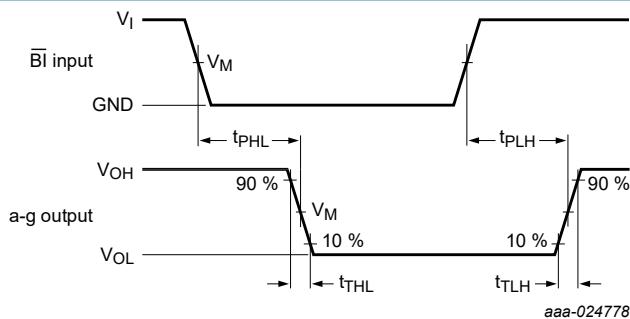
Measurement points are given in [Table 8](#).
Logic levels V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Fig. 7. Input (A-D, LT) to output (a-g) propagation delays and the output transition times



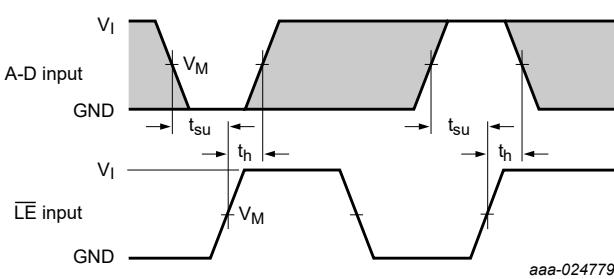
Measurement points are given in [Table 8](#).
Logic levels V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Fig. 8. Input (LE) to output (a-g) propagation delays, latch enable pulse width, and output transition times



Measurement points are given in [Table 8](#).
Logic levels V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Fig. 9. Input (BI) to output (a-g) propagation delays and output transition times

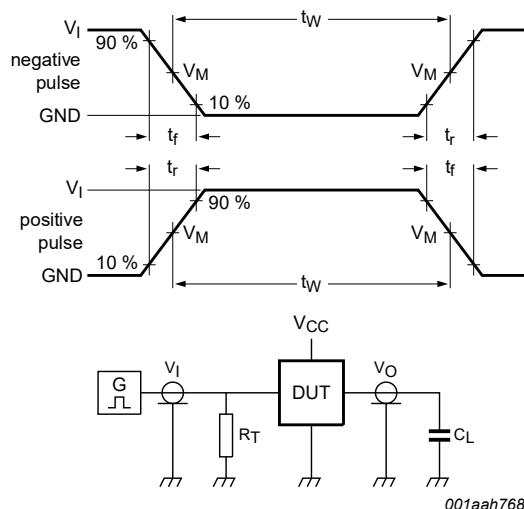


Measurement points are given in [Table 8](#).
The shaded areas indicate when the input is permitted to change for predictable output performance.

Fig. 10. Data set-up and hold times for a-g input to LE input

Table 8. Measurement points

Type	Input		Output
	V_M	V_I	
74HC4511	$0.5 \times V_{CC}$	GND to V_{CC}	$0.5 \times V_{CC}$
74HCT4511	1.3 V	GND to 3 V	1.3 V



Test data is given in [Table 9](#).

Test circuit definitions:

R_T = Termination resistance should be equal to output impedance Z_o of the pulse generator;

C_L = Load capacitance including jig and probe capacitance;

Fig. 11. Test circuit for measuring switching times

Table 9. Test data

Type	Input		Load	Test
	V_I	t_r, t_f		
74HC4511	V_{CC}	6 ns	15 pF, 50 pF	t_{PHL}, t_{PLH}
74HCT4511	3 V	6 ns	15 pF, 50 pF	t_{PHL}, t_{PLH}

11. Application information

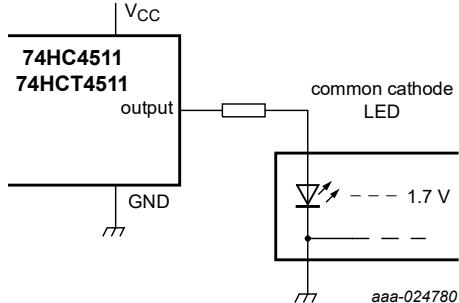


Fig. 12. Connection to common cathode LED display readout

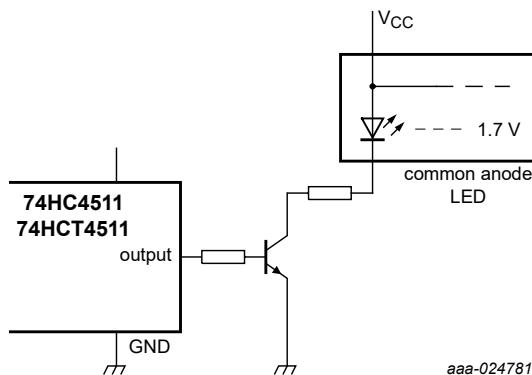
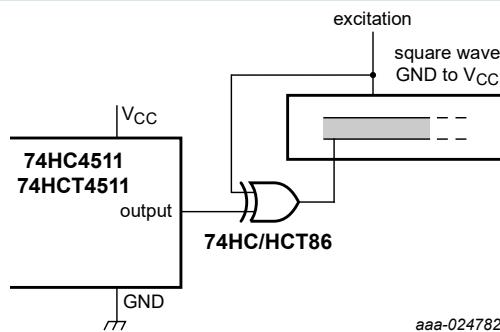


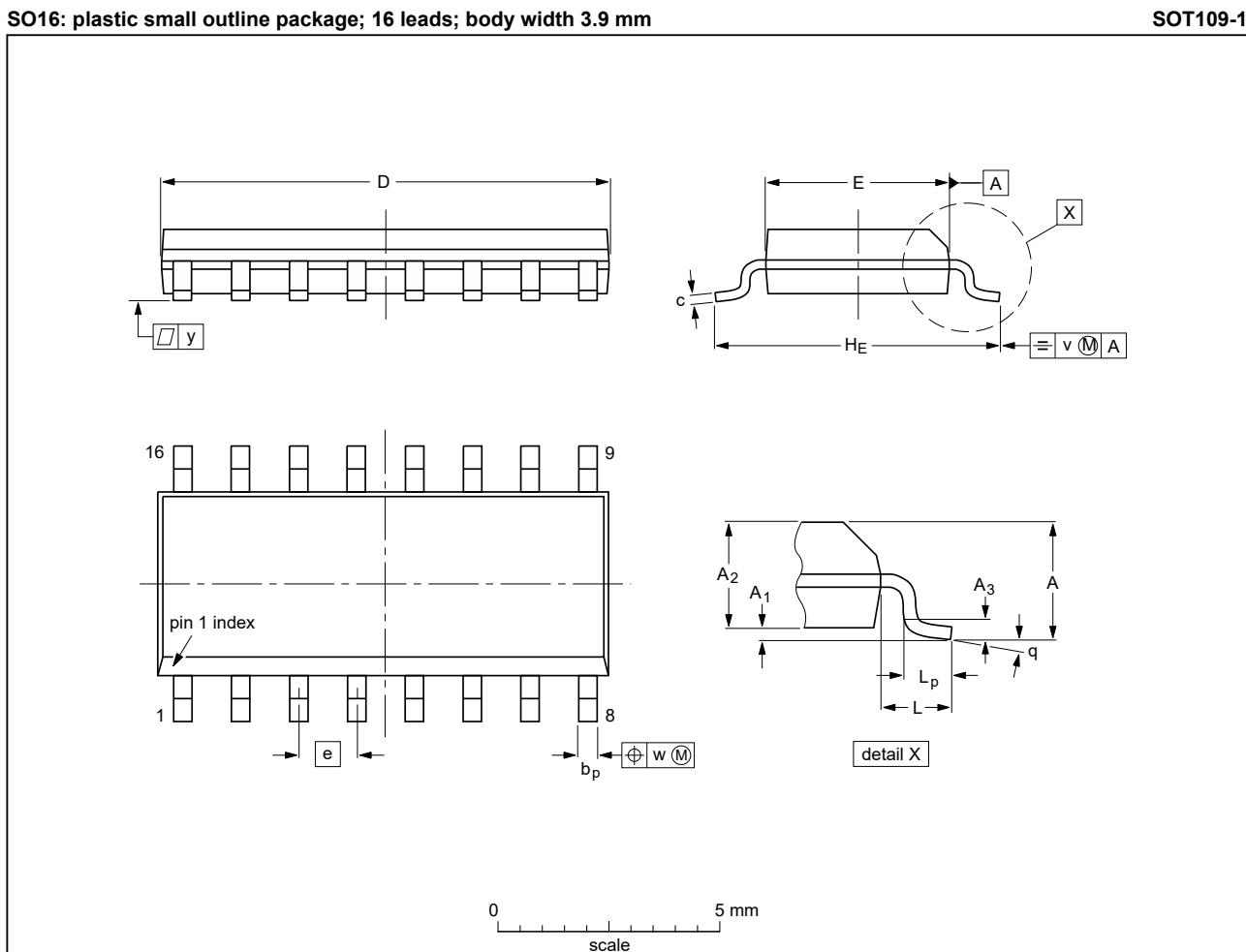
Fig. 13. Connection to common anode LED display readout



Direct DC drive is not recommended as it can shorten the life of LCD displays.

Fig. 14. Connection to LCD display readout

12. Package outline



Dimensions (inch dimensions are derived from the original mm dimensions)

Unit	A	A ₁	A ₂	A ₃	b _p	c	D ⁽¹⁾	E ⁽¹⁾	e	H _E	L	L _p	v	w	y	θ	
mm	max 1.75	0.25			0.51	0.25	10.0	4.0		6.2		1.27	0.2	0.25	0.1	8°	
mm	nom			0.25					1.27		1.05					0°	
mm	min 0.10	1.25		0.31	0.10	9.8	3.8		5.8		0.4					0°	
inches	max 0.069	0.010			0.020	0.010	0.394	0.16		0.244		0.05				8°	
inches	nom			0.01					0.05		0.041			0.008	0.01	0.004	0°
inches	min 0.004	0.049		0.012	0.004	0.386	0.15		0.228		0.016					0°	

Note

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

sot109-1_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT109-1		MS-012				03-02-19 23-10-27

Fig. 15. Package outline SOT109-1 (SO16)

13. Abbreviations

Table 10. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
TTL	Transistor-Transistor Logic

14. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74HC_HCT4511 v.4	20240328	Product data sheet	-	74HC_HCT4511 v.3
Modifications:		<ul style="list-style-type: none"> The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Section 2: ESD specification updated according to the latest JEDEC standard. Table 4: Derating values for P_{tot} total power dissipation updated. Fig. 11 and Table 9 aligned with specification (errata). Fig. 15: Aligned SO package outline drawing to JEDEC MS-012 		
74HC_HCT4511 v.3	20161115	Product data sheet	-	74HC_HCT4511 v.2
Modifications:		<ul style="list-style-type: none"> The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. Legal texts have been adapted to the new company name where appropriate. Type numbers 74HC4511N, 74HCT4511N removed. 		
74HC_HCT4511 v.2	19901201	Product specification	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal

injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nexperia.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Contents

1. General description.....	1
2. Features and benefits.....	1
3. Ordering information.....	1
4. Functional diagram.....	2
5. Pinning information.....	4
5.1. Pinning.....	4
5.2. Pin description.....	4
6. Functional description.....	5
7. Limiting values.....	6
8. Recommended operating conditions.....	6
9. Static characteristics.....	6
10. Dynamic characteristics.....	8
10.1. Waveforms and test circuit.....	10
11. Application information.....	12
12. Package outline.....	13
13. Abbreviations.....	14
14. Revision history.....	14
15. Legal information.....	15

© Nexperia B.V. 2024. All rights reserved

For more information, please visit: <http://www.nexperia.com>

For sales office addresses, please send an email to: salesaddresses@nexperia.com

Date of release: 28 March 2024