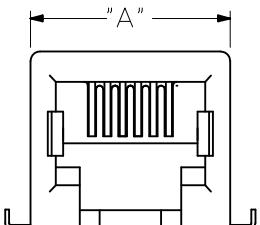
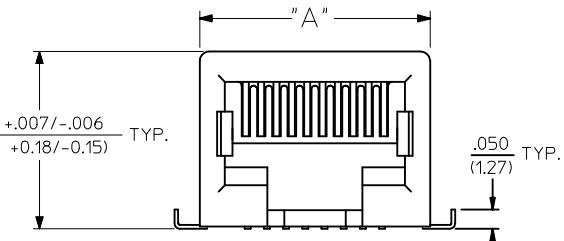


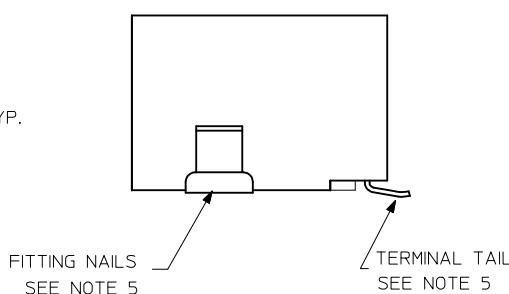
HOUSING SIZE	CIRCUIT SIZE	DIM. A	DIM. B
6	6	.520 (13.21)	.250 (6.35)
6	4	.520 (13.21)	.150 (3.81)
6	2	.520 (13.21)	.050 (1.27)
8	8	.600 (15.24)	.350 (8.89)
8	6	.600 (15.24)	.250 (6.35)



6 CIRCUIT ASSEMBLY



8 CIRCUIT ASSEMBLY



ASSEMBLY ITEM NO.	CONNECTOR SIZE	CIRCUIT SIZE	TERMINAL PLATING	TAPE & REEL PACKAGING No.	VOIDED CIRCUITS
43743-6001	6	6	A	PK-70873-7001	NA
43743-6002	6	6	B		
43743-6003	6	6	C		
43743-6101	6	4	A		
43743-6102	6	4	B		
43743-6103	6	4	C	PK-70873-7001	
43743-8101	8	8	A	PK-70873-7002	
43743-8102	8	8	B		
43743-8103	8	8	C		
43743-8201	8	6	A		
43743-8202	8	6	B		
43743-8203	8	6	C		
43743-8301	8	4	A		
43743-8302	8	4	B		
43743-8303	8	4	C	PK-70873-7002	
43743-6201	6	2	A	PK-70873-7001	
43743-6202	6	2	B	PK-70873-7001	
43743-6203	6	2	C	PK-70873-7001	NA

NOTES:

1) MATERIAL:
HOUSING & INSULATOR: NYLON(PA), GLASS FILLED, UL94V-0, COLOR: BLACK
TERMINALS: PHOSPHOR BRONZE, .012(0.30) THICK
FITTING NAILS: BRASS, .010(0.25) THICK

2) FINISH:
TERMINALS:

A = SELECT GOLD IN CONTACT AREA: .000050/(0.00127) MIN.
*SELECT MATTE TIN IN PC TAIL AREA: .000100/(0.00254) MN.,
WITH OVERALL NICKEL UNDERPLATE: .000050/(0.00127) MIN.

B = SELECT GOLD IN CONTACT AREA: .000030/(0.00076) MIN.
*SELECT MATTE TIN IN PC TAIL AREA: .000100/(0.00254) MN.,
WITH OVERALL NICKEL UNDERPLATE: .000050/(0.00127) MIN.

C = SELECT GOLD IN CONTACT AREA: .000015/(0.00038) MIN.,
*SELECT MATTE TIN IN PC TAIL AREA: .000100/(0.00254) MN.,
WITH OVERALL NICKEL UNDERPLATE: .000050/(0.00127) MIN.

FITTING NAILS:

*100 MICROINCHES MATTE TIN OVER 50 MICROINCHES NICKEL UNDERPLATE
*THE PRIMARY SHIPPING CARTON WILL BE LABELED "COMPLIANT TO ROHS
DIRECTIVE 2002/95/EC AND ELV ANNEX II OF DIRECTIVE 2000/53/EC".
CARTONS WITHOUT THIS LABEL MAY CONTAIN PRODUCT WITH TIN-LEAD
IN THE PC TAILS AND/OR FITTING NAILS.

3) PRODUCT SPECIFICATION AND PROCESSING PARAMETERS: PS-43743-0001.

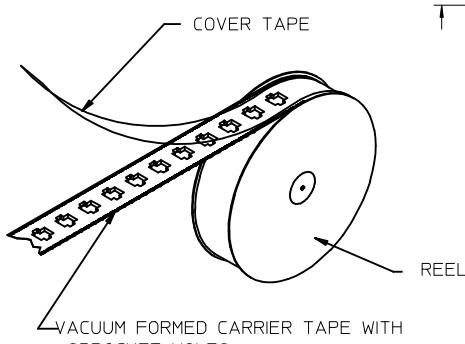
4) PACKAGING SPECIFICATION:
CONNECTOR ASSEMBLIES TO BE PACKAGED USING TAPE & REEL SYSTEM.5) COPLANARITY SPECIFICATION:
WHEN AN ASSEMBLY IS PLACED UPON A LEVEL SURFACE, THE MAXIMUM

GAP BETWEEN THE LEVEL SURFACE AND ALL TERMINAL TAILS AND
BOTH FITTING NAILS IS .006/.015.

6) FOR ASSEMBLIES HAVING VOIDED CIRCUITS, THE TAIL PORTION OF THE TERMINAL REMAINS INTACT.
AND REMOVED, THE MATING PORTION OF THE TERMINAL REMAINS INTACT.

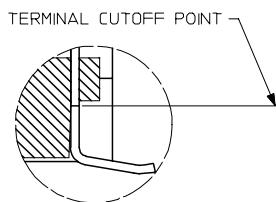
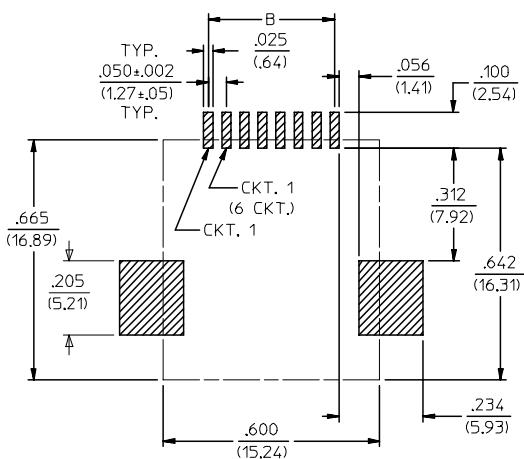
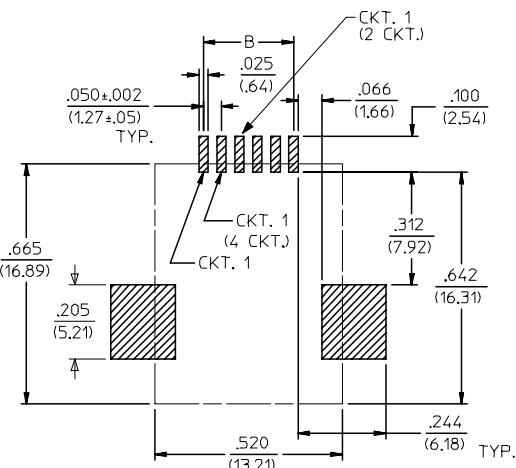
VOIDED CIRCUITS MUST BE THE SAME FOR BOTH PARTS.

7) CONFORMS TO FCC REGULATION PART 68.5 FOR MODULAR JACKS.

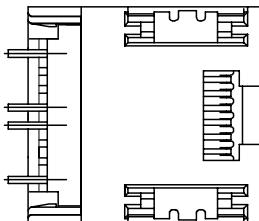
8) THIS PART CONFORMS TO CLASS B REQUIREMENTS OF COSMETIC
SPECIFICATION PS-45499-002.

TAPE AND REEL PACKAGING

REVISED NOTES		QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE IN/MM	SCALE 1:1	DESIGN UNITS INCH	THIRD ANGLE PROJECTION	
EC NO: UCF2009-1244	DRWN:JKLOSTENE IER/2008/12/10		4 PLACES	mm				INCH	DRAWN BY PEARSON
	CHKD:JBELL	3 PLACES	---	---	CHECKED BY PEARSON	DATE			
	APPR:FSMITH	2 PLACES	± 0.25	± 0.014	APPROVED BY PEARSON	DATE 6/18/96			
	REV: J3	1 PLACE	± 0.36	---	EDGLEY	DATE 6/18/96	MOLEX INCORPORATED		
		DRAFT WHERE APPLICABLE		MATERIAL NO. SDA-43743-****		DOCUMENT NO. 1 OF 2			
		MUST REMAIN WITHIN DIMENSIONS		SIZE		SHEET NO.			
				THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					



VIEW SHOWING TYPICAL TERMINAL
CUTOFF OF TAIL (SEE NOTE 5) FOR VOIDED CIRCUITS.



TYPICAL VIEW SHOWING VOIDED CIRCUIT
(CIRCUITS 2,3,6 & 7 SHOWN VOIDED)

SEE SHEET ONE		GENERAL TOLERANCES (UNLESS SPECIFIED)		SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION		REVISE ON CAD ONLY
EC NO: UCP2004-0798 DRAWN BY SCHMIDT 2004/01/05 CHK'D BY SCHMIDT 2004/01/05 APPR'D BY SMITH 2004/01/08		mm	INCH	---	IN/MM			
QUALITY SYMBOLS				DRAWN BY	DATE			
$\nabla = 0$				PEARSON	06/18/96			
$\nabla = 0$				PEARSON	06/18/96			
				CHECKED BY	DATE			
				PEARSON	06/18/96			
				APPROVED BY	DATE	MATERIAL NO.	DOCUMENT NO.	SHEET NO.
				EDGLEY	06/18/96	SEE TABLE	SDA-43743-****	2 OF 2
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