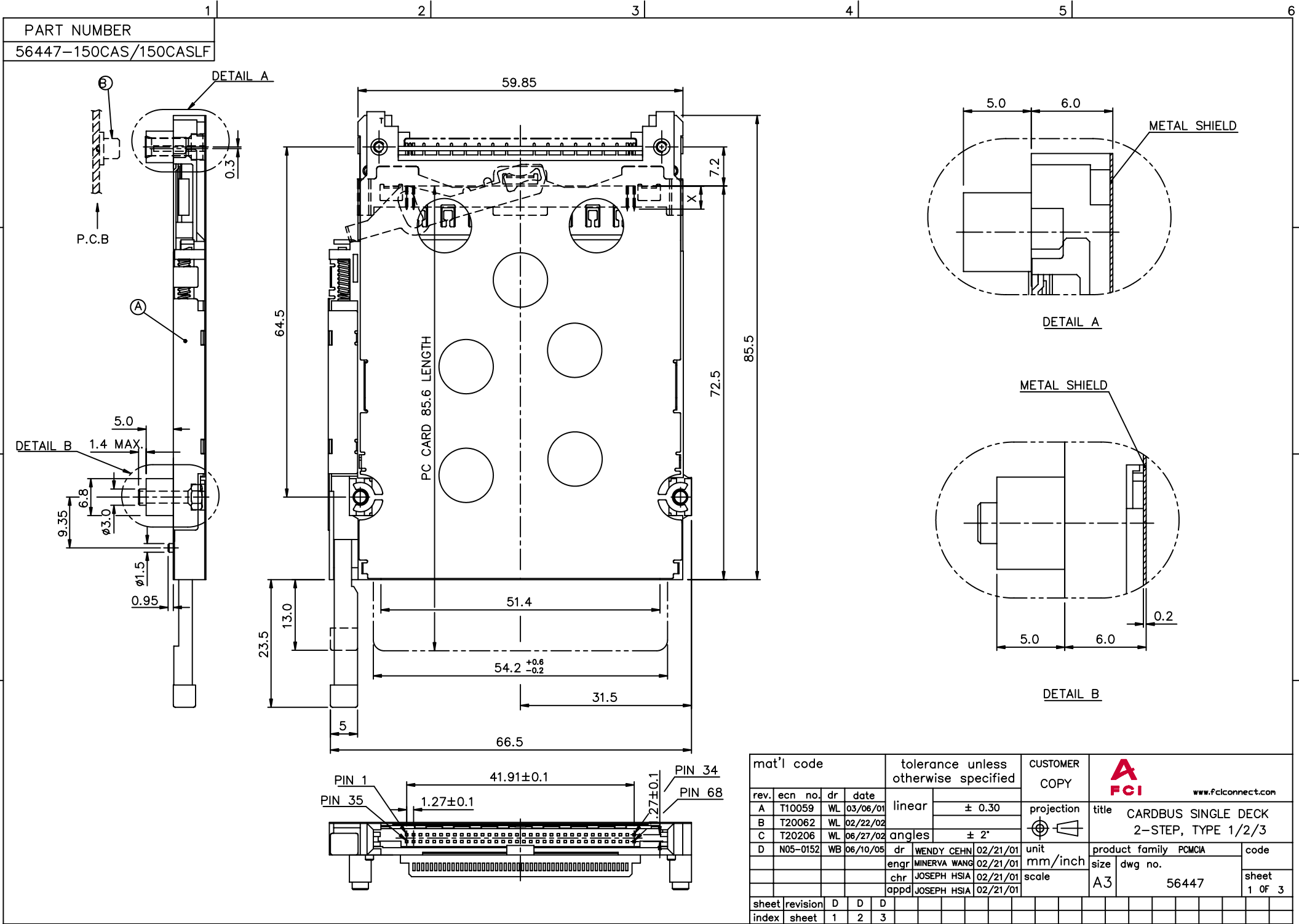


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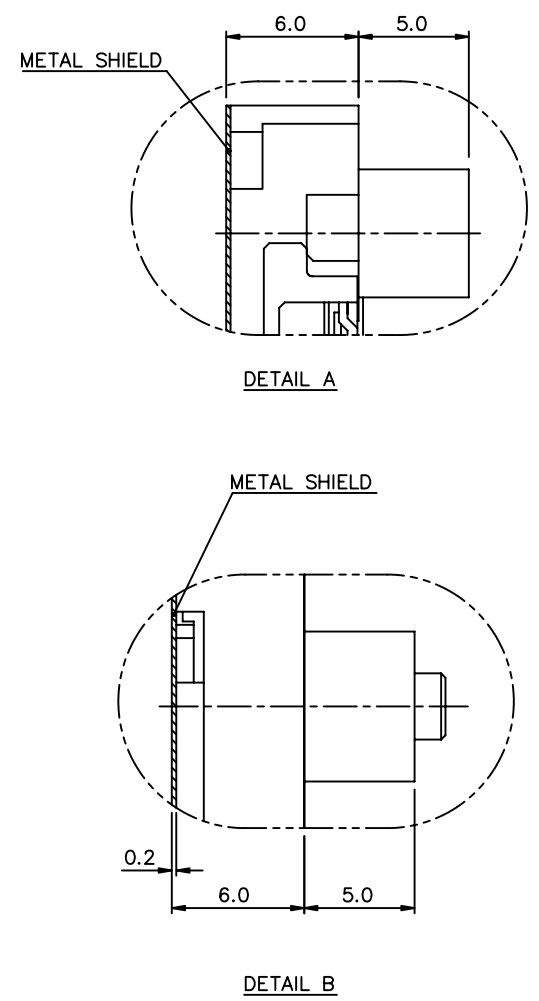
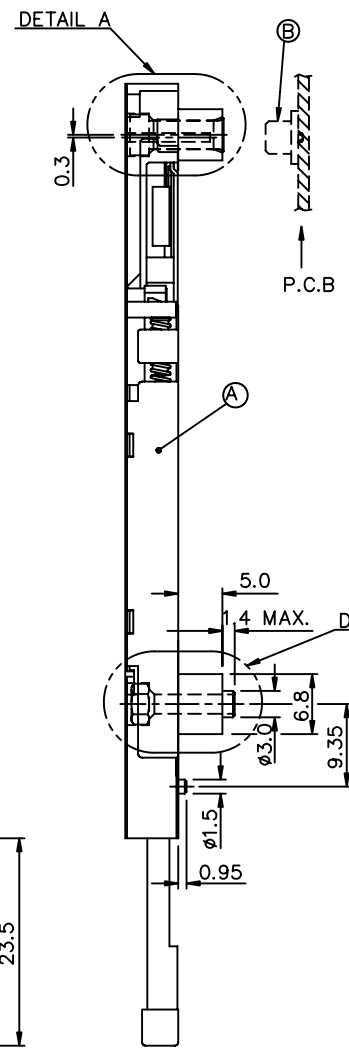
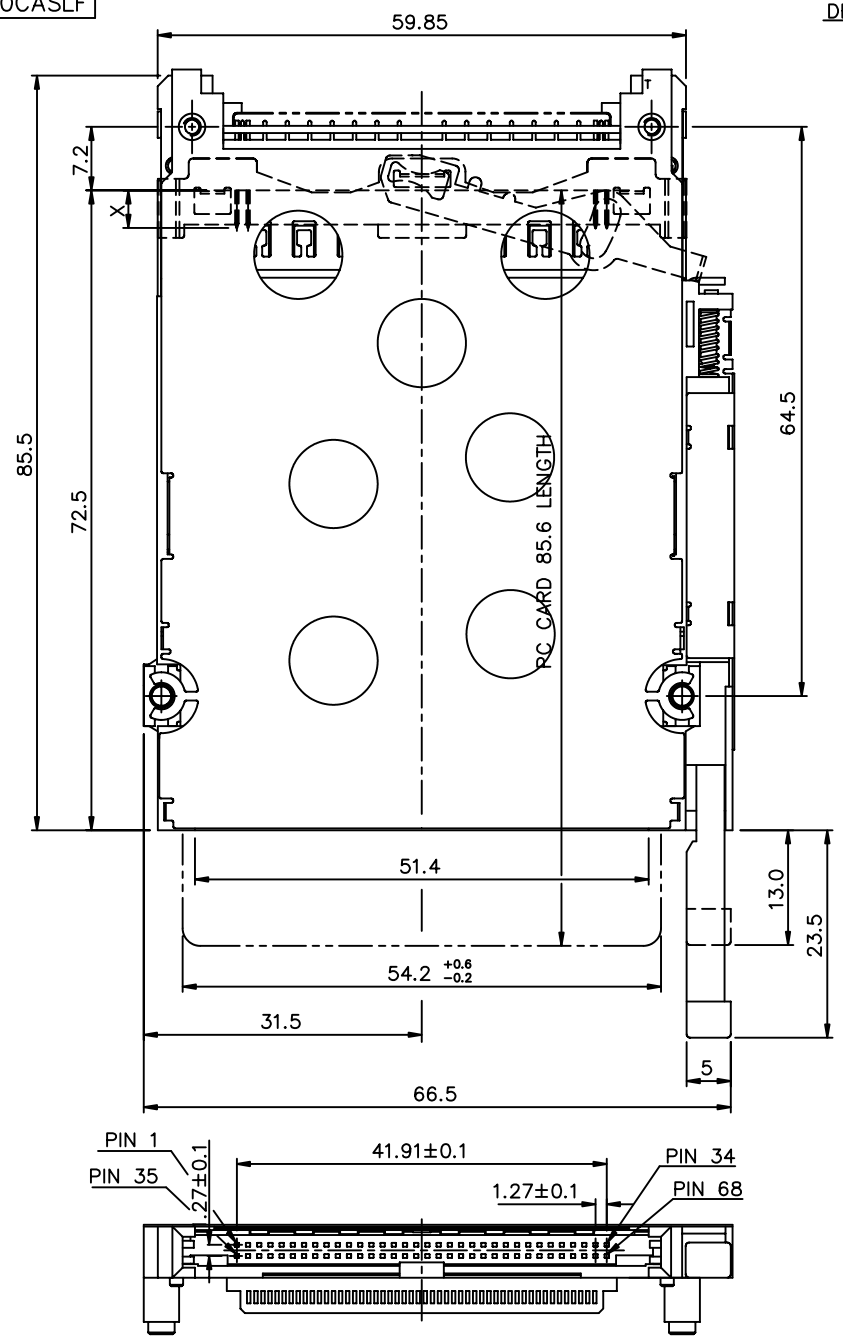
mat'l code				tolerance unless otherwise specified				CUSTOMER COPY		 www.fciconnect.com	
rev.	ecn no.	dr	date	linear	± 0.30			projection 	title CARDBUS SINGLE DECK 2-STEP, TYPE 1/2/3		
A	T10059	WL	03/06/01								
B	T20062	WL	02/22/02								
C	T20206	WL	06/27/02	angles	± 2°				product family PCMCIA size dwg no. A3 56447		
D	N05-0152	WB	06/10/05	dr	WENDY CEHN	02/21/01	unit				
				engr	MINERVA WANG	02/21/01	mm/inch				
				chr	JOSEPH HSIA	02/21/01	scale	sheet 1 OF 3			
				appd	JOSEPH HSIA	02/21/01					
sheet	revision	D	D	D							
index	sheet	1	2	3							

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PART NUMBER
56447-050CAS
56447-050CASLF

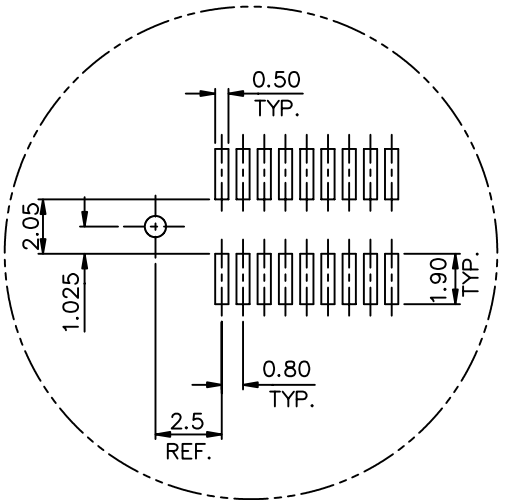


mat'l code					tolerance unless otherwise specified			CUSTOMER COPY		 www.fciconnect.com	
rev.	ecn	no.	dr	date	linear	± 0.30		projection 	title CARDBUS SINGLE DECK 2-STEP, TYPE 1/2/3		
D											
					angles	± 2°					
					dr	WENDY CEHN	02/22/02	unit	product family		code
					enrg	MINERVA WANG	02/22/02	mm/inch	size		
					chr	MINERVA WANG	02/22/02	scale	A3		sheet
					appd	JENN TSAO	02/22/02		56447		2 of
sheet	revision										
index	sheet										

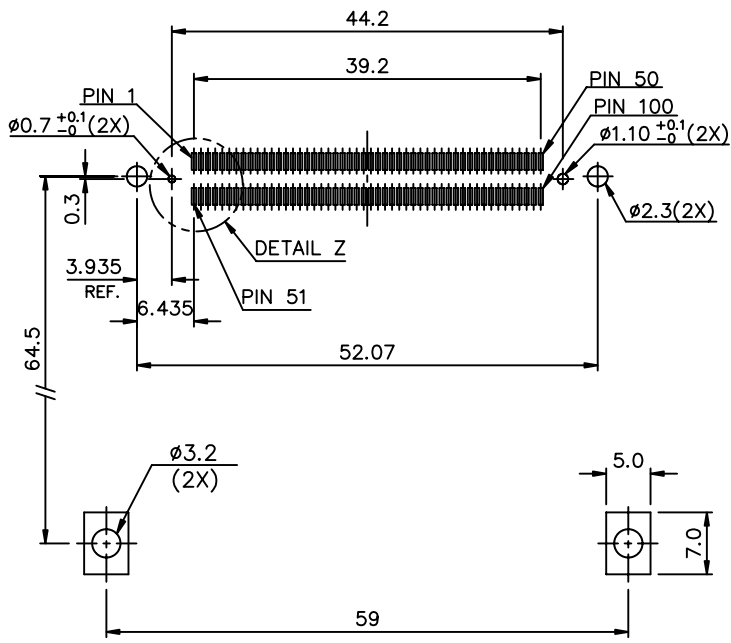
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PART NUMBER
56447-*50CAS
56447-*50CASLF



DETAIL Z



4 CARD INSERTION DIRECTION

ORDERING INFORMATION

NAME	PART NO.		Q'TY
Ⓐ EJECT HEADER ASS'Y	56447-*****	56447-*****LF	1
Ⓑ MATING RECEPTACLE	73277-101000	73277-101000LF	1

NOTES:

- 8.0mm MIN CARD EJECT.
- EJECT TRAVEL: 9.50mm
- MATERIAL:
 - EJECT HEADER ASS'Y
PLASTIC HOUSING: LCP UL94V-0 BLACK
PIN: COPPER ALLOY
PCB: FR4
PLASTIC GUIDE: POLYPHTHARAMID UL94V-0 ,BLACK
 - EMI CONTACT: PHOSPHOR BRONZE
- RECEPTACLE
PLASTIC: LCP 94V-0 NATURAL
PIN: BeCu
- FINISH
PIN :
UNDER PLATING: 0.5um MIN. Ni
CONTACT AREA: 0.1um MIN. GOLD OVER
0.5um MIN. Pd-Ni
SOLDER TAIL: 2.5um MIN. Sn-Pb
2.5um MIN. PURE Sn (-*****LF)
- GROUND SHIELD :
UNDER PLATING: 0.75um MIN. Ni
CONTACT SURFACE: 0.03um GOLD MIN. OVER
PdNi 0.5um MIN.(GOLD FLASH DOUBLE SIDES)
OR, 0.38um GOLD(ONE SIDE ONLY)
- PUSH ROD LOCATION AND P.C.B. ARE AS APPEARS INSTALLED
- DIM "X"

4.25±0.1	3.5±0.1	5.0±0.1
OTHERS	36,67	1,17,34,35,51,68
- DO NOT TOUCH AND KEEP CLEAN THE SURFACES OF PCB OF HEADER ASS'Y.
- IF LEAD FREE P/N. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°C PEAK TEMPERATURE FOR 10 SECONDS IN A WAVE SOLDER APPLICATION WITH A 1.00MM MINIMUM THICK CIRCUIT BOARD.
- LEAD FREE PRODUCT MEETS EUROPEAN UNION DIRECTIVES AND OTHER COUNTRY REGULATIONS AS DESCRIBED IN GS-22-008
- IF LEAD FREE P/N. PACKAGING MEETS GS-14-920 SPECIFICATION

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rev.	ecn	no.	dr	date	linear	± 0.1	projection	title	
D					angles	± 2°	⊕	CARDBUS SINGLE DECK	
					dr	WENDY CEHN 02/21/01	unit	product family : PCMCIA	
					enr	MINERVA WANG 02/21/01	mm/inch	code	
					chr	JOSEPH HSIA 02/21/01	scale	size	
					appd	JOSEPH HSIA 02/21/01	A3	56447	
sheet	revision							TWN	
index	sheet							3 OF	