

(ACTIVE) FPDXSDUR-43USB

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The FPDXSDUR-43USB is an evaluation kit designed to demonstrate performance and capabilities of the DS99R421 FPD-Link to FPD-Link II Embedded Clock LVDS Converter

The DS99R421 Serializer board accepts FPD Link LVDS (3 Data + 1 Clock) input signals and provides single serialized FPD-Link II LVDS data pair as an output. The DS90UR124 Deserializer board accepts the FPD-Link II LVDS serialized data stream and converts the data back into parallel LVCMOS signals and clock. USB cable is provided as a generic medium for the high speed data link. Jumpers and switches on the boards can be configured for evaluation of chipset features such as pre-emphasis, Progressive Turn On, etc.

Part Number	Buy from Texas Instruments or Third Party	Status	Lead-Free	RoHS
FPDXSDUR-43USB/NOPB: FPD- Link LVDS to FPD-Link II Embedded Clock LVDS Converter DS99R421 Evaluation Kit	\$249.00(USD) In Stock Typically Ships in 1 to 3 Business Days Buy from TI	ACTIVE	Yes	Yes

 TI's Standard Terms and Conditions for Evaluation Modules apply.

User Guides (1)

Title	Abstract	Type	Size (KB)	Date	Views	TI Recommends
 FPD to SERDES (UR) Translator Chip DS99R421 Evaluation Kit User's Guide		PDF	4481	26 Jan 2012	362	

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DS99R421	5-43 MHz FPD-Link LVDS(3 Data + 1 Clk) to FPD-Link II LVDS (Embedded Clk DC-Balanced) Conv	Display & Imaging SerDes
DS99R421-Q1	5-43 MHz FPD-Link LVDS(3 Data + 1 Clk) to FPD-Link II LVDS (Embedded Clk DC-Balanced) Conv	Display & Imaging SerDes

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