

TC74HC4075P TRIPLE 3-INPUT OR GATE

GENERAL DESCRIPTION

The TC74HC4075 is a high speed CMOS 3- INPUT OR GATE fabricated with silicon gate C²MOS technology.

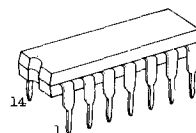
It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

The internal circuit is composed of 4 stages including buffer output, which enables high noise immunity and stable output.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

FEATURES:

- High Speed $t_{pd}=10ns(Typ.)$ at $V_{CC}=5V$
- Low Power Dissipation $I_{CC}=1\mu A(Max.)$ at $T_a=25^{\circ}C$
- High Noise Immunity $V_{NIH}=V_{NIL}=28\% V_{CC} (Min.)$
- Output Drive Capability 10 LSTTL Loads
- Symmetrical Output Impedance $|I_{OH}|=I_{OL}=4mA(Min.)$
- Balanced Propagation Delays $t_{pLH}=t_{pHL}$
- Wide Operating Voltage Range $V_{CC}(Opr.)=2V \sim 6V$
- Pin and Function Compatible with 4075B



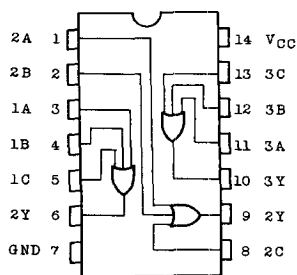
DIP(5-21F)

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage Range	V_{CC}	$-0.5 \sim 7$	V
DC Input Voltage	V_{IN}	$-0.5 \sim V_{CC}+0.5$	V
DC Output Voltage	V_{OUT}	$-0.5 \sim V_{CC}+0.5$	V
Input Diode Current	I_{IK}	± 20	mA
Output Diode Current	I_{OK}	± 20	mA
DC Output Current	I_{OUT}	± 25	mA
DC V_{CC} /Ground Current	I_{CC}	± 50	mA
Power Dissipation	P_D	500*	mW
Storage Temperature	T_{stg}	$-65 \sim 150$	$^{\circ}C$
Lead Temperature 10sec	T_L	300	$^{\circ}C$

* 500mW in the range of $T_a=-40^{\circ} \sim 65^{\circ}C$ and from $T_a=65^{\circ}C$ up to $85^{\circ}C$ derating factor of $-10mW/^{\circ}C$ shall be applied until 300mW.

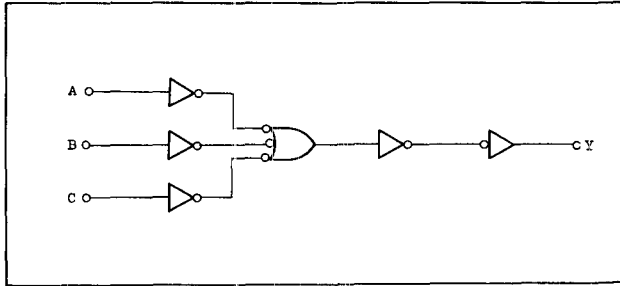
PIN ASSIGNMENT



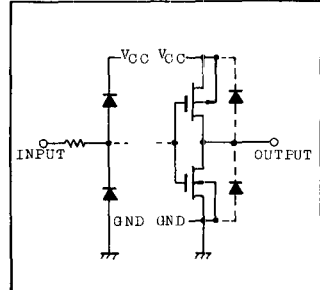
(TOP VIEW)

TC74HC4075P

LOGIC DIAGRAM



INPUT and OUTPUT
EQUIVALENT CIRCUIT



RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	LIMIT	UNIT
Supply Voltage	V_{CC}	2 ~ 6	V
Input Voltage	V_{IN}	0 ~ V_{CC}	V
Output Voltage	V_{OUT}	0 ~ V_{CC}	V
Operating Temperature	T_{opr}	-40 ~ 85	°C
Input Rise and Fall Time	t_r, t_f	0 ~ 1000 ($V_{CC}=2.0V$) 0 ~ 500 ($V_{CC}=4.5V$) 0 ~ 400 ($V_{CC}=6.0V$)	ns

DC ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITION	V_{CC}	$T_a=25^{\circ}C$			$T_a=-40\sim 85^{\circ}C$		UNIT
				MIN.	TYP.	MAX.	MIN.	MAX.	
High-Level Input Voltage	V_{IH}		2.0	1.5	-	-	1.5	-	V
			4.5	3.15	-	-	3.15	-	
			6.0	4.2	-	-	4.2	-	
Low-Level Input Voltage	V_{IL}		2.0	-	-	0.5	-	0.5	V
			4.5	-	-	1.35	-	1.35	
			6.0	-	-	1.8	-	1.8	
High-Level Output Voltage	V_{OH}	$V_{IN}=V_{IH}$ or V_{IL}	$I_{OH}=-20\mu A$	2.0	1.9	2.0	-	1.9	V
				4.5	4.4	4.5	-	4.4	
			$I_{OH}=-4mA$	6.0	5.9	6.0	-	5.9	
				4.5	4.18	4.31	-	4.13	
			$I_{OH}=-5.2mA$	6.0	5.68	5.80	-	5.63	

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DC ELECTRICAL CHARACTERISTICS (Continued)

PARAMETER	SYMBOL	TEST CONDITION		V _{CC}	Ta=25°C			Ta=-40~85°C		UNIT
					MIN.	TYP.	MAX.	MIN.	MAX.	
Low-Level Output Voltage	V _{OL}	V _{IN} =V _{IL}	I _{OL} =20μA	2.0	-	0.0	0.1	-	0.1	V
				4.5	-	0.0	0.1	-	0.1	
				6.0	-	0.0	0.1	-	0.1	
			I _{OL} =4mA	4.5	-	0.17	0.32	-	0.37	
			I _{OL} =5.2mA	6.0	-	0.18	0.32	-	0.37	
Input Leakage Current	I _{IN}	V _{IN} =V _{CC} or GND	6.0	-	-	±0.1	-	±1.0	μA	
Quiescent Supply Current	I _{CC}	V _{IN} =V _{CC} or GND	6.0	-	-	1.0	-	10.0		

AC ELECTRICAL CHARACTERISTICS (C_L=50pF, Input t_r=t_f=6ns)

PARAMETER	SYMBOL	TEST CONDITION	V _{CC}	Ta=25°C			Ta=-40~85°C		UNIT
				MIN.	TYP.	MAX.	MIN.	MAX.	
Output Transition Time	t _{TLH} t _{THL}		2.0	-	30	75	-	90	ns
			4.5	-	8	15	-	18	
			6.0	-	7	13	-	16	
Propagation Delay Time	t _{PLH} t _{PHL}		2.0	-	48	110	-	130	ns
			4.5	-	13	22	-	26	
			6.0	-	11	19	-	23	
Input Capacitance	C _{IN}			-	5	10	-	10	pF
Power Dissipation Capacitance	C _{PD(1)}			-	30	-	-	-	

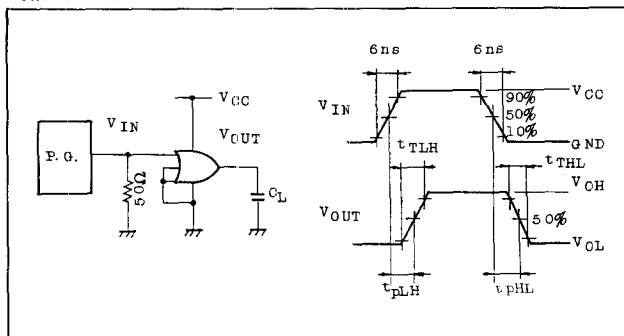
Note (1) C_{PD} is defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load (refer to Test Circuit).

Average operating current can be obtained by the equation hereunder.

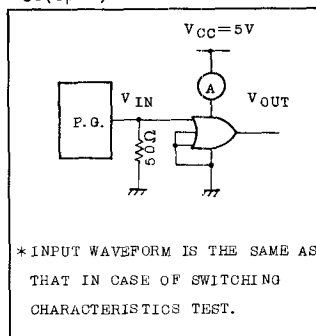
$$I_{CC(opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3 \quad (\text{per gate})$$

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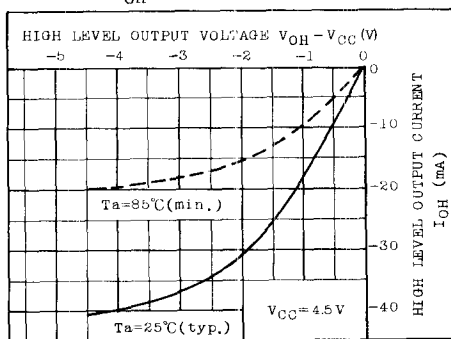
SWITCHING CHARACTERISTICS TEST CIRCUIT



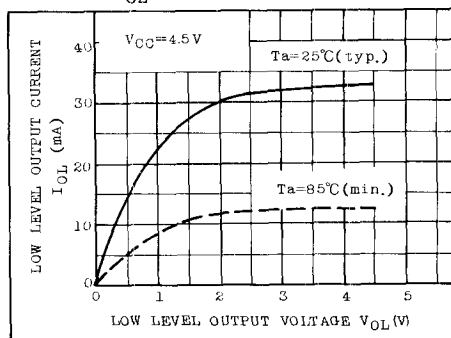
$I_{CC(opr.)}$ TEST CIRCUIT



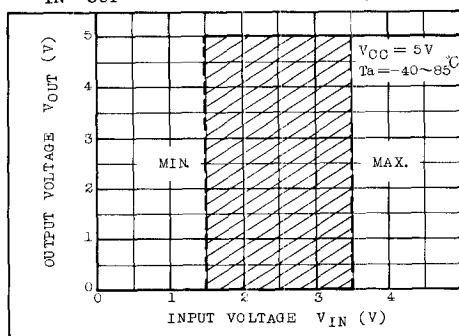
I_{OH} CHARACTERISTICS



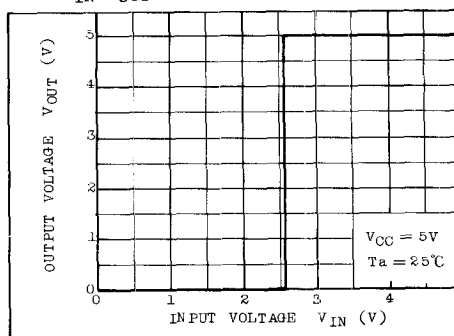
I_{OL} CHARACTERISTICS



V_{IN} - V_{OUT} CHARACTERISTICS (MIN., MAX.)

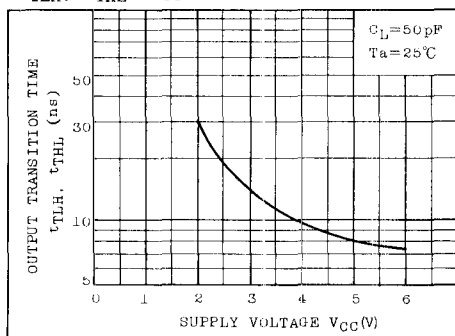


V_{IN} - V_{OUT} CHARACTERISTICS (TYP.)

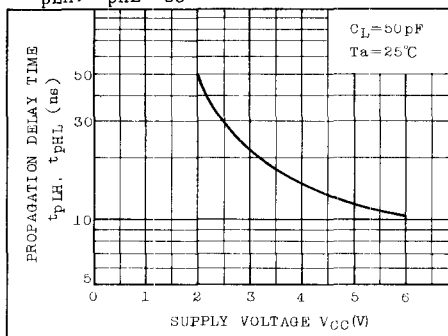


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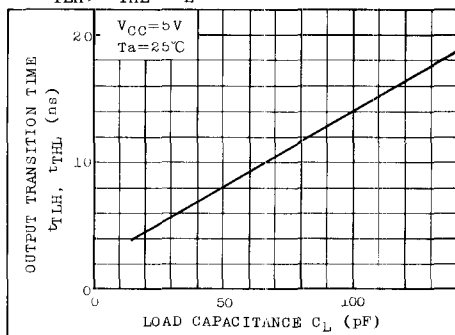
t_{TLH} , t_{THL} - V_{CC} CHARACTERISTICS (TYP.)



t_{PLH} , t_{PHL} - V_{CC} CHARACTERISTICS (TYP.)



t_{TLH} , t_{THL} - C_L CHARACTERISTICS (TYP.)



t_{PLH} , t_{PHL} - C_L CHARACTERISTICS (TYP.)

