

Tiny Integrated Temperature Sensor & Brushless DC Fan Controller with Over-Temperature Alert

Features

- Integrated Temperature Sensing and Multi-speed Fan Control
- Built-in Over Temperature Alert ($T_{\overline{OVER}}$)
- Temperature Proportional Fan Speed Control for Acoustic Noise Reduction and Longer Fan Life
- Pulse Width Modulation Output Drive for Cost and Power Savings
- Solid-State Temperature Sensing
- $\pm 1^{\circ}\text{C}$ (Typical) Accuracy from 25°C to $+70^{\circ}\text{C}$
- 2.8V – 5.5V Operating Range
- TC651 includes Auto Fan Shutdown
- Low Operating Current: 50 μA (Typical)

Applications

- Thermal Protection For Personal Computers
- Digital Set-Top Boxes
- Notebook Computers
- Data Communications
- Power Supplies
- Projectors

Related Literature

- Application Note: AN771

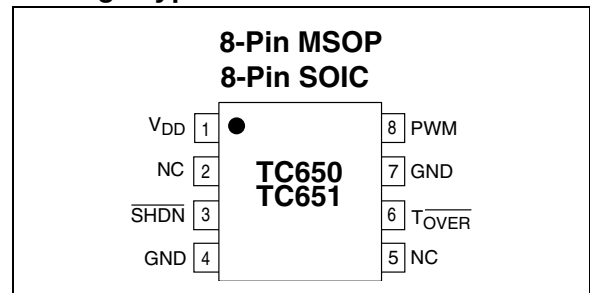
Device Selection Table

Part Number	Package	Temperature Range
TC650XXVUA	8-Pin SOIC	-40°C to $+125^{\circ}\text{C}$
TC651XXVUA	8-Pin MSOP	-40°C to $+125^{\circ}\text{C}$

X	Temperature
A	25
B	30
C	35
D	40
E	45
F	50
G	55

The "X" denotes a suffix for temperature threshold settings. Contact factory for other temperature ranges.

Package Type



General Description

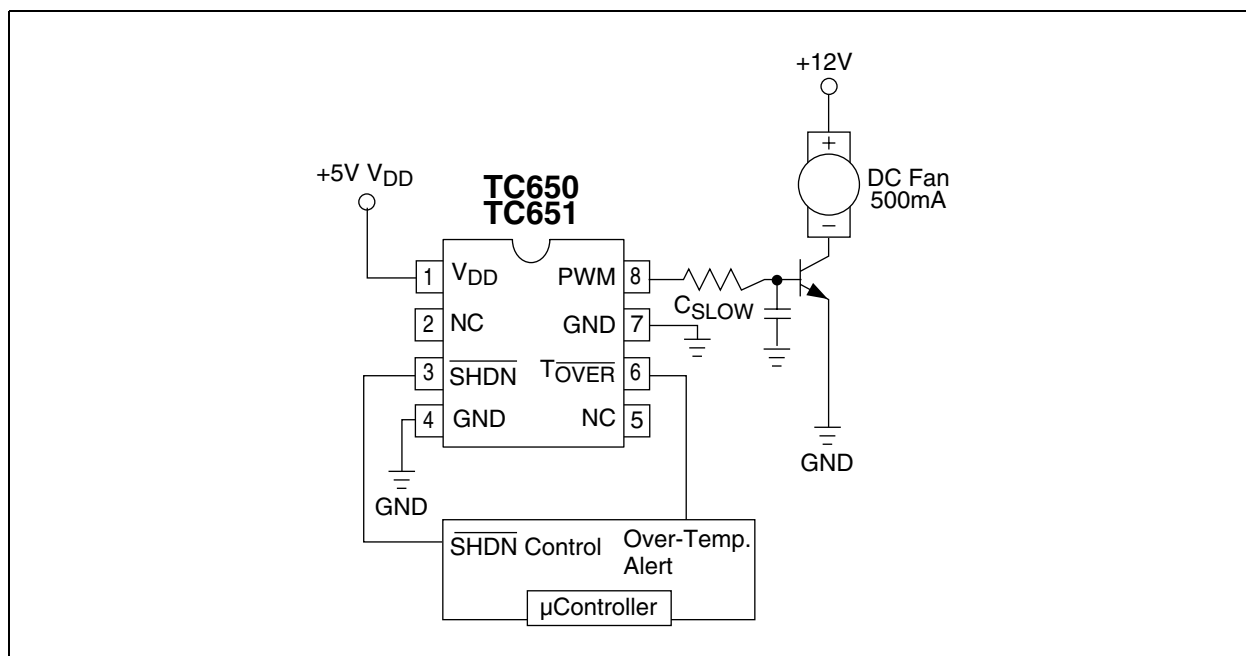
The TC650/TC651 are integrated temperature sensors and brushless DC fan speed controllers. The TC650/TC651 measure their junction temperature and control the speed of the fan based on that temperature, making them especially suited for applications in modern electronic equipment.

Temperature data is converted from the on-chip thermal sensing element and translated into a fractional fan speed from 40% to 100%. A temperature selection guide in the data sheet is used to choose the low and high temperature limits to control the fan. The TC650/TC651 also include a single trip point over temperature alert ($T_{\overline{OVER}}$) that eliminates the need for additional temperature sensors. In addition, the TC651 features an auto fan shutdown function for additional power savings.

The TC650/TC651 are easy to use, require no software overhead and are therefore the ideal choice for implementing thermal management in a variety of systems.

TC650/TC651

Typical Application Diagram



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings*

Input Voltage (V_{DD} to GND)	+6V
Output Voltage (OUT to GND)	6V
Voltage On Any Pin	(GND – 0.3V) to (V_{DD} + 0.3V)
Package Thermal Resistance (θ_{JA})	250°C/W
Operating Temperature Range	-40°C to +125°C
Storage Temperature	-65°C to +150°C

*Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operation sections of the specifications is not implied. Exposure to Absolute Maximum Rating conditions for extended periods may affect device reliability.

TC650/TC651 ELECTRICAL SPECIFICATIONS

Electrical Characteristics: $V_{DD} = 2.8V$ to $5.5V$, $\overline{SHDN} = V_{DD}$, $T_A = -40^\circ C$ to $125^\circ C$ unless otherwise specified.						
Symbol	Parameter	Min	Typ.	Max	Units	Test Conditions
V_{DD}	Supply Voltage	2.8	—	5.5	V	
I_{DD}	Supply Current	—	50	90	μA	PWM, $\overline{T_{OVER}}$ are open
$\overline{SHDN}$ Input						
V_{IH}	$\overline{SHDN}$ Input High Threshold	65	—	—	% V_{DD}	
V_{IL}	$\overline{SHDN}$ Input Low Threshold	—	—	15	% V_{DD}	
PWM Output						
V_{OL}	PWM Output Low Voltage	—	—	0.3	V	$I_{SINK} = 1mA$
V_{OH}	PWM Output High Voltage	$V_{DD} - 0.5$	—	—	V	$I_{SOURCE} = 5mA$
t_R	PWM Rise Time	—	10	—	μsec	$I_{OH} = 5mA$, 1nF from PWM to GND
t_F	PWM Fall Time	—	10	—	μsec	$I_{OL} = 1mA$, 1nF from PWM to GND
f_{OUT}	PWM Frequency	10	15	—	Hz	
$t_{STARTUP}$	Start-up Time	—	$32/f_{OUT}$	—	sec	V_{DD} Rises from GND, or $\overline{SHDN}$ Released
Temperature Accuracy						
$T_{H\ ACC}$	High Temperature Accuracy	$T_H - 3$	T_H	$T_H + 3$	$^\circ C$	Note 1
$(T_H - T_L)_{ACC}$	Temp. Range Accuracy	-1.0	—	+1.0	$^\circ C$	$(T_H - T_L) \leq 20^\circ C$
		-2.5	—	+2.5	$^\circ C$	$(T_H - T_L) \geq 20^\circ C$
T_{HYST}	Auto-shutdown Hysteresis	—	$(T_H - T_L)/5$	—	$^\circ C$	TC651 Only
$\overline{T_{OVER}}$ Output						
V_{HIGH}	$\overline{T_{OVER}}$ Output High Voltage	$V_{DD} - 0.5$	—	—	V	$I_{SOURCE} = 1.2mA$
V_{LOW}	$\overline{T_{OVER}}$ Output Low Voltage	—	—	0.4	V	$I_{SINK} = 2.5mA$
$\overline{T_{OVER\ ACC}}$	Absolute Accuracy	—	$T_H + 10$	—	$^\circ C$	At Trip Point
$\overline{T_{OVER\ HYST}}$	Trip Point Hysteresis	—	5	—	$^\circ C$	

Note 1: Transition from 90% to 100% Duty Cycle.

TC650/TC651

2.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 2-1.

TABLE 2-1: PIN FUNCTION TABLE

Pin No. (8-Pin MSOP) (8-Pin SOIC)	Symbol	Description
1	V _{DD}	Power Supply Input. May be independent of fan power supply.
2	NC	No Internal Connect.
3	SHDN	Fan Shutdown, Active-Low Input. During Shutdown mode the chip still monitors temperature and T _{OVER} is low if temperature rises above factory set point.
4	GND	Ground. Ground return for all TC650/TC651 functions.
5	NC	No Connect.
6	T _{OVER}	Over-Temperature Alert, Active-Low Output.
7	GND	Ground.
8	PWM	PWM Fan Drive Output. Pulse width modulated rail-to-rail logic output. Nominal Frequency is 15Hz.

3.0 DETAILED DESCRIPTION

The TC650/TC651 acquire and convert their junction temperature (T_J) information from an on-chip solid-state sensor with a typical accuracy of $\pm 1^\circ\text{C}$. The temperature data is digitally stored in an internal register. The register is compared with pre-defined threshold values. The six threshold values are equally distributed over a pre-defined range of temperatures (see Table 3-1 and Table 3-2). The TC650/TC651 control the speed of a DC brushless fan using a fractional speed control scheme. The output stage requires only a 2N2222-type small-signal BJT for fans up to 300mA. For larger current fans (up to 1 Amp) a logic-level N-channel MOSFET may be used. In addition to controlling the speed of the fan, the TC650/TC651 include an on-chip over-temperature alarm (T_{OVER}) that gives a low signal when the temperature of the chip exceeds T_H by 10°C , (typical). This feature eliminates the need for a separate temperature sensor for over-temperature monitoring.

TABLE 3-1: TEMPERATURE RANGE DEFINITION FOR TC650 (MINIMUM-SPEED MODE)

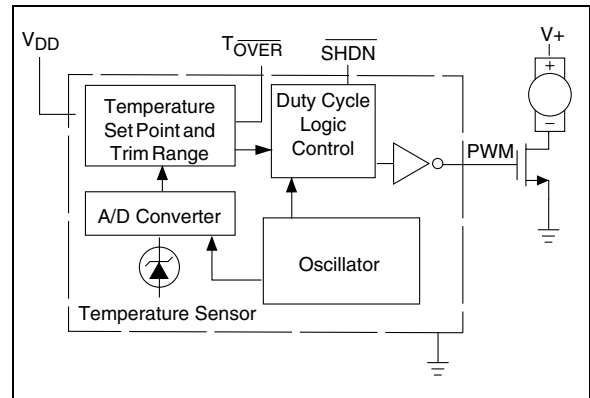
Temperature ($T = T_J$)	PWM Duty Cycle
$T < T_L$	40%
$T_L \leq T < T_1$	50%
$T_1 \leq T < T_2$	60%
$T_2 \leq T < T_3$	70%
$T_3 \leq T < T_4$	80%
$T_4 \leq T < T_H$	90%
$T_H \leq T < T_{\text{OV}}$	100%
$T_{\text{OV}} \leq T$	100% with Over Temp Alert ($T_{\text{OVER}} = L$)

TABLE 3-2: TEMPERATURE RANGE DEFINITION FOR TC651 (AUTO-SHUTDOWN MODE)

Temperature ($T = T_J$)	PWM Duty Cycle
$T < T_L$	"OFF"
$T_L \leq T < T_1$	50%
$T_1 \leq T < T_2$	60%
$T_2 \leq T < T_3$	70%
$T_3 \leq T < T_4$	80%
$T_4 \leq T < T_H$	90%
$T_H \leq T < T_{\text{OV}}$	100%
$T_{\text{OV}} \leq T$	100% with Over-Temp Alert ($T_{\text{OVER}} = L$)

Note: The temperature regions defined by the six temperature thresholds are pre-defined in the TC650/TC651 by means of trimming. Once a T_L and T_H are programmed, the $T_1 - T_4$ thresholds are automatically equally spaced between T_L and T_H .

FIGURE 3-1: FUNCTIONAL BLOCK DIAGRAM



3.1 PWM Output

The PWM pin is designed to drive a low cost transistor or MOSFET as the low side power switching element in the system. This output has an asymmetric complementary drive and is optimized for driving NPN transistors or N-channel MOSFETs. Since the system relies on PWM rather than linear power control, the dissipation in the power switch is kept to a minimum. Generally, very small devices (TO-92 or SOT packages) will suffice. The frequency of the PWM is about 15Hz. The PWM is also the time base for the Start-up Timer (see paragraphs below). The PWM duty cycle has a range of 40% to 100% for the TC650 and 50% to 100% for the TC651.

3.2 Start-Up Timer

To ensure reliable fan start-up, the Start-up Timer turns PWM high for about 2 seconds whenever the fan is started from the off state. This occurs at power-up and when coming out of Shutdown mode.

3.3 Over-Temperature Alert (T_{OVER})

This pin goes low when the T_H set point is exceeded by 10°C (typical). This indicates that the fan is at maximum drive and the potential exists for system overheating; either heat dissipation in the system has gone beyond the cooling system's design limits, or some FAULT exists such as fan bearing failure or an airflow obstruction. This output may be treated as a "System Overheat" warning and used to trigger system shutdown, or bring other fans to full speed in the system. The fan will continue to run at 100% speed while T_{OVER} is asserted. Built-in hysteresis prevents T_{OVER} from "chattering" when measured temperature is at or near the $T_H + 10^\circ\text{C}$ trip point. As temperature falls through the $T_H + 10^\circ\text{C}$ trip point, hysteresis maintains the T_{OVER} output low until measured temperature is 5°C above the trip point setting.

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3.4 Shutdown (SHDN)

The fan can be unconditionally shutdown by pulling low the SHDN pin. During shutdown, the PWM output is low. This is ideal for notebook computers and other portable applications when you need to change batteries and must not have the fan running at that time. Thermal monitoring and T_{OVER} are still in operation during shutdown. I_{DD} shutdown current is around 50 μ A.

3.5 Auto-Shutdown Mode

The TC651 features auto-shutdown; when the temperature is below the factory set point at minimum speed (T_L), PWM is low and the fan is automatically shut off (Auto-shutdown mode). This feature is ideal for notebook computers and other portable equipment that need to conserve as much battery power as possible and thus run a fan when it is only absolutely needed. The TC651 will continue to be active so as to monitor temperature for Tover. The TC651 exits Auto-shutdown mode when the temperature rises above the factory set point (T_1).

3.6 Temperature Selection Guide (Minimum Fan Speed/Full Speed)

The five temperature regions defined by the six thresholds are defined in the TC650/TC651 by means of factory trimming. Once a T_L and T_H are set, the $T_1 - T_4$ thresholds are automatically equally spaced between T_L and T_H .

Temp. Range	T_L	T_H	Part #
10°C	25	35	AC
	30	40	BD
	35	45	CE
	25	40	AD
15°C	30	45	BE
	35	50	CF
20°C	25	45	AE
	30	50	BF
	35	55	CG
	25	55	AG

Note: The **Bold Type** temperature settings are available for ordering. Contact factory for other temperature selections.

T_L and T_H can be selected in 5°C increments. T_H must be chosen at least 10°C higher than T_L . T_L can range anywhere from 25°C to 35°C.

As an example, suppose you wanted the fan to **run at 40% speed at 25°C** or less and go to **full speed at 45°C**. You would order the part number TC650AEVUA.

As another example, suppose you wanted the fan to **turn on at 30°C** and go to **full speed at 45°C**. You would order the part number TC651BEVUA.

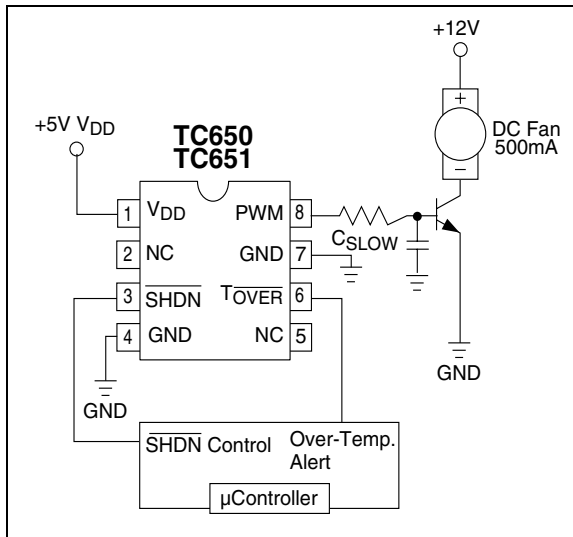
4.0 TYPICAL APPLICATIONS

4.1 Reducing Switching Noise

For fans consuming more than 300mA, a slowdown capacitor (C_{SLOW}) is recommended for reducing switching PWM induced noise (see Figure 4-1). The value of this capacitor should be 4.7 μ F to 47 μ F, depending on the fan current consumption.

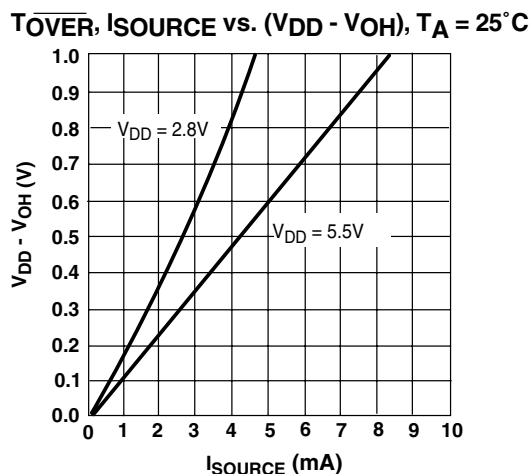
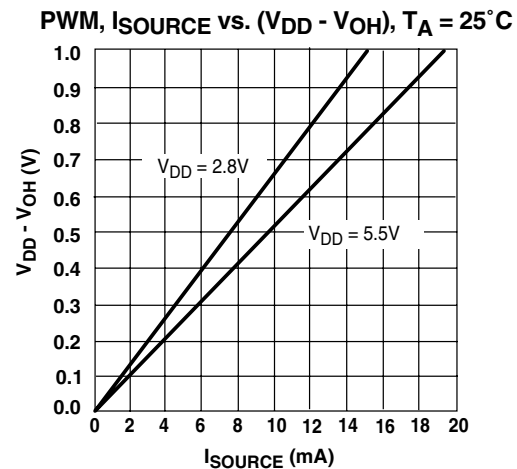
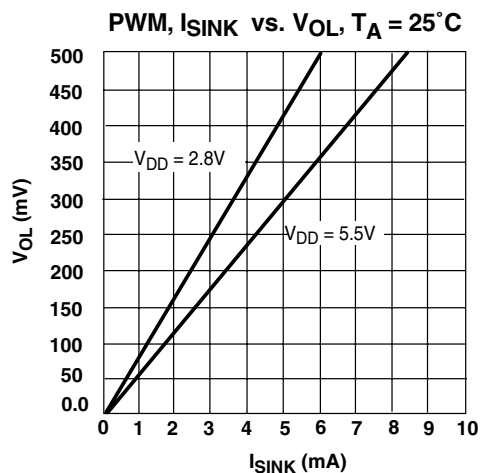
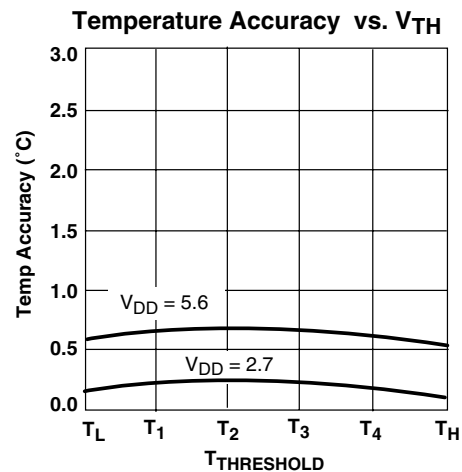
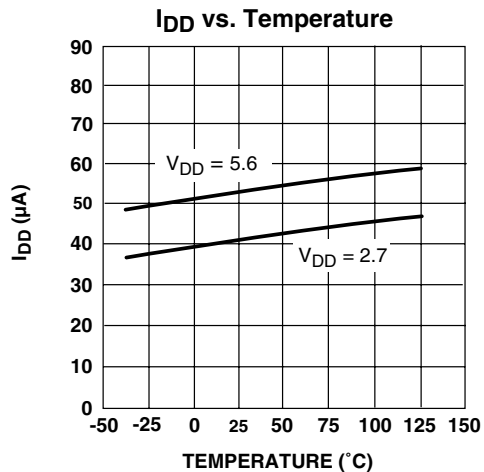
See Application Note AN771 "Suppressing Acoustic Noise in PWM Fan Speed Control Systems" for more information.

FIGURE 4-1: REDUCING SWITCHING NOISE



5.0 TYPICAL CHARACTERISTICS

The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range



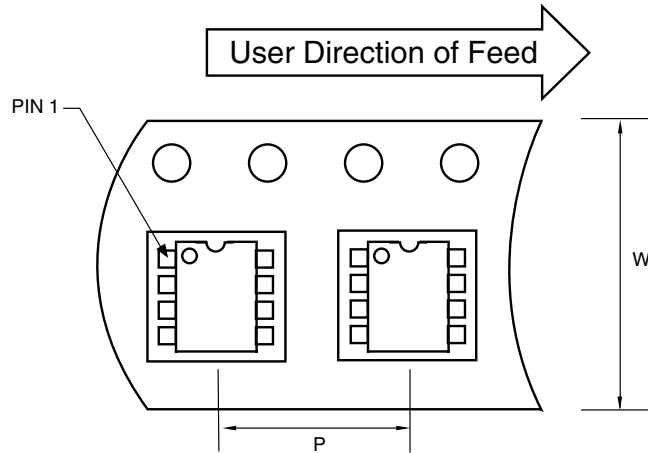
6.0 PACKAGING INFORMATION

6.1 Package Marking Information

Package marking data not available at this time.

6.2 Taping Form

Component Taping Orientation for 8-Pin MSOP Devices

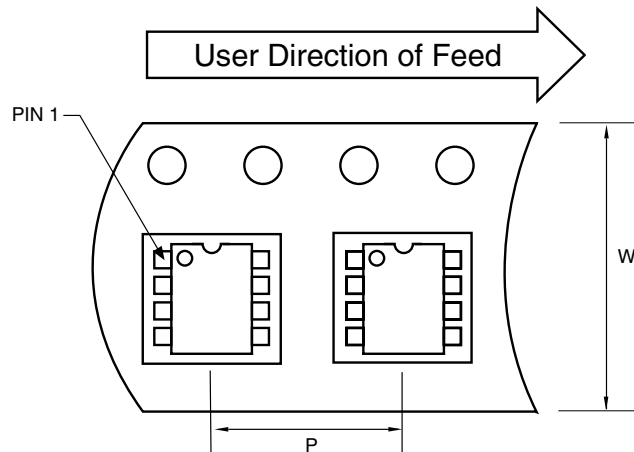


Standard Reel Component Orientation
for TR Suffix Device

Carrier Tape, Number of Components Per Reel and Reel Size

Package	Carrier Width (W)	Pitch (P)	Part Per Full Reel	Reel Size
8-Pin MSOP	12 mm	8 mm	2500	13 in

Component Taping Orientation for 8-Pin SOIC (Narrow) Devices



Standard Reel Component Orientation
for TR Suffix Device

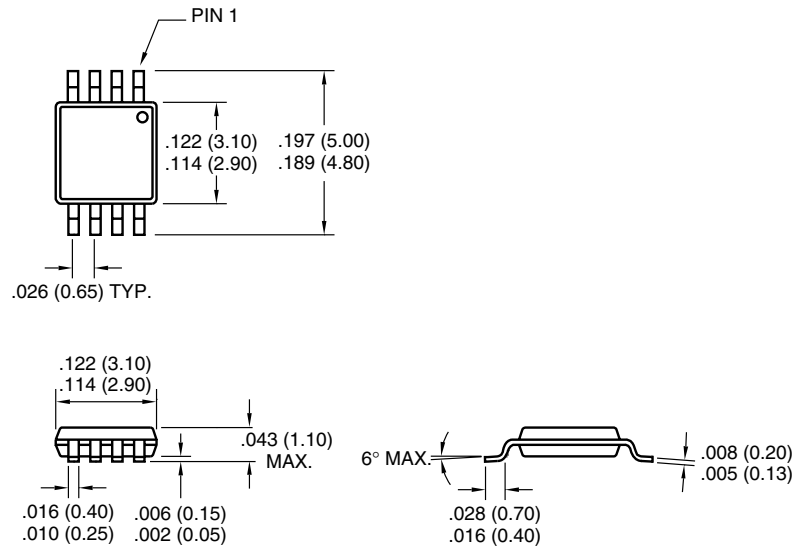
Carrier Tape, Number of Components Per Reel and Reel Size

Package	Carrier Width (W)	Pitch (P)	Part Per Full Reel	Reel Size
8-Pin SOIC (N)	12 mm	8 mm	2500	13 in

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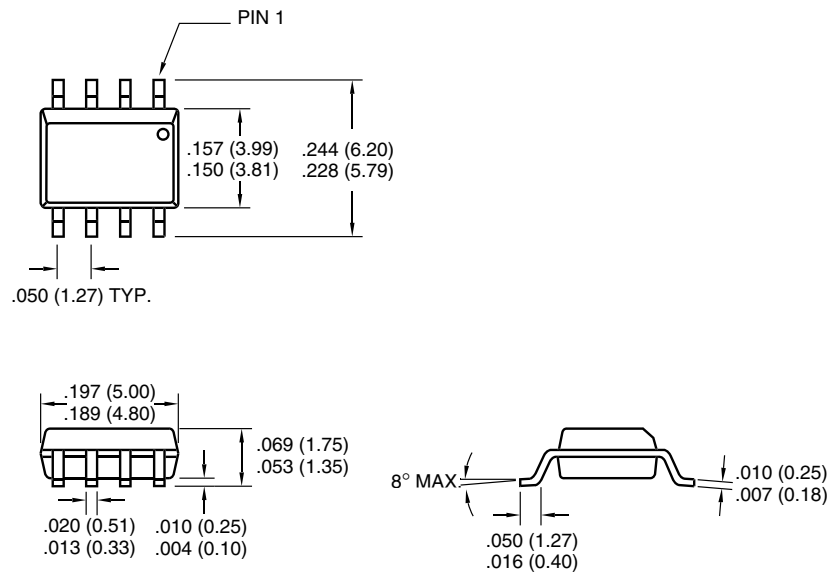
6.3 Package Dimensions

8-Pin MSOP



Dimensions: inches (mm)

8-Pin SOIC



Dimensions: inches (mm)

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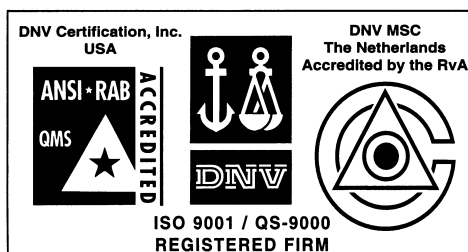
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