



MM54HC02/MM74HC02 Quad 2-Input NOR Gate

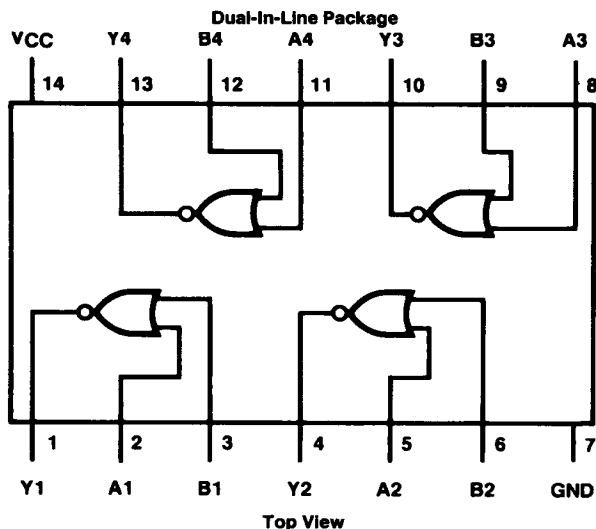
General Description

These NOR gates utilize advanced silicon-gate CMOS technology to achieve operating speeds similar to LS-TTL gates with the low power consumption of standard CMOS integrated circuits. All gates have buffered outputs, providing high noise immunity and the ability to drive 10 LS-TTL loads. The 54HC/74HC logic family is functionally as well as pin-out compatible with the standard 54LS/74LS logic family. All inputs are protected from damage due to static discharge by internal diode clamps to V_{CC} and ground.

Features

- Typical propagation delay: 8 ns
- Wide power supply range: 2–6V
- Low quiescent supply current: 20 μ A maximum (74HC Series)
- Low input current: 1 μ A maximum
- High output current: 4 mA minimum

Connection and Logic Diagrams



TL/F/5294-1

Order Number MM54HC02* or MM74HC02*

*Please look into Section 8, Appendix D for availability of various package types.



TL/F/5294-2