



MIC94300

200mA Switch with Ripple Blocker™ Technology



General Description

The MIC94300 is an integrated load switch that incorporates Micrel's Ripple Blocker™ active filter technology. The MIC94300 provides high-frequency ripple attenuation (switching noise rejection) for applications where a switching noise cannot be tolerated by sensitive downstream circuits such as in RF applications. A low-voltage logic enable pin disconnects the pass element and places the MIC94300 into a low current-shutdown state when disabled.

The MIC94300 operates from an input voltage of 1.8V to 3.6V, allowing true load switching of low-voltage power rails in any electronic device. The output voltage (V_{OUT}) is set at a fixed drop (typically 170mV) from the input voltage ($V_{OUT} = V_{IN} - 170\text{mV}$). This maintains high efficiency independent of given load conditions and currents.

Packaged in a 0.88mm x 0.88mm 4-ball CSP or 4-pin 1.2mm x 1.6mm Thin MLF® package, the MIC94300 has a junction operating temperature range of -40°C to +125°C.

Data sheets and support documentation can be found on Micrel's web site at: www.micrel.com.

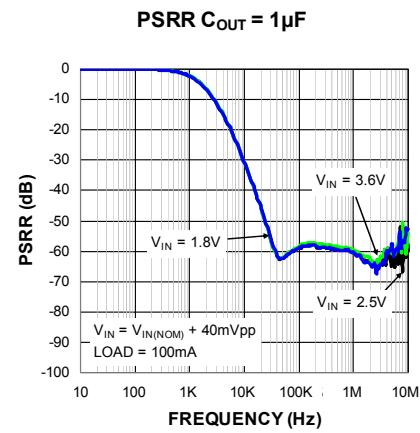
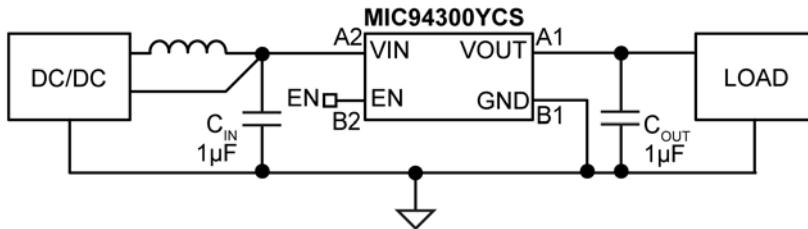
Features

- 1.8V to 3.6V input voltage range
- Active noise rejection over a wide frequency band
 - >60dB from 40kHz to 5MHz
- Rated to 200mA output current
- Current-limit and thermal-limit protected
- Ultra-small 0.88mm x 0.88mm 4-ball CSP
- 1.2mm x 1.6mm, 4-pin Thin MLF®
- Logic-controlled enable pin
- -40°C to $+125^{\circ}\text{C}$ junction temperature range

Applications

- Smart phones
- Tablet PC/notebooks and webcams
- Digital still and video cameras
- Videoconferencing
- Bar-code scanners
- Global positioning systems
- Automotive and industrial applications

Typical Application



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MLF and *MicroLeadFrame* are registered trademarks of Amkor Technology, Inc.

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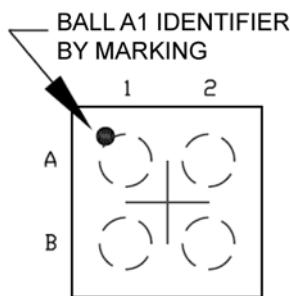
Ordering Information

Part Number	Marking Code	Package	Lead Finish
MIC94300YCS	AF	0.88mm × 0.88mm WLCSP	Pb-Free
MIC94300YMT ^{1,2}	43Z	1.2mm × 1.6mm Thin MLF®	Pb-Free

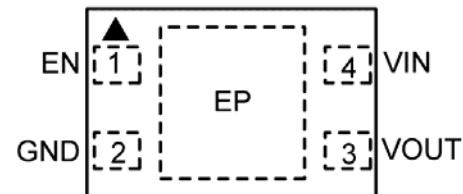
Notes:

1. Thin MLF® ▲ = Pin 1 identifier.
2. Thin MLF® is a GREEN RoHS-compliant package. Lead finish is NiPdAu. Mold compound is Halogen Free.

Pin Configuration



Top View
4-Ball 0.88mm × 0.88mm CSP (CS)

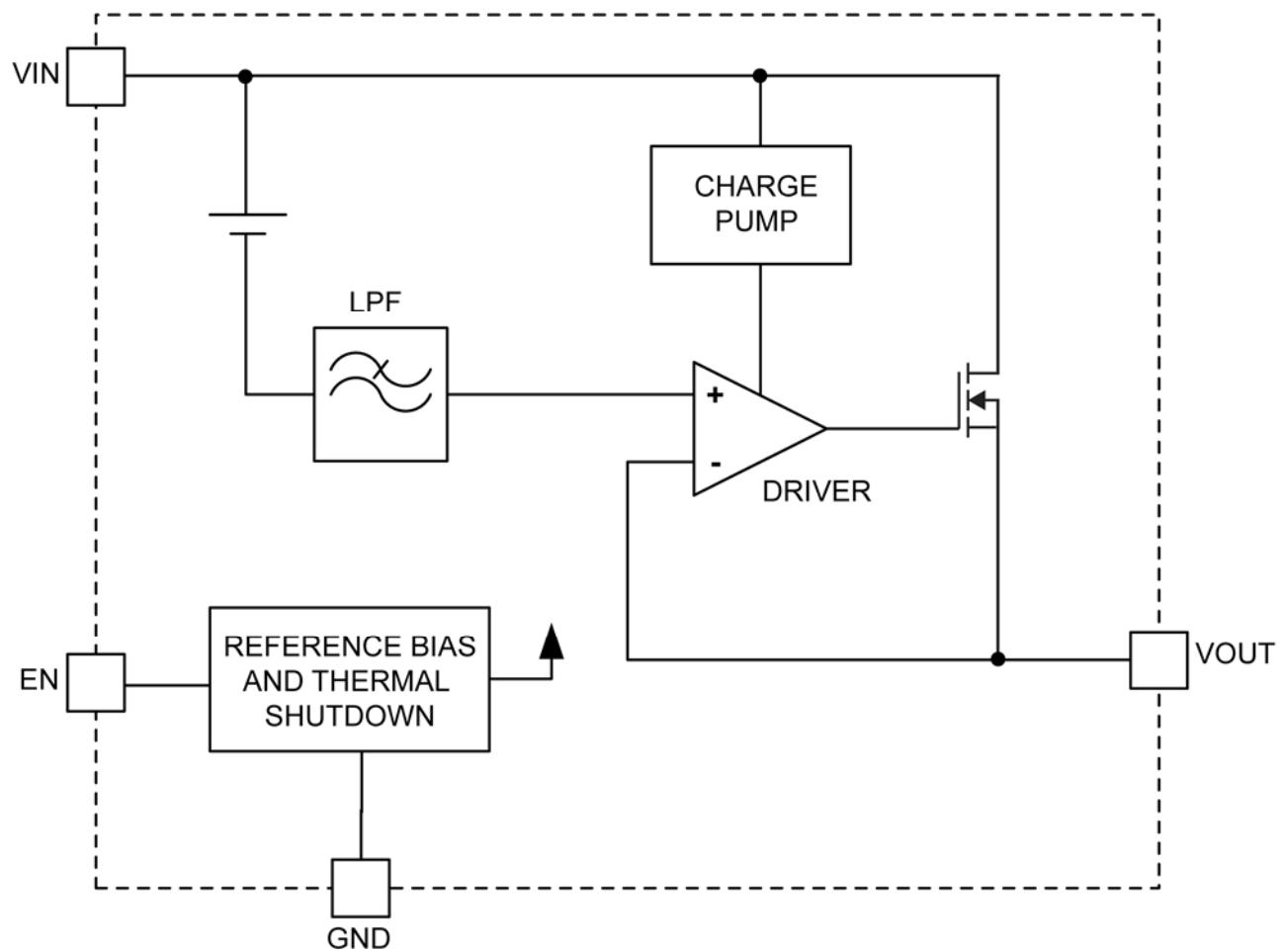


Top View
4-Pin 1.2mm × 1.6mm Thin MLF® (MT)

Pin Description

Pin Number (Thin MLF®)	Ball Number (CSP)	Pin Name	Pin Name
1	B2	EN	Enable input. A logic HIGH signal on this pin enables the part. Logic LOW disables the output. Do not leave floating.
2	B1	GND	Ground.
3	A1	VOUT	Power switch output.
4	A2	VIN	Power switch input and chip supply.
EP	–	ePad	Exposed Heatsink Pad. Connect to Ground for best thermal performance.

Functional Block Diagram



Absolute Maximum Ratings⁽¹⁾

Input Voltage (V_{IN})	-0.3V to +4V
Output Voltage (V_{OUT})	-0.3V to +4V
Enable Voltage (V_{EN})	-0.3V to V_{IN} + 0.3V or +4V
Lead Temperature (soldering, 10s)	260°C
Storage Temperature (T_S)	-65°C to +150°C
ESD Rating ⁽³⁾	3kV

Operating Ratings⁽²⁾

Input Voltage (V_{IN})	+1.8V to +3.6V
Enable Voltage (V_{EN})	0V to V_{IN}
Junction Temperature (T_J)	-40°C to +125°C
Junction Thermal Resistance	
0.88mm x 0.88mm WLCSP (θ_{JA})	250°C/W
1.2mm x 1.6mm Thin MLF® (θ_{JA})	173°C/W

Electrical Characteristics⁽⁴⁾

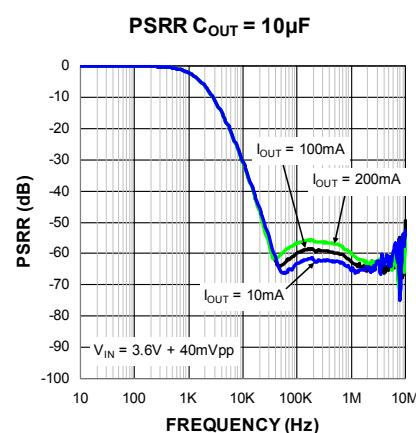
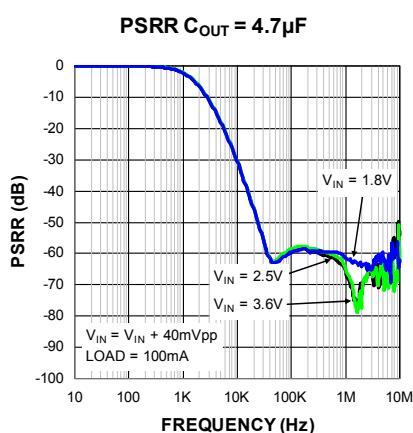
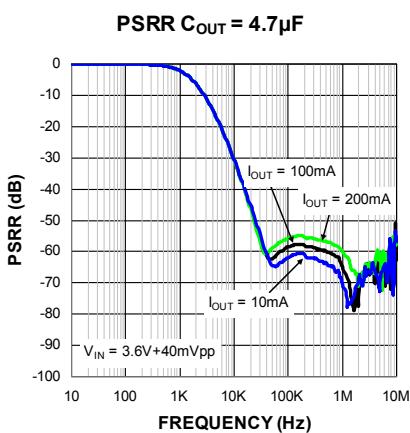
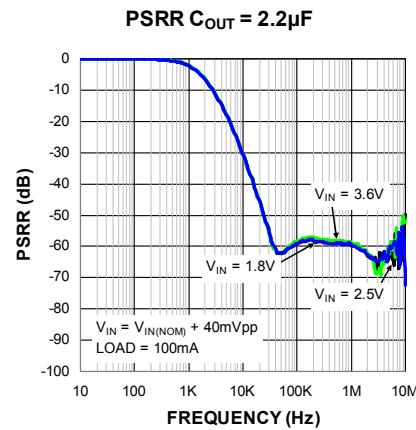
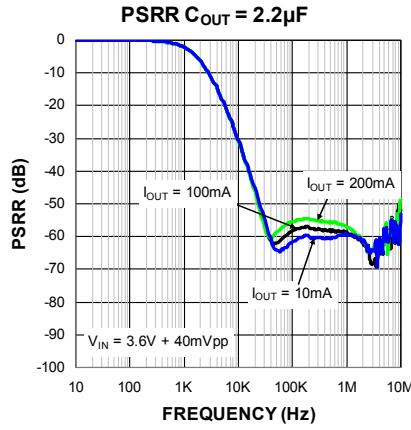
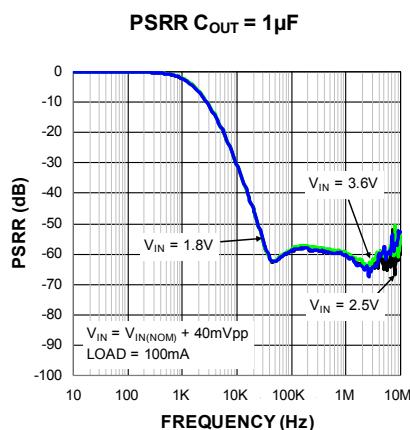
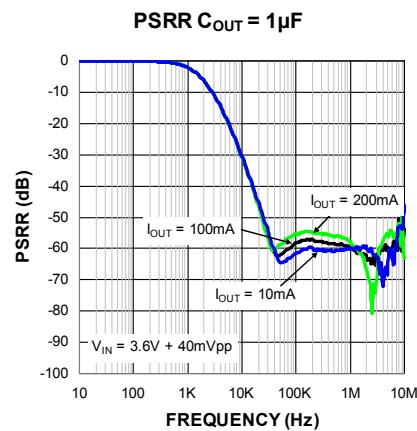
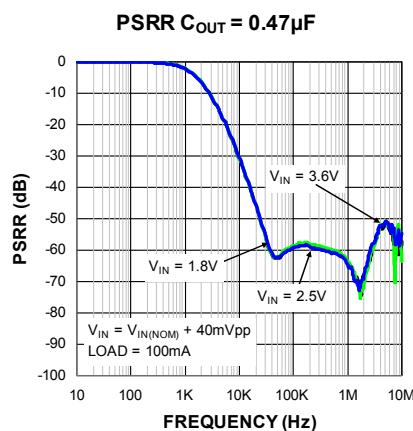
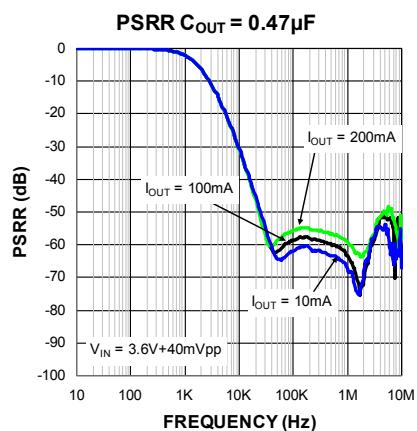
$V_{IN} = V_{EN} = 3.6V$; $I_{OUT} = 100\mu A$; $C_{OUT} = 1\mu F$; $T_A = 25^\circ C$, **bold** values indicate $-40^\circ C \leq T_J \leq +125^\circ C$, unless noted.

Parameter	Condition	Min.	Typ.	Max.	Units
Input Voltage		1.8		3.6	V
Voltage Drop	$V_{IN} - V_{OUT}$, $-40^\circ C \leq T_J \leq +85^\circ C$		170	250	mV
V_{IN} Ripple Rejection (PSRR)	$f = 20\text{kHz}$, $I_{OUT} = 100\text{mA}$		45		dB
	$f = 100\text{kHz}$ to 5MHz , $I_{OUT} = 100\text{mA}$		60		
Total Output Noise	$f = 10\text{Hz}$ to 100kHz		98		μV_{RMS}
Current Limit	$V_{OUT} = 0V$	200	315	400	mA
Turn-On Time	EN controlled		40	150	μs
Load Regulation	100 μA to 100mA		10		mV
Ground Current	$I_{OUT} = 100\mu\text{A}$		138	200	μA
Shutdown Current	$V_{EN} = 0V$		0.2		μA
Enable					
Input Logic LOW				0.4	V
Input Logic HIGH		1.0			V
Input Current			0.01	1	μA

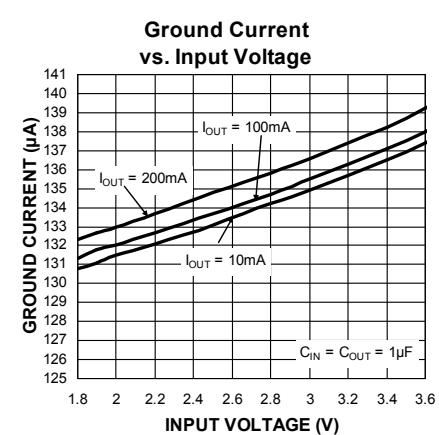
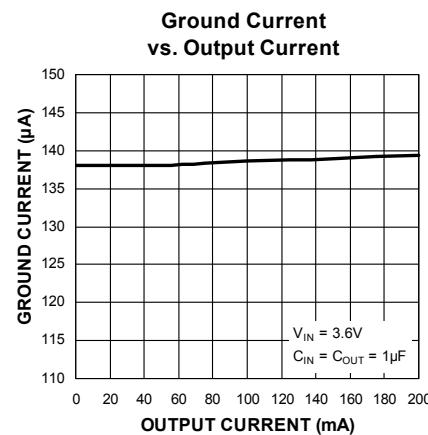
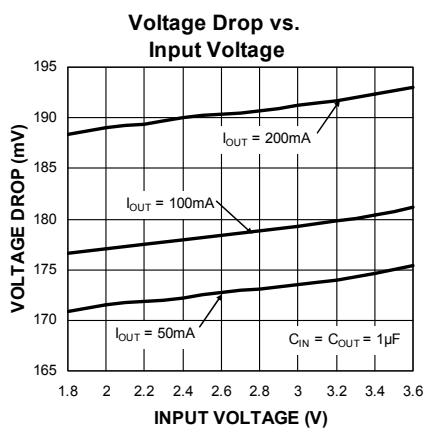
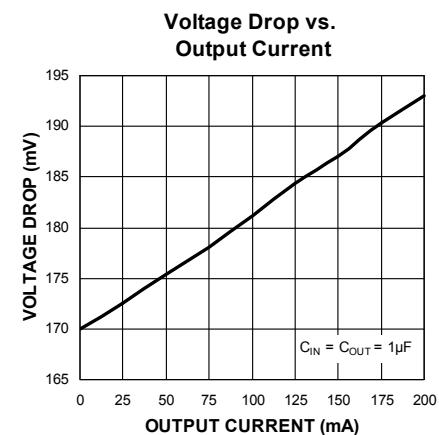
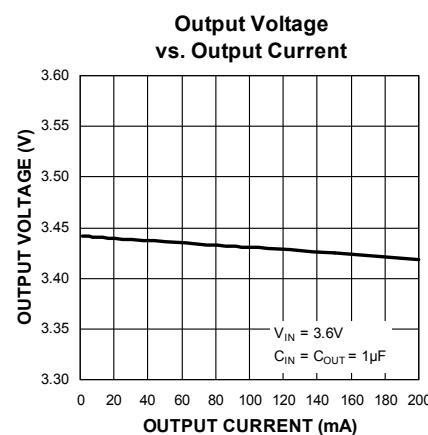
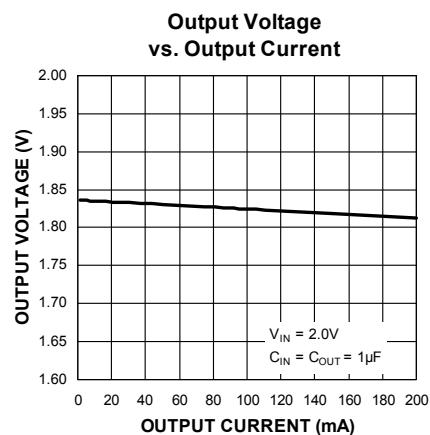
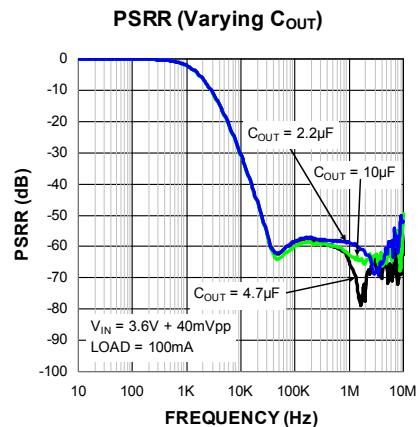
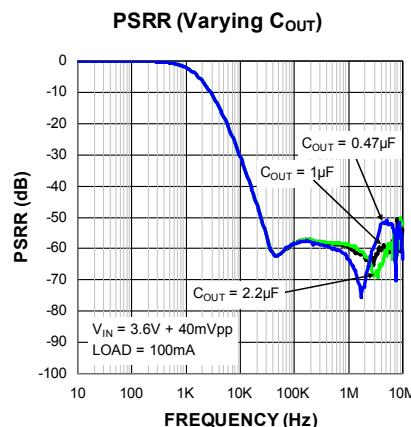
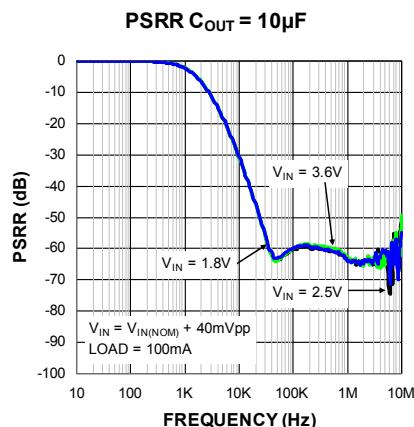
Notes:

1. Exceeding the absolute maximum rating may damage the device.
2. The device is not guaranteed to function outside its operating rating.
3. Devices are ESD sensitive. Handling precautions recommended. Human body model, $1.5\text{k}\Omega$ in series with 100pF .
4. Specification for packaged product only.

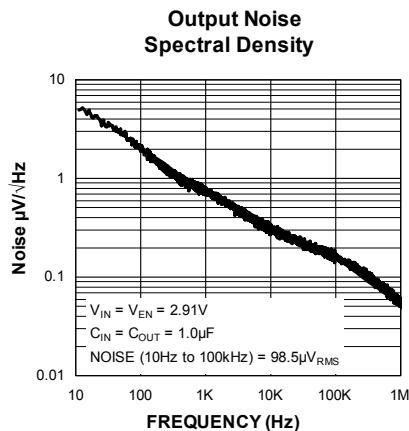
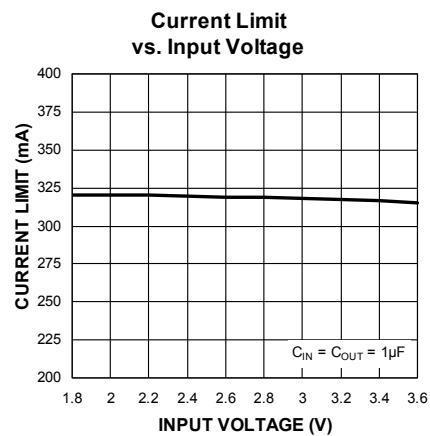
Typical Characteristics



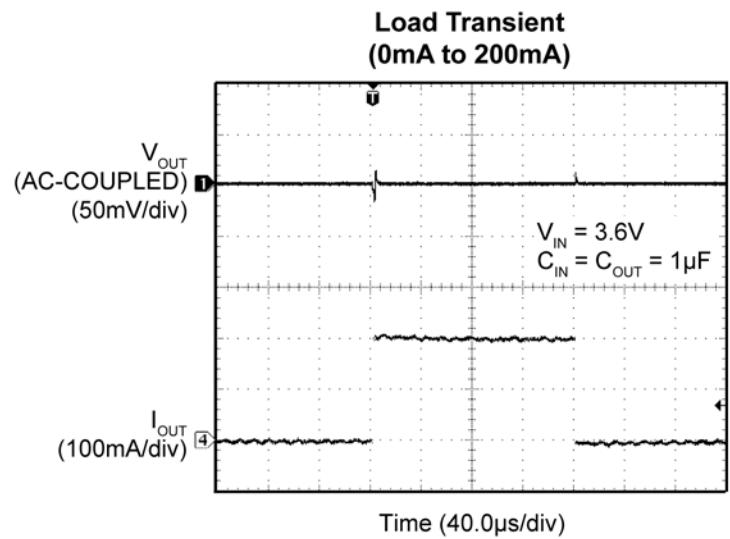
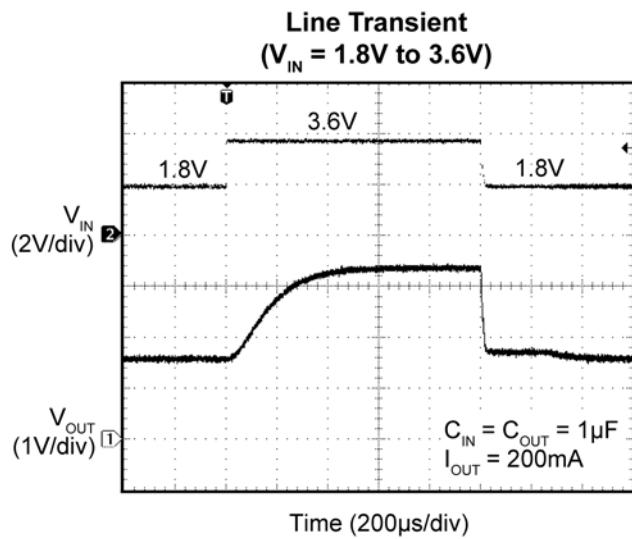
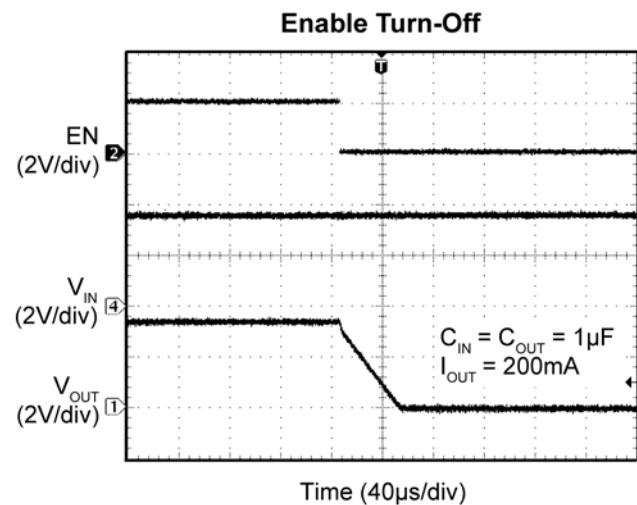
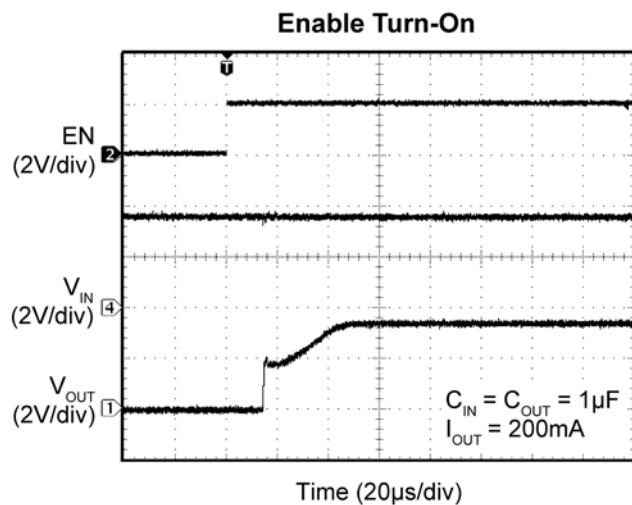
Typical Characteristics (Continued)



Typical Characteristics (Continued)



Functional Characteristics



Application Information

The MIC94300 utilizes Ripple Blocker™ technology to integrate a load switch with a high-performance active filter. The MIC94300 includes a low-voltage logic enable pin, and is fully protected from damage due to fault conditions, offering linear current limiting and thermal shutdown.

Input Capacitor

The MIC94300 is a high-performance, high-bandwidth device. An input capacitor of 470nF is required from the input to ground to provide stability. Low-ESR ceramic capacitors provide optimal performance at a minimum of space. Additional high-frequency capacitors, such as small-valued NPO dielectric-type capacitors, help filter out high-frequency noise and are good practice in any RF-based circuit. X5R or X7R dielectrics are recommended for the input capacitor. Y5V dielectrics lose most of their capacitance over temperature and are therefore, not recommended.

Output Capacitor

The MIC94300 requires an output capacitor of 0.47μF or greater to maintain stability. For optimal ripple rejection performance a 1μF capacitor is recommended. The design is optimized for use with low-ESR ceramic-chip capacitors. High-ESR capacitors are not recommended because they may cause high-frequency oscillation. The output capacitor can be increased, but performance has been optimized for a 1μF ceramic output capacitor and does not improve significantly with larger capacitance.

X7R/X5R dielectric type ceramic capacitors are recommended because of their temperature performance. X7R type capacitors change capacitance by 15% over their operating temperature range and are the most stable type of ceramic capacitors. Z5U and Y5V dielectric capacitors change value by as much as 50% and 60%, respectively, over their operating temperature ranges. To use a ceramic-chip capacitor with Y5V dielectric, the value must be much higher than an X7R ceramic capacitor to ensure the same minimum capacitance over the equivalent operating temperature range.

No Load Stability

The MIC94300 will remain stable with no load. This is especially important in CMOS RAM keep-alive applications.

Enable/Shutdown

The MIC94300 comes with an active-high enable pin that allows the Ripple Blocker™ to be disabled. Forcing the enable pin low disables the MIC94300 and sends it into a “zero” off mode current state. In this state, current consumed by the MIC94300 goes nearly to zero. Forcing the enable pin high enables the output voltage. The active-high enable pin uses CMOS technology and the enable pin cannot be left floating; a floating enable pin may cause an indeterminate state on the output.

Thermal Considerations

The MIC94300 is designed to provide 200mA of continuous current in a very-small package. Maximum ambient operating temperature can be calculated based on the output current and the voltage drop across the part which is fixed at 170mV typical, 250mV worst case. For example if the input voltage is 2.75V, the output voltage is 2.5V, and the output current = 200mA. The actual power dissipation of the Ripple Blocker™ can be determined using the equation:

$$P_D = (V_{IN} - V_{OUT1}) I_{OUT} + V_{IN} I_{GND}$$

Because this device is CMOS and the ground current is typically <100μA over the load range, the power dissipation contributed by the ground current is <1% and can be ignored for this calculation:

$$P_D = (2.75V - 2.5V) \times 200mA$$

$$P_D = 0.05W$$

To determine the maximum ambient operating temperature of the package, use the junction-to-ambient thermal resistance of the device and the following basic equation:

$$P_{D(MAX)} = \left(\frac{T_{J(MAX)} - T_A}{\theta_{JA}} \right)$$

$T_{J(MAX)} = 125^\circ\text{C}$, the maximum junction temperature of the die, θ_{JA} thermal resistance = 250°C/W for the YCS package and 173°C/W for the YMT package.

Substituting P_D for $P_{D(\max)}$ and solving for the ambient operating temperature will give the maximum operating conditions for the regulator circuit.

The maximum power dissipation must not be exceeded for proper operation.

For example, when operating the MIC94300YMT at an input voltage of 2.75V and 200mA load with a minimum footprint layout, the maximum ambient operating temperature T_A can be determined as follows:

$$0.05W = (125^{\circ}\text{C} - T_A)/(173^{\circ}\text{C}/W)$$

$$T_A = 116^{\circ}\text{C}$$

Therefore the maximum ambient operating temperature of 116°C is allowed in a 1.2mm x 1.6mm MLF® package. For a full discussion of heat sinking and thermal effects on voltage regulators, refer to the "Regulator Thermals" section of *Micrel's Designing with Low-Dropout Voltage Regulators* handbook. This information can be found on Micrel's website at:

http://www.micrel.com/_PDF/other/LDOBk_ds.pdf

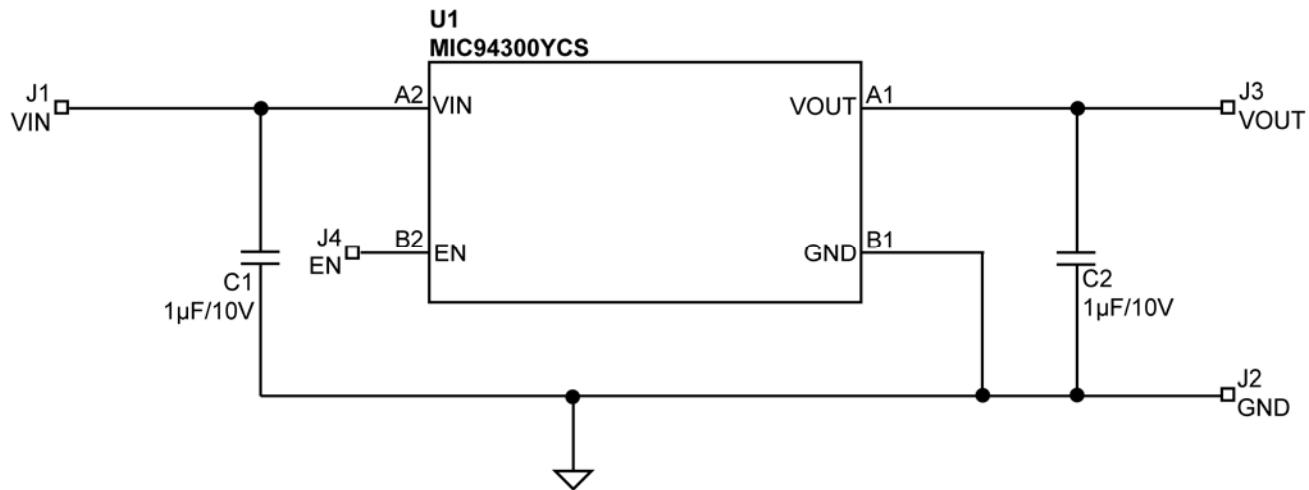
For more information about Micrel's Ripple Blocker™ products, go to:

<http://www.micrel.com/rippleblocker/>

<http://www.micrel.com/page.do?page=/product-info/products/mic94300.jsp>

<http://www.micrel.com/page.do?page=/product-info/products/mic94310.jsp>

Evaluation Board Schematic



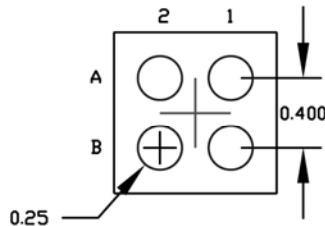
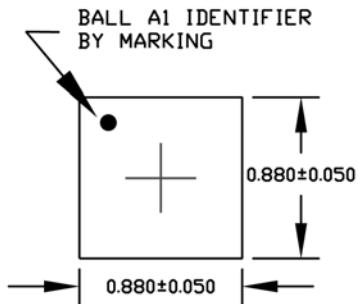
Bill of Materials

Item	Part Number	Manufacturer	Description	Qty.
C1, C2	GRM155R61A105KE15D	Murata ⁽¹⁾	Capacitor, 1µF Ceramic, 10V, X7R, Size 0402	2
U1	MIC94300YCS	Micrel, Inc.⁽²⁾	200mA Ripple Blocker™ Voltage Follower	1

Notes:

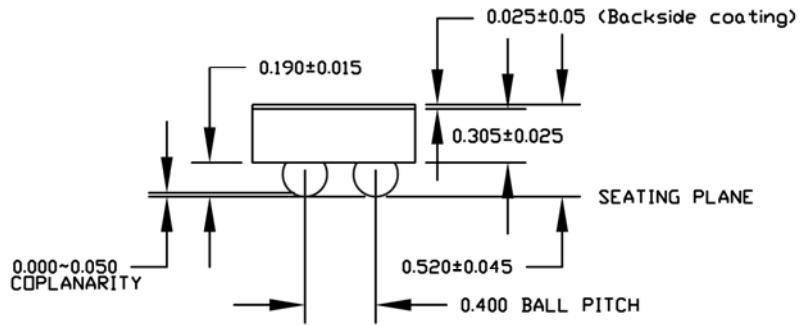
1. Murata Tel: www.murata.com.
2. **Micrel, Inc.**: www.micrel.com.

Package Information¹



TOP VIEW

BOTTOM VIEW



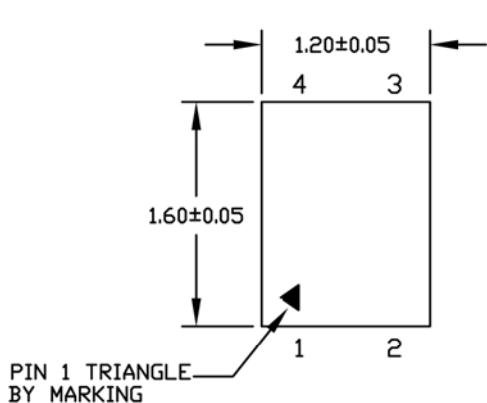
SIDE VIEW

4-Ball 0.88mm x 0.88mm WL-CSP (CS)

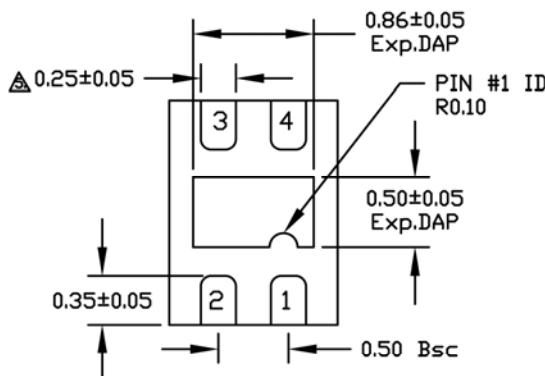
Note:

1. Package information is correct as of the publication date. For updates and most current information, go to www.micrel.com.

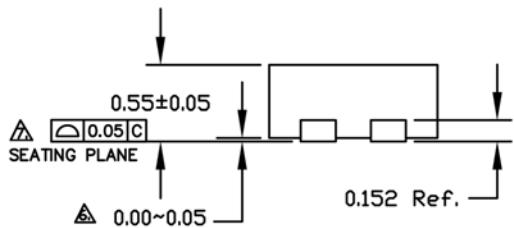
Package Information¹ (Continued)



TOP VIEW



BOTTOM VIEW



SIDE VIEW

4-Pin 1.2mm x 1.6mm Thin MLF® (MT)

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