

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

2SK882

FM Tuner, VHF RF Amplifier Applications

Unit: mm

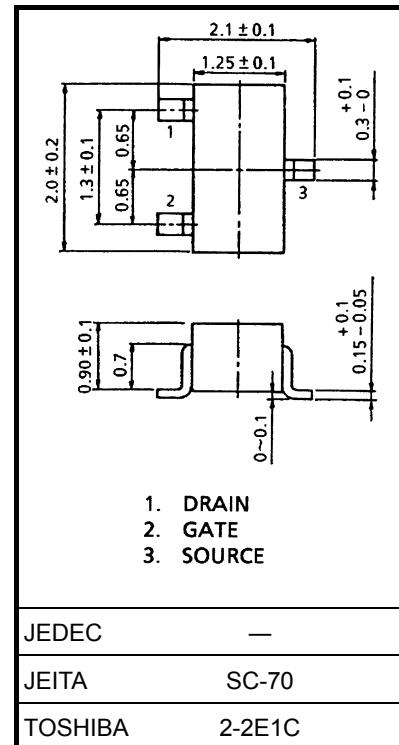
- Low reverse transfer capacitance: $C_{rss} = 0.025 \text{ pF}$ (typ.)
- Low noise figure: $NF = 1.7 \text{ dB}$ (typ.)
- High power gain: $G_{ps} = 28 \text{ dB}$ (typ.)
- Recommend operation voltage: $5 \sim 15 \text{ V}$

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DS}	20	V
Gate-source voltage	V_{GS}	± 5	V
Drain current	I_D	30	mA
Drain power dissipation	P_D	100	mW
Channel temperature	T_{ch}	125	$^\circ\text{C}$
Storage temperature	T_{stg}	$-55 \sim 125$	$^\circ\text{C}$

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).



JEDEC —

JEITA SC-70

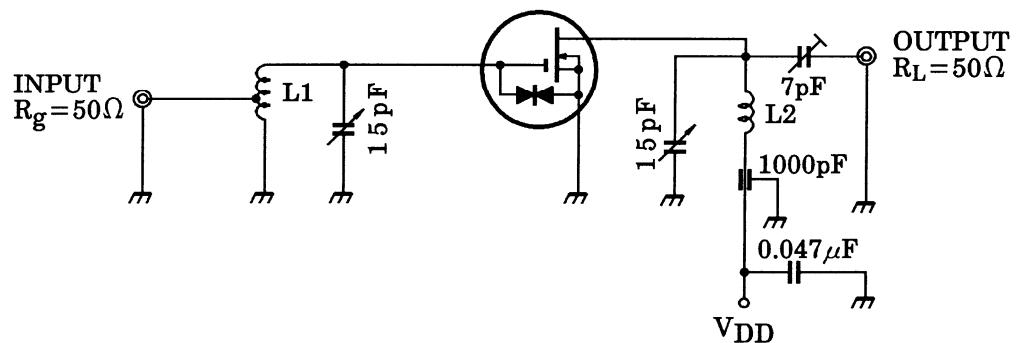
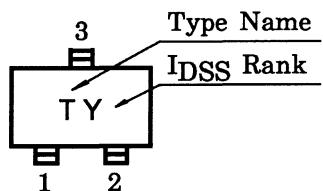
TOSHIBA 2-2E1C

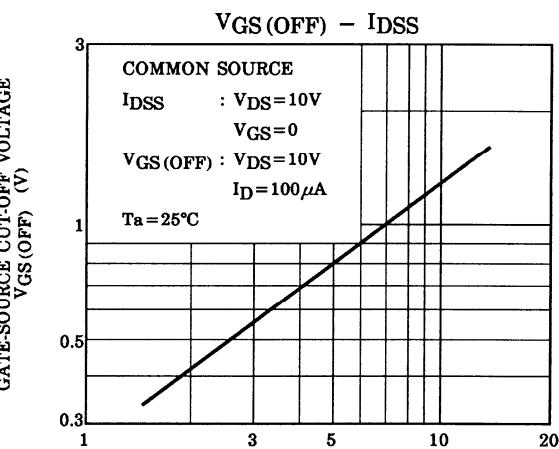
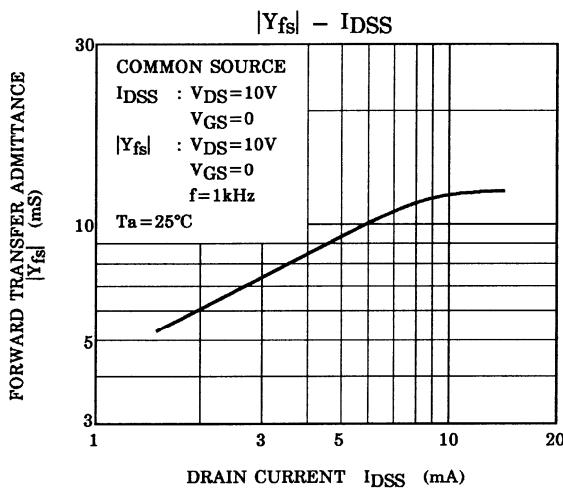
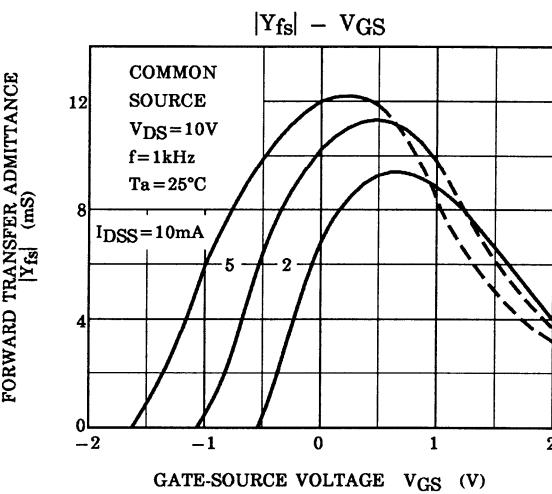
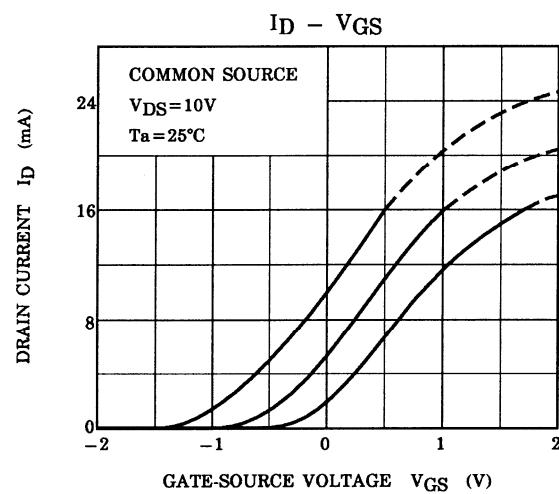
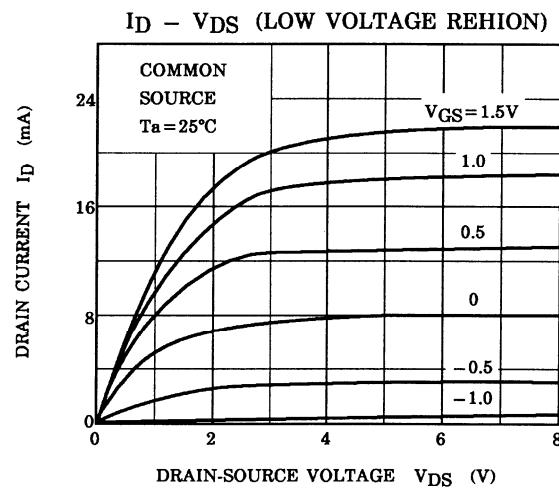
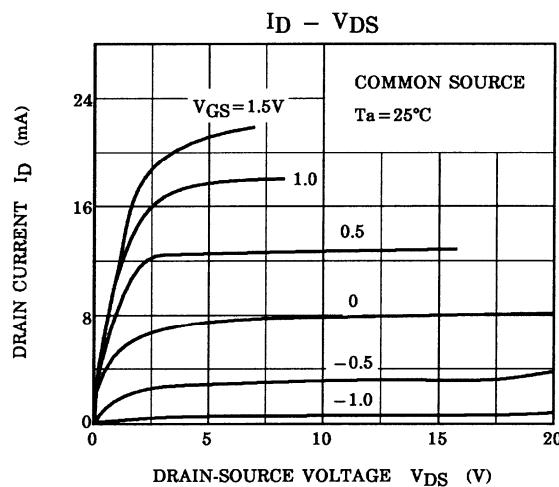
Weight: 0.006 g (typ.)

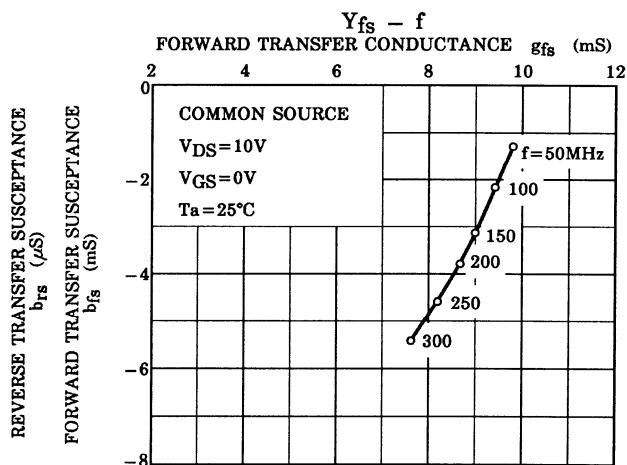
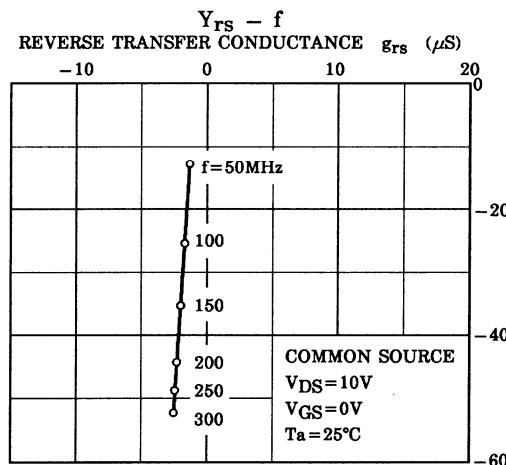
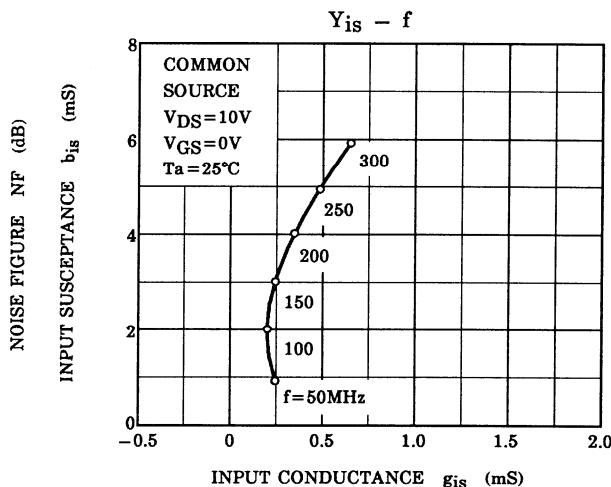
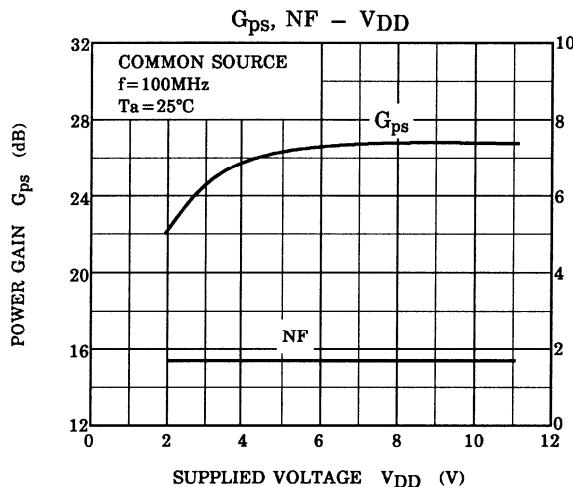
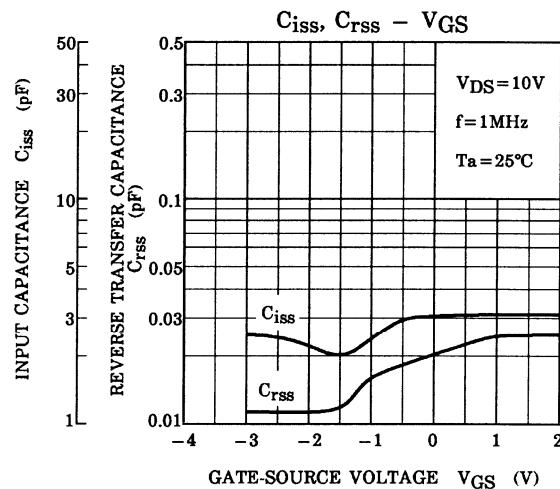
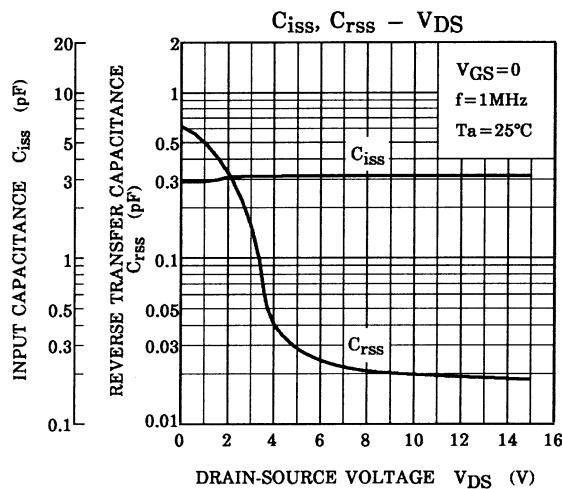
Electrical Characteristics ($T_a = 25^\circ\text{C}$)

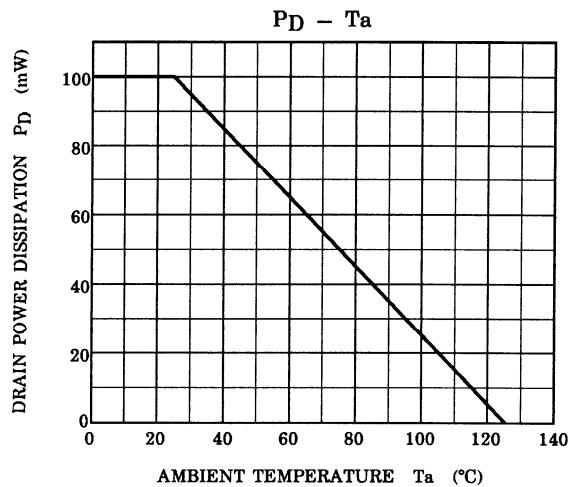
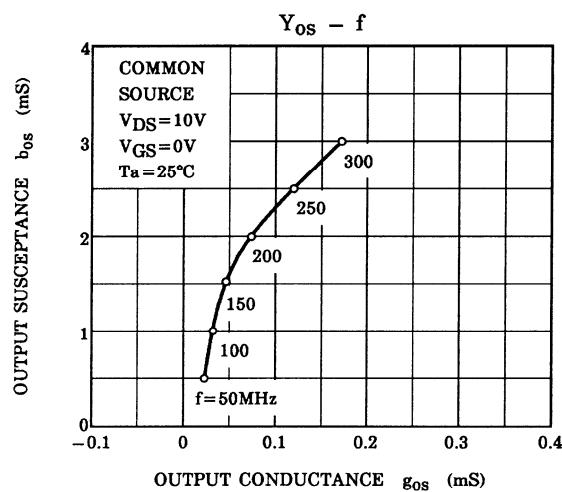
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current	I_{GSS}	$V_{DS} = 0, V_{GS} = \pm 5 \text{ V}$	—	—	± 50	nA
Drain-source voltage	V_{DSX}	$V_{GS} = -4 \text{ V}, I_D = 100 \mu\text{A}$	20	—	—	V
Drain current	I_{DSS} (Note)	$V_{DS} = 10 \text{ V}, V_{GS} = 0$	3	—	14	mA
Gate-source cut-off voltage	$V_{GS}(\text{OFF})$	$V_{DS} = 10 \text{ V}, I_D = 100 \mu\text{A}$	—	—	-2.5	V
Forward transfer admittance	$ Y_{fs} $	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1 \text{ kHz}$	—	10	—	mS
Input capacitance	C_{iss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	—	3.0	4.3	pF
Reverse transfer capacitance	C_{rss}		—	0.025	0.04	pF
Power gain	G_{ps}	$V_{DS} = 10 \text{ V}, f = 100 \text{ MHz}$ (Figure 1)	20	28	—	dB
Noise figure	NF		—	1.7	3.0	dB

Note: I_{DSS} classification Y: 3.0~7.0 mA, GR: 6.0~14.0 mA

**Figure 1 G_{ps}, NF Test Circuit****Marking**







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