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ADC1210S105F1 ADC1210S105F1 Demo Board; CMOS

version

IDTs ADC1210S105F1 demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVCMOS output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance



Features

- · SMA connector for clock and analog input signals
- CMOS (buffered outputs)
- Single power supply (on-board regulators)

Product Options

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ADC1210S105F1-DB	Active New	BD0	BOARD	0	С	N	Check Inventory

Documents

	TITLE	TYPE▲	FORMAT	FILE SIZE	DATE
A	Demo Board Software ADC_1_r04	Manual - Software	ZIP	2.33 MB	07/19/2012

Related Products

PRODUCTID	TITLE
ADC1210S105HN	Single 12-bit ADC 105 Msps CMOS or LVDS DDR digital outputs



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