NPN 100mA 50V Digital Transistors (Bias Resistor Built-in Transistors)

Datasheet

Parameter	Value
V <sub>CC</sub>	50V
I <sub>C(MAX.)</sub>	100mA
R <sub>1</sub>	22kΩ
R <sub>2</sub>	47kΩ

### Features

- 1) Built-In Biasing Resistors,  $R_1 = 22k\Omega$ ,  $R_2 = 47k\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary PNP Types: DTA024X series
- 6) Lead Free/RoHS Compliant.

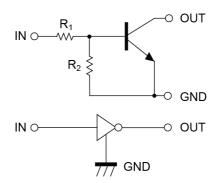
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### •Inner circuit

DTC024XUB

(SC-85)

Outline



### Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

### Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTC024XM	VMT3	1212	T2L	180	8	8000	69
DTC024XEB	EMT3F	1616	TL	180	8	3000	69
DTC024XUB	UMT3F	2021	TL	180	8	3000	69

## ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter			Values	Unit
Supply voltage		V <sub>cc</sub>	50	V
Input voltage		V <sub>IN</sub>	40 to -7	V
Output current		Io	50	mA
Collector current		I <sub>C(MAX)</sub> *1	100	mA
	DTC024XM		150	
Power dissipation	DTC024XEB	P <sub>D</sub> *2	150	mW
	DTC024XUB		200	
Junction temperature		T <sub>j</sub>	150	°C
Range of storage temperature		T <sub>stg</sub>	-55 to +150	°C

## • Electrical characteristics $(T_a = 25^{\circ}C)$

Darameter	Cymahal	Canditions	Values				
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
land to alterna	$V_{l(off)}$	V <sub>CC</sub> = 5V, I <sub>O</sub> = 100μA	-	-	0.5	V	
Input voltage	V <sub>I(on)</sub>	V <sub>O</sub> = 0.3V, I <sub>O</sub> = 5mA	2.5	-	-		
Output voltage	V <sub>O(on)</sub>	$I_{O}/I_{I} = 5mA/0.5mA$	-	0.05	0.15	V	
Input current	I <sub>I</sub>	V <sub>I</sub> = 5V	-	-	0.36	mA	
Output current	I <sub>O(off)</sub>	V <sub>CC</sub> = 50V, V <sub>I</sub> = 0V	-	-	500	nA	
DC current gain	G <sub>I</sub>	V <sub>O</sub> = 10V, I <sub>O</sub> = 5mA	80	-	-	-	
Input resistance	R <sub>1</sub>	-	15.4	22	28.6	kΩ	
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	-	1.7	2.1	2.6	-	
Transition frequency	f <sub>T</sub> *1	V <sub>CE</sub> = 10V, I <sub>E</sub> = -5mA, f = 100MHz	-	250	-	MHz	

<sup>\*1</sup> Characteristics of built-in transistor

<sup>\*2</sup> Each terminal mounted on a reference footprint

## ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

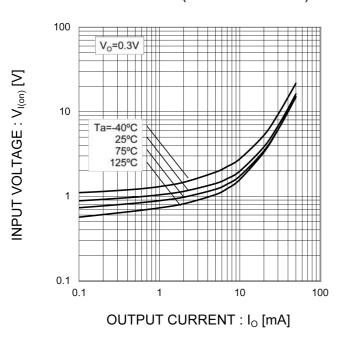


Fig.2 Output current vs. input voltage (OFF characteristics)

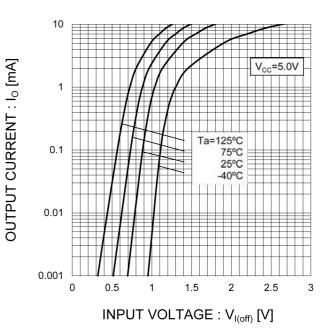


Fig.3 Output current vs. output voltage

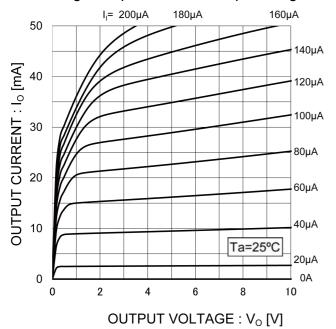
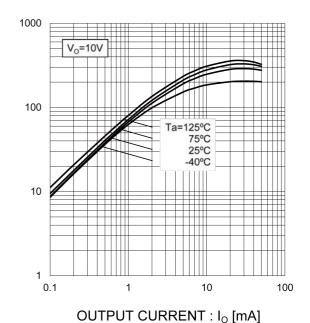


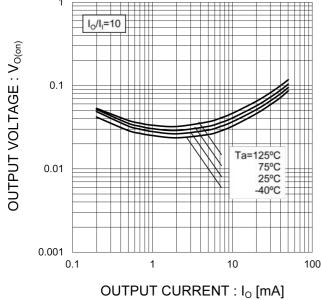
Fig.4 DC current gain vs. output current



OC CURRENT GAIN: G

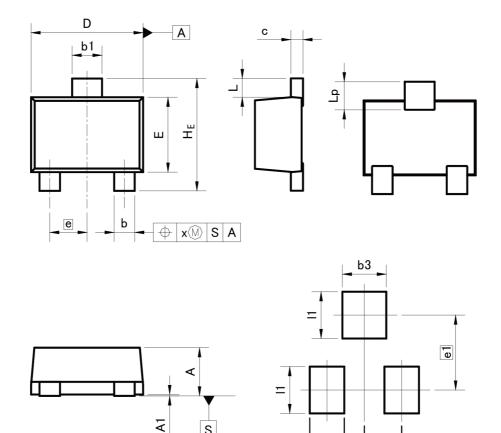
## ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.5 Output voltage vs. output current



### Dimensions

VMT3



S

Pattern of terminal position areas [Not a recommended pattern of soldering pads]

b2

DIM -	MILIM	ETERS	INC	HES
DIM [	MIN	MAX	MIN	MAX
Α	0.45	0.55	0.018	0.022
A1	0.00	0.10	0.000	0.004
b	0.17	0.27	0.007	0.011
b1	0.27	0.37	0.011	0.015
С	0.08	0.18	0.003	0.007
D	1.10	1.30	0.043	0.051
E	0.70	0.90	0.028	0.035
е	0.4	40	0.02	
HE	1.10	1.30	0.043	0.051
L	0.10	0.30	0.004	0.012
Lp	0.20	0.40	0.008	0.016
х	=	0.10	-	0.004
DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
b2	=	0.37	-	0.015
b3	155	0.47	-	0.019
e1	0.80		0.0	31

Dimension in mm/inches

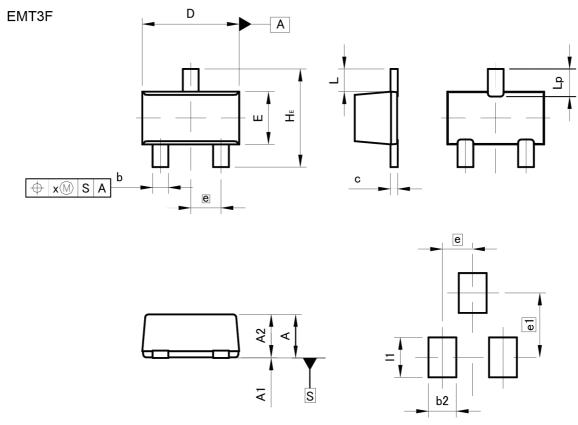
11



0.020

0.50

### Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

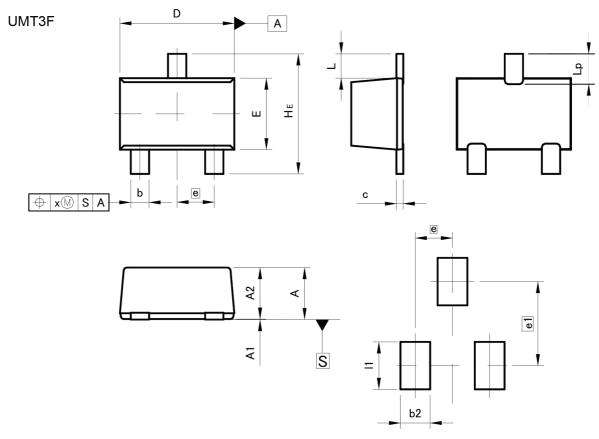
DIM	MILIM	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	0.65	0.85	0.026	0.033
A1	0.00	0.10	0.000	0.004
A2	0.60	0.80	0.024	0.031
b	0.21	0.36	0.008	0.014
С	0.08	0.18	0.003	0.007
D	1.50	1.70	0.059	0.067
E	0.76	0.96	0.030	0.038
е	0.9	50	0.0	20
HE	1.50	1.70	0.059	0.067
L	0.3	0.37		15
Lp	0.35	0.55	0.014	0.022
х	=	0.10	=	0.004

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2	_	0.46		0.018
e1	=	1.05	<i>#</i> 3	0.041
11	;=:	0.65	-	0.026

Dimension in mm/inches



### Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	0.85	1.05	0.033	0.041
A1	0.00	0.10	0.000	0.004
A2	0.80	1.00	0.031	0.039
b	0.27	0.42	0.011	0.017
С	0.08	0.18	0.003	0.007
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.0	65	0.0	26
HE	2.00	2.20	0.079	0.087
L	0.43		0.0	17
Lp	0.43	0.63	0.017	0.025
х	-	0.10		0.004

DIM -	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
b2	_	0.52	<u>—</u>	0.020
e1	1.	47	0.0	058
11	<del>-</del>	0.83		0.033

Dimension in mm/inches



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