

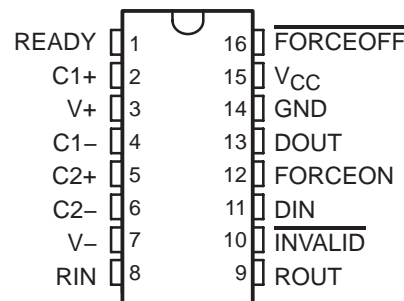
MAX3227

3-V TO 5.5-V SINGLE-CHANNEL RS-232 LINE DRIVER/RECEIVER WITH ± 15 -kV ESD PROTECTION

SLLS673 – AUGUST 2005

- Meets or Exceeds the Requirements of TIA/EIA-232-F and ITU v.28 Standards
- Operates With 3-V to 5.5-V V_{CC} Supply
- Operates at Least 1 Mbit/s
- Low Standby Current . . . 1 μ A Typ
- External Capacitors . . . $4 \times 0.1 \mu$ F
- Accepts 5-V Logic Input With 3.3-V Supply
- Designed to Be Interchangeable With Maxim MAX3227
- Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II
- ESD Protection for RS-232 I/O Pins
 - ± 15 kV – Human-Body Model
 - ± 8 kV – IEC1000-4-2, Contact Discharge
 - ± 8 kV – IEC1000-4-2, Air-Gap Discharge
- Auto-Powerdown Plus Feature Automatically Disables Drivers for Power Savings
- Applications
 - Battery-Powered, Hand-Held, and Portable Equipment
 - PDAs and Palmtop PCs
 - Notebooks, Sub-Notebooks, and Laptops
 - Digital Cameras
 - Mobile Phones and Wireless Devices
- Packaged in Plastic Shrink Small-Outline Package

DB PACKAGE
(TOP VIEW)



description/ordering information

The MAX3227 consists of one line driver, one line receiver, and a dual charge-pump circuit with ± 15 -kV ESD protection pin to pin (serial-port connection pins, including GND). The device meets the requirements of TIA/EIA-232-F and provides the electrical interface between an asynchronous communication controller and the serial-port connector. The charge pump and four small external capacitors allow operation from a single 3-V to 5.5-V supply. This device operates at data-signaling rates of 1 Mbit/s in normal operating mode and a maximum of 30-V/ μ s driver output slew rate. This device also features a logic-level output (READY) that asserts when the charge pump is regulating and the device is ready to begin transmitting.

The MAX3227 achieves a 1- μ A supply current using the auto-powerdown plus feature. This device automatically enters a low-power powerdown mode when the RS-232 cable is disconnected or the drivers of the connected peripherals are inactive for more than 30 s. They turn on again when they sense a valid transition at any driver or receiver input. Auto-powerdown saves power without changes to the existing BIOS or operating system.

The MAX3227C is characterized for operation from 0°C to 70°C. The MAX3227I is characterized for operation from -40°C to 85°C.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

 **TEXAS
INSTRUMENTS**

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SLLS673 – AUGUST 2005

AVAILABLE OPTIONS

T _A	PACKAGED DEVICE
	SHRINK SMALL OUTLINE (DB)
0°C to 70°C	MAX3227CDB
–40°C to 85°C	MAX3227IDB

The DB package is available taped and reeled.
Add the suffix R to device type (e.g., MAX3227CDBR).

Function Table

INPUT CONDITIONS				OUTPUT STATES				OPERATING MODE
FORCE ON	FORCE OFF	RECEIVER OR DRIVER EDGE WITHIN 30 S	VALID RS-232 LEVEL PRESENT AT RECEIVER	DRIVER OUTPUT	RECEIVER OUTPUT	INVALID\ OUTPUT	READY OUTPUT	
AUTO-POWERDOWN PLUS CONDITIONS								
H	H	NO	NO	Active	Active	L	H	Normal operation, auto-powerdown plus disabled
H	H	NO	YES	Active	Active	H	H	Normal operation, auto-powerdown plus disabled
L	H	YES	NO	Active	Active	L	H	Normal operation, auto-powerdown plus enabled
L	H	YES	YES	Active	Active	H	H	Normal operation, auto-powerdown plus enabled
L	H	NO	NO	Z	Active	L	L	Powerdown, auto-powerdown plus enabled
L	H	NO	YES	Z	Active	H	L	Powerdown, auto-powerdown plus enabled
X	L	X	NO	Z	Active	L	L	Manual powerdown
X	L	X	YES	Z	Active	H	L	Manual powerdown
AUTO-POWERDOWN CONDITIONS								
INVALID\	INVALID\	X	NO	Z	Active	L	L	Powerdown, auto-powerdown enabled
INVALID\	INVALID\	X	YES	Active	Active	H	H	Normal operation, auto-powerdown enabled

H = high level, L = low level, X = irrelevant, Z = high impedance



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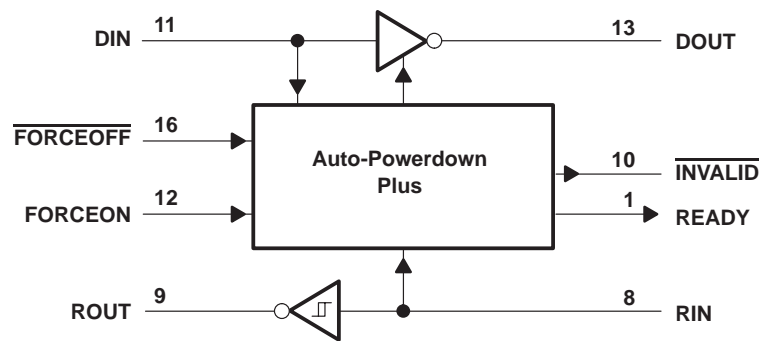
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SLLS673 – AUGUST 2005

Terminal Functions

TERMINAL NAME	NO.	DESCRIPTION
C1+	2	Positive terminal of voltage-doubler charge-pump capacitor
C1–	4	Negative terminal of voltage-doubler charge-pump capacitor
C2+	5	Positive terminal of inverting charge-pump capacitor
C2–	6	Negative terminal of inverting charge-pump capacitor
DIN	11	CMOS driver input
DOUT	13	RS-232 driver output
$\overline{\text{FORCEOFF}}$	16	Force-off input, active low. Drive low to shut down drivers, receivers, and charge pump. This overrides auto-shutdown and FORCEON (see Table 1).
FORCEON	12	Force-on input, active high. Drive high to override powerdown, keeping drivers and receivers on (FORCEOFF must be high) (see Table 1).
GND	14	Ground
$\overline{\text{INVALID}}$	10	Valid signal detector output, active low. A logic high indicates that a valid RS-232 level is present on a receiver input.
READY	1	Ready to transmit output, active high. READY is enabled high when V– goes below -3.5 V and the device is ready to transmit.
RIN	8	RS-232 receiver input
ROUT	9	CMOS receiver output
V+	3	$+2 \times V_{CC}$ generated by the charge pump
V–	7	$-2 \times V_{CC}$ generated by the charge pump
V _{CC}	15	3-V to 5.5-V single-supply voltage

logic diagram (positive logic)



MAX3227**3-V TO 5.5-V SINGLE-CHANNEL RS-232 LINE DRIVER/RECEIVER****WITH ± 15 -kV ESD PROTECTION**

SLLS673 – AUGUST 2005

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage range, V_{CC} (see Note 1)	–0.3 V to 6 V
Positive output supply voltage range, V_+ (see Note 1)	–0.3 V to 7 V
Negative output supply voltage range, V_- (see Note 1)	0.3 V to –7 V
Supply voltage difference, $V_+ - V_-$ (see Note 1)	13 V
Input voltage range, V_I : Driver ($\overline{\text{FORCEOFF}}$, FORCEON)	–0.3 V to 6 V
Receiver	–25 V to 25 V
Output voltage range, V_O : Driver	–13.2 V to 13.2 V
Receiver ($\overline{\text{INVALID}}$, READY)	–0.3 V to $V_{CC} + 0.3$ V
Short-circuit duration: DOUT to GND	Unlimited
Package thermal impedance, θ_{JA} (see Note 2)	82°C/W
Lead temperature 1,6 mm (1/16 in) from case for 10 s	260°C
Storage temperature range, T_{stg}	–65°C to 150°C

[†] Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. All voltages are with respect to network GND.

2. The package thermal impedance is calculated in accordance with JESD 51-7.

recommended operating conditions (see Note 3 and Figure 6)

			MIN	NOM	MAX	UNIT
Supply voltage		$V_{CC} = 3.3$ V	3	3.3	3.6	V
		$V_{CC} = 5$ V	4.5	5	5.5	
V_{IH} Driver and control high-level input voltage	DIN, $\overline{\text{FORCEOFF}}$, FORCEON	$V_{CC} = 3.3$ V	2		5.5	V
		$V_{CC} = 5$ V	2.4		5.5	
V_{IL} Driver and control low-level input voltage	DIN, $\overline{\text{FORCEOFF}}$, FORCEON		0		0.8	V
V_I Receiver input voltage			–25		25	V
T_A Operating free-air temperature		MAX3227C	0		70	°C
		MAX3227I	–40		85	

NOTE 3: Test conditions are C1–C4 = 0.1 μ F at $V_{CC} = 3.3$ V \pm 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at $V_{CC} = 5$ V \pm 0.5 V.

electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Note 3 and Figure 6)

PARAMETER		TEST CONDITIONS	MIN	TYP [‡]	MAX	UNIT
I_I Input leakage current	$\overline{\text{FORCEOFF}}$, FORCEON			± 0.01	± 1	μ A
I_{CC} Supply current ($T_A = 25^\circ\text{C}$)	Auto-powerdown plus disabled	No load, $\overline{\text{FORCEOFF}}$ and FORCEON at V_{CC}		0.3	2	μ A
	Powered off	No load, $\overline{\text{FORCEOFF}}$ at GND		1	10	
	Auto-powerdown plus enabled	No load, $\overline{\text{FORCEOFF}}$ at V_{CC} , FORCEON at GND, All RIN are open or grounded		1	10	

[‡] All typical values are at $V_{CC} = 3.3$ V or $V_{CC} = 5$ V, and $T_A = 25^\circ\text{C}$.

NOTE 3: Test conditions are C1–C4 = 0.1 μ F at $V_{CC} = 3.3$ V \pm 0.3 V; C1 = 0.047 μ F, C2–C4 = 0.33 μ F at $V_{CC} = 5$ V \pm 0.5 V.



MAX3227
3-V TO 5.5-V SINGLE-CHANNEL RS-232 LINE DRIVER/RECEIVER
WITH ± 15 -kV ESD PROTECTION

SLLS673 – AUGUST 2005

DRIVER SECTION

electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Note 3 and Figure 6)

PARAMETER	TEST CONDITIONS	MIN	TYP†	MAX	UNIT
V_{OH} High-level output voltage	DOUT at $R_L = 3\text{ k}\Omega$ to GND, DIN = GND	5	5.4		V
V_{OL} Low-level output voltage	DOUT at $R_L = 3\text{ k}\Omega$ to GND, DIN = V_{CC}	–5	–5.4		V
I_{IH} High-level input current	$V_I = V_{CC}$		± 0.01	± 1	μA
I_{IL} Low-level input current	V_I at GND		± 0.01	± 1	μA
I_{OS} Short-circuit output current‡	$V_{CC} = 3.6\text{ V}$, $V_O = 0\text{ V}$		± 35	± 60	mA
	$V_{CC} = 5.5\text{ V}$, $V_O = 0\text{ V}$		± 35	± 60	
r_o Output resistance	V_{CC} , V_+ , and $V_- = 0\text{ V}$, $V_O = \pm 2\text{ V}$	300	10M		Ω
I_{off} Output leakage current	$\overline{\text{FORCEOFF}} = \text{GND}$, $V_O = \pm 12\text{ V}$, $V_{CC} = 0$ to 5.5 V			± 25	μA

† All typical values are at $V_{CC} = 3.3\text{ V}$ or $V_{CC} = 5\text{ V}$, and $T_A = 25^\circ\text{C}$.

‡ Short-circuit durations should be controlled to prevent exceeding the device absolute power-dissipation ratings, and not more than one output should be shorted at a time.

NOTE 3: Test conditions are C_1 – $C_4 = 0.1\text{ }\mu\text{F}$ at $V_{CC} = 3.3\text{ V} \pm 0.3\text{ V}$; $C_1 = 0.047\text{ }\mu\text{F}$, C_2 – $C_4 = 0.33\text{ }\mu\text{F}$ at $V_{CC} = 5\text{ V} \pm 0.5\text{ V}$.

switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Note 3 and Figure 6)

PARAMETER	TEST CONDITIONS	MIN	TYP†	MAX	UNIT
0 Maximum data rate	$C_L = 1000\text{ pF}$, See Figure 1 $R_L = 3\text{ k}\Omega$, One DIN switching,	250			kbit/s
	$C_L = 1000\text{ pF}$, $V_{CC} = 4.5\text{ V}$, $R_L = 3\text{ k}\Omega$, See Figure 1 One DIN switching,	1000			
	$C_L = 250\text{ pF}$, $V_{CC} = 3\text{ V}$, $R_L = 3\text{ k}\Omega$, See Figure 1 One DIN switching,	1000			
$t_{sk(p)}$ Pulse skew§	$C_L = 150\text{ pF}$ to 2500 pF , $R_L = 3\text{ k}\Omega$ to $7\text{ k}\Omega$, See Figure 2		25		ns
$SR(tr)$ Slew rate, transition region	$V_{CC} = 3.3\text{ V}$, $R_L = 3\text{ k}\Omega$ to $7\text{ k}\Omega$, $C_L = 150\text{ pF}$ to 1000 pF , See Figure 1	24		150	V/ μs

† All typical values are at $V_{CC} = 3.3\text{ V}$ or $V_{CC} = 5\text{ V}$, and $T_A = 25^\circ\text{C}$.

§ Pulse skew is defined as $|t_{PLH} - t_{PHL}|$ of each channel of the same device.

NOTE 3: Test conditions are C_1 – $C_4 = 0.1\text{ }\mu\text{F}$ at $V_{CC} = 3.3\text{ V} \pm 0.3\text{ V}$; $C_1 = 0.047\text{ }\mu\text{F}$, C_2 – $C_4 = 0.33\text{ }\mu\text{F}$ at $V_{CC} = 5\text{ V} \pm 0.5\text{ V}$.

ESD protection

TERMINAL	TEST CONDITIONS	TYP	UNIT
NAME NO.			
DOUT 13	HBM	± 15	kV
	Contact discharge, IEC1000-4-2	± 8	
	Air-gap discharge, IEC1000-4-2	± 8	



MAX3227**3-V TO 5.5-V SINGLE-CHANNEL RS-232 LINE DRIVER/RECEIVER****WITH ± 15 -kV ESD PROTECTION**

SLLS673 – AUGUST 2005

RECEIVER SECTION

electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Note 3 and Figure 6)

PARAMETER	TEST CONDITIONS	MIN	TYP†	MAX	UNIT
V_{OH} High-level output voltage	$I_{OH} = -1$ mA	$V_{CC} - 0.6$	$V_{CC} - 0.1$		V
V_{OL} Low-level output voltage	$I_{OL} = 1.6$ mA			0.4	V
V_{IT+} Positive-going input threshold voltage	$V_{CC} = 3.3$ V		1.5	2.4	V
	$V_{CC} = 5$ V		1.8	2.4	
V_{IT-} Negative-going input threshold voltage	$V_{CC} = 3.3$ V	0.6	1.2		V
	$V_{CC} = 5$ V	0.8	1.5		
V_{hys} Input hysteresis ($V_{IT+} - V_{IT-}$)			0.5		V
I_{off} Output leakage current			± 0.05	± 10	μ A
r_i Input resistance	$V_I = \pm 3$ V to ± 25 V	3	5	7	k Ω

† All typical values are at $V_{CC} = 3.3$ V or $V_{CC} = 5$ V, and $T_A = 25^\circ\text{C}$.

NOTE 3: Test conditions are C_1 – $C_4 = 0.1$ μ F at $V_{CC} = 3.3$ V ± 0.3 V; $C_1 = 0.047$ μ F, C_2 – $C_4 = 0.33$ μ F at $V_{CC} = 5$ V ± 0.5 V.

switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Note 3)

PARAMETER	TEST CONDITIONS	TYP†	UNIT
t_{PLH} Propagation delay time, low- to high-level output	$C_L = 150$ pF, See Figure 3	150	ns
t_{PHL} Propagation delay time, high- to low-level output	$C_L = 150$ pF, See Figure 3	150	ns
$t_{sk(p)}$ Pulse skew‡	See Figure 3	50	ns

† All typical values are at $V_{CC} = 3.3$ V or $V_{CC} = 5$ V, and $T_A = 25^\circ\text{C}$.

‡ Pulse skew is defined as $|t_{PLH} - t_{PHL}|$ of each channel of the same device.

NOTE 3: Test conditions are C_1 – $C_4 = 0.1$ μ F at $V_{CC} = 3.3$ V ± 0.3 V; $C_1 = 0.047$ μ F, C_2 – $C_4 = 0.33$ μ F at $V_{CC} = 5$ V ± 0.5 V.

ESD protection

TERMINAL	TEST CONDITIONS		TYP	UNIT
NAME NO.				
RIN 8	HBM		± 15	kV
	Contact discharge, IEC1000-4-2		± 8	
	Air-gap discharge, IEC1000-4-2		± 15	

MAX3227
3-V TO 5.5-V SINGLE-CHANNEL RS-232 LINE DRIVER/RECEIVER
WITH ± 15 -kV ESD PROTECTION

SLLS673 – AUGUST 2005

AUTO-POWERDOWN SECTION

electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 4)

PARAMETER		TEST CONDITIONS	MIN	MAX	UNIT
$V_{T+}(\text{valid})$	Receiver input threshold for $\overline{\text{INVALID}}$ high-level output voltage	FORCEON = GND, $\overline{\text{FORCEOFF}} = V_{CC}$		2.7	V
$V_{T-}(\text{valid})$	Receiver input threshold for $\overline{\text{INVALID}}$ high-level output voltage	FORCEON = GND, $\overline{\text{FORCEOFF}} = V_{CC}$	-2.7		V
$V_{T}(\text{invalid})$	Receiver input threshold for $\overline{\text{INVALID}}$ low-level output voltage	FORCEON = GND, $\overline{\text{FORCEOFF}} = V_{CC}$	-0.3	0.3	V
V_{OH}	$\overline{\text{INVALID}}$, READY output voltage high	$I_{OH} = -1 \text{ mA}$, FORCEON = GND, $\overline{\text{FORCEOFF}} = V_{CC}$	$V_{CC} - 0.6$		V
V_{OL}	$\overline{\text{INVALID}}$, READY output voltage low	$I_{OL} = 1.6 \text{ mA}$, FORCEON = GND, $\overline{\text{FORCEOFF}} = V_{CC}$		0.4	V

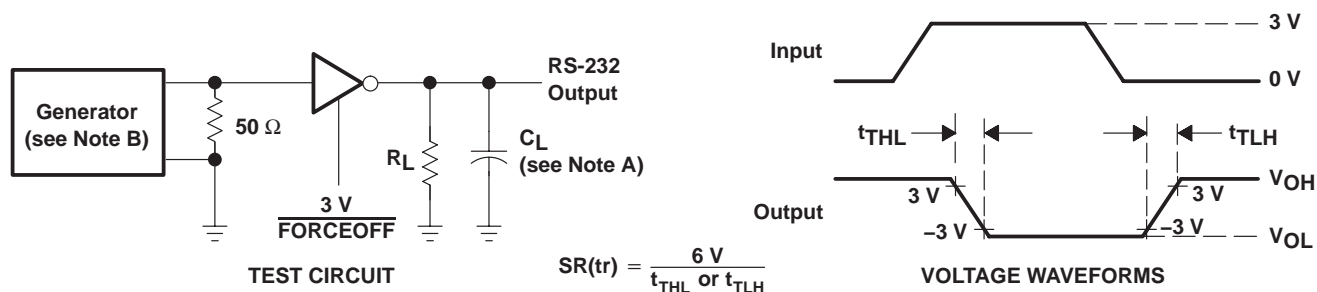
switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 4)

PARAMETER			MIN	TYP†	MAX	UNIT
tINVH	Propagation delay time, low- to high-level output		1			μs
tINVL	Propagation delay time, high- to low-level output		30			μs
tWU	Supply enable time		100			μs
tAUTOPRDN	Driver or receiver edge to driver's shutdown	VCC = 5 V	15	30	60	sec

† All typical values are at $V_{CC} = 3.3 \text{ V}$ or $V_{CC} = 5 \text{ V}$, and $T_A = 25^\circ\text{C}$.



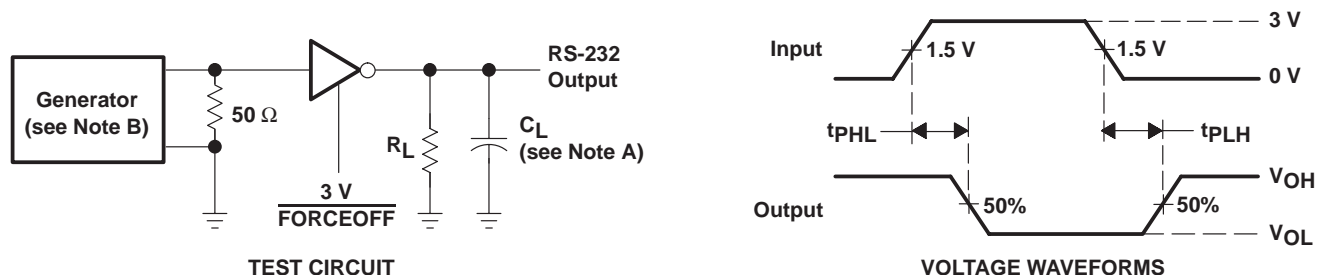
PARAMETER MEASUREMENT INFORMATION



NOTES: A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: PRR = 250 kbit/s, $Z_O = 50\ \Omega$, 50% duty cycle, $t_r \leq 10\text{ ns}$, $t_f \leq 10\text{ ns}$.

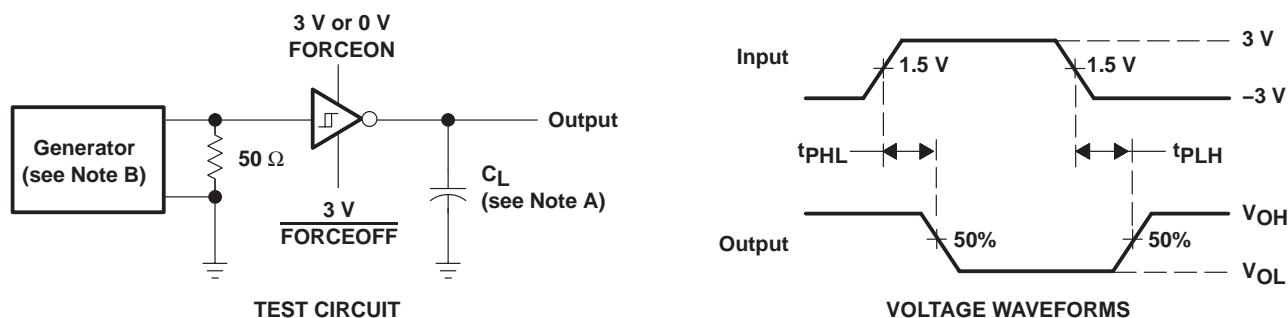
Figure 1. Driver Slew Rate



NOTES: A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: PRR = 250 kbit/s, $Z_O = 50\ \Omega$, 50% duty cycle, $t_r \leq 10\text{ ns}$, $t_f \leq 10\text{ ns}$.

Figure 2. Driver Pulse Skew



NOTES: A. C_L includes probe and jig capacitance.

B. The pulse generator has the following characteristics: $Z_O = 50\ \Omega$, 50% duty cycle, $t_r \leq 10\text{ ns}$, $t_f \leq 10\text{ ns}$.

Figure 3. Receiver Propagation Delay Times

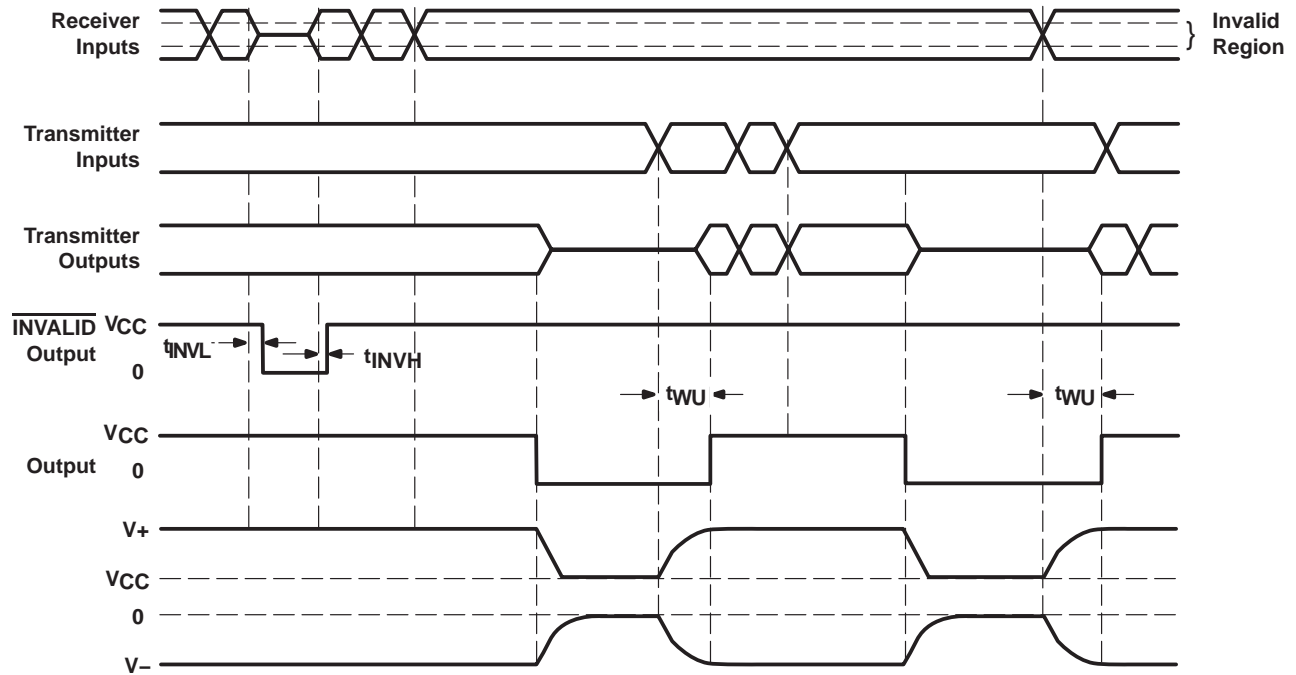
MAX3227

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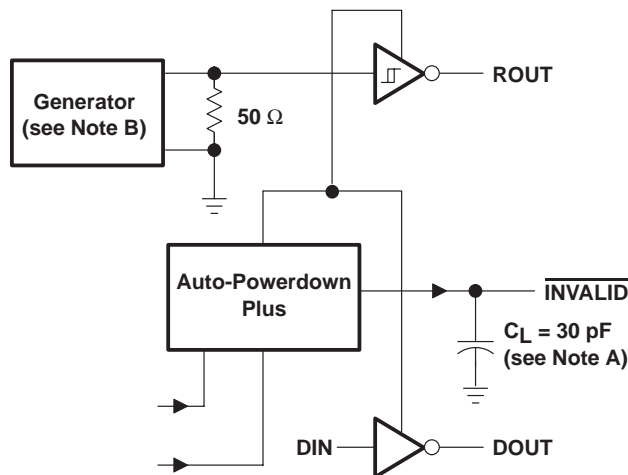
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SLLS673 – AUGUST 2005

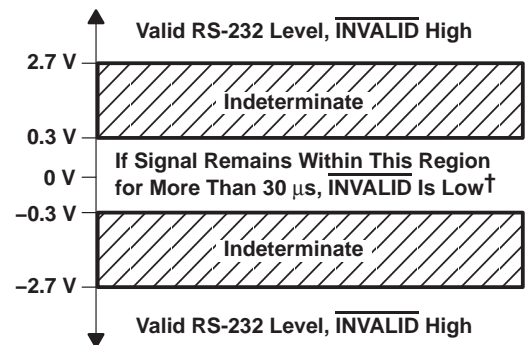
PARAMETER MEASUREMENT INFORMATION



VOLTAGE WAVEFORMS



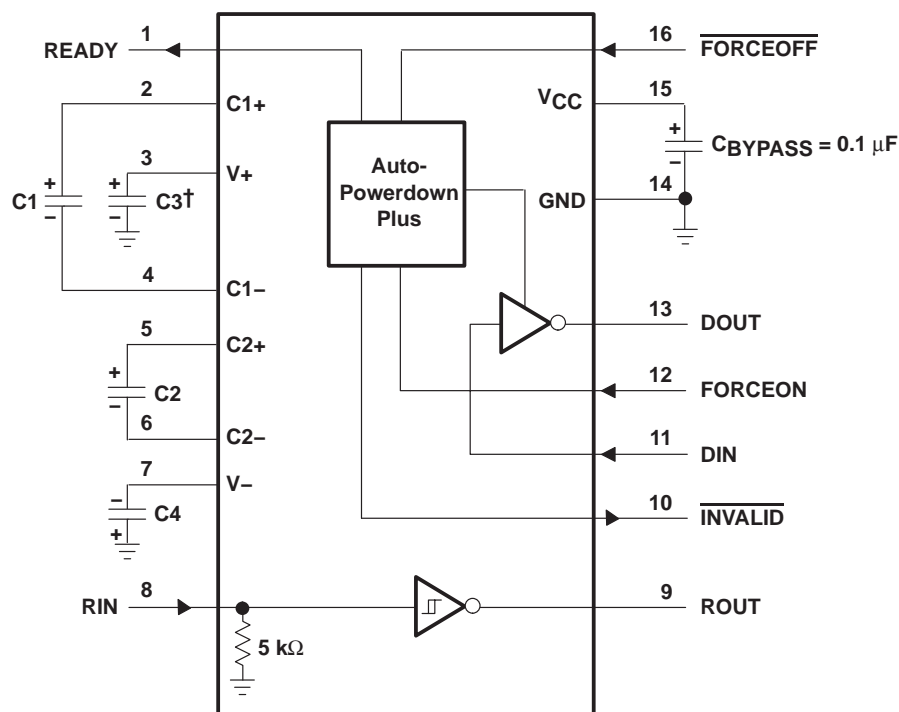
TEST CIRCUIT



† Auto-powerdown disables drivers and reduces supply current to $1 \mu\text{A}$.

Figure 4. $\overline{\text{INVALID}}$ Propagation Delay Times and Driver Enabling Time

APPLICATION INFORMATION



† C3 can be connected to V_{CC} or GND.

NOTES: A. Resistor values shown are nominal.

B. Nonpolarized ceramic capacitors are acceptable. If polarized tantalum or electrolytic capacitors are used, they should be connected as shown.

V_{CC} vs CAPACITOR VALUES

V _{CC}	C1	C2, C3, and C4
3.3 V \pm 0.3 V	0.1 μ F	0.1 μ F
5 V \pm 0.5 V	0.047 μ F	0.33 μ F
3 V to 5.5 V	0.1 μ F	0.47 μ F

Figure 5. Typical Operating Circuit and Capacitor Values

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
MAX3227CDB	ACTIVE	SSOP	DB	16	80	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227CDBE4	ACTIVE	SSOP	DB	16	80	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227CDBR	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227CDBRE4	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227IDB	ACTIVE	SSOP	DB	16	80	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227IDBE4	ACTIVE	SSOP	DB	16	80	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227IDBR	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
MAX3227IDBRE4	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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DB (R-PDSO-G**)

PLASTIC SMALL-OUTLINE

28 PINS SHOWN



- NOTES: A. All linear dimensions are in millimeters.
 B. This drawing is subject to change without notice.
 C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
 D. Falls within JEDEC MO-150

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