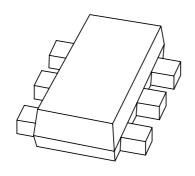
## **DISCRETE SEMICONDUCTORS**

# DATA SHEET



# PBSS4140V 40 V low V<sub>CEsat</sub> NPN transistor

Product data sheet Supersedes data of 2001 Nov 05 2002 Jun 20



# 40 V low V<sub>CEsat</sub> NPN transistor

### **PBSS4140V**

#### **FEATURES**

- 300 mW total power dissipation
- Very small 1.6 mm x 1.2 mm x 0.55 mm ultra thin package
- Improved thermal behaviour due to flat leads
- · Excellent coplanarity due to straight leads
- Low collector-emitter saturation voltage
- · High current capabilities
- Reduced required PCB area.

### **APPLICATIONS**

- · General purpose switching and muting
- · LCD backlighting
- · Supply line switching circuits
- Battery driven equipment (mobile phones, video cameras and hand-held devices).

### **DESCRIPTION**

NPN low V<sub>CEsat</sub> transistor with high current capability in a SOT666 plastic package. PNP complement: PBSS5140V.

### **MARKING**

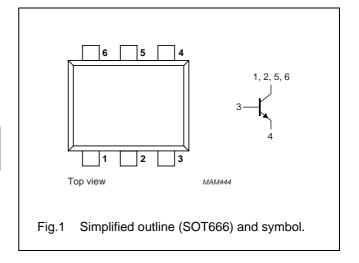
TYPE NUMBER	MARKING CODE
PBSS4140V	22

### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	MAX.	UNIT
V <sub>CEO</sub>	collector-emitter voltage	40	V
I <sub>C</sub>	collector current (DC)	1	Α
I <sub>CRP</sub>	peak collector current	2	Α
R <sub>CEsat</sub>	equivalent on-resistance	<190	mΩ

### **PINNING**

PIN	DESCRIPTION
1	collector
2	collector
3	base
4	emitter
5	collector
6	collector



## 40 V low V<sub>CEsat</sub> NPN transistor

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#### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	40	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	40	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	5	V
Ic	collector current (DC)		-	1	А
I <sub>CM</sub>	peak collector current		_	3	А
I <sub>CRP</sub>	repetitive peak collector current	note 1	_	2	А
I <sub>B</sub>	base current (DC)		-	300	mA
I <sub>BM</sub>	peak base current		_	1	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 2	-	300	mW
		T <sub>amb</sub> ≤ 25 °C; note 3	-	500	mW
		T <sub>amb</sub> ≤ 25 °C; notes 1 and 2	-	1.2	W
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

### **Notes**

- 1. Operated under pulsed conditions:  $t_p \le 30$  ms;  $\delta \le 0.2$ .
- 2. Device mounted on a printed-circuit board; single sided copper; tinplated; standard footprint.
- 3. Device mounted on a printed-circuit board; single sided copper; tinplated; mounting pad for collector 1 cm<sup>2</sup>.

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	410	K/W
		note 2	215	K/W
		notes 1 and 3	110	K/W

### **Notes**

- 1. Device mounted on a printed circuit board; single sided copper; tinplated; standard footprint.
- 2. Device mounted on a printed-circuit board; single sided copper; tinplated; mounting pad for collector 1 cm<sup>2</sup>.
- 3. Operated under pulsed conditions:  $t_p \le 30$  ms;  $\delta \le 0.2$ .

### Soldering

The only recommended soldering method is reflow soldering.

# 40 V low $V_{\text{CEsat}}$ NPN transistor

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### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

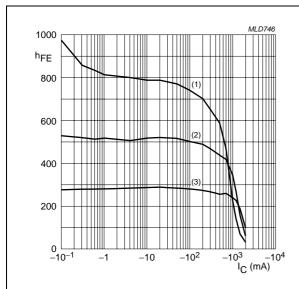
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = 40 V; I <sub>E</sub> = 0	_	_	100	nA
		V <sub>CB</sub> = 40 V; I <sub>E</sub> = 0; T <sub>amb</sub> = 150 °C	_	_	50	μΑ
I <sub>CEO</sub>	collector-emitter cut-off current	$V_{CE} = 30 \text{ V}; I_{B} = 0$	_	_	100	nA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0$	_	_	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 1 \text{ mA}$	300	_	_	
		$V_{CE} = 5 \text{ V}; I_{C} = 500 \text{ mA}$	300	_	900	
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 1 A	200	_	_	
		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 A; note 1	75	_	_	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 1 mA	_	50	80	mV
		$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}$	_	70	110	mV
		I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA; note 1	_	150	190	mV
		$I_C = 2 \text{ A}$ ; $I_B = 200 \text{ mA}$ ; note 1	_	320	440	mV
R <sub>CEsat</sub>	equivalent on-resistance	I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA; note 1	_	150	<190	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 1 A; I <sub>B</sub> = 100 mA	_	_	1.2	V
$V_{BEon}$	base-emitter turn-on voltage	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 1 A	_	_	1.1	V
f <sub>T</sub>	transition frequency	$I_C = 50 \text{ mA}; V_{CE} = 10 \text{ V};$ f = 100 MHz	150	_	_	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = I_e = 0; f = 1 \text{ MHz}$	_	_	10	pF

### Note

1. Pulse test:  $t_p \leq 300~\mu s;~\delta \leq 0.02.$ 

# 40 V low V<sub>CEsat</sub> NPN transistor

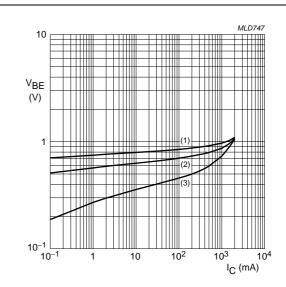
### **PBSS4140V**



V<sub>CE</sub> = 5 V.

- (1)  $T_{amb} = 150 \, ^{\circ}C$ .
- (2)  $T_{amb} = 25 \, ^{\circ}C$ .
- (3)  $T_{amb} = -55 \, ^{\circ}C$ .

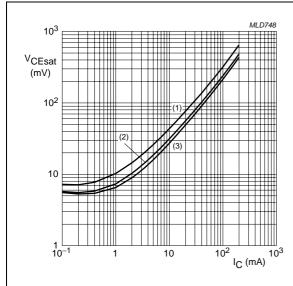
Fig.2 DC current gain as a function of collector current; typical values.



 $V_{CE} = 5 V.$ 

- (1)  $T_{amb} = -55 \, ^{\circ}C.$
- (2)  $T_{amb} = 25 \, ^{\circ}C$ .
- (3)  $T_{amb} = 150 \, ^{\circ}C$ .

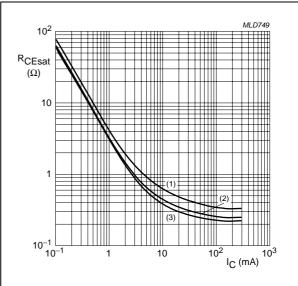
Fig.3 Base-emitter voltage as a function of collector current; typical values.



 $I_{\rm C}/I_{\rm B} = 10.$ 

- (1)  $T_{amb} = 150 \, ^{\circ}C$ .
- (2)  $T_{amb} = 25 \, ^{\circ}C$ .
- (3)  $T_{amb} = -55 \, ^{\circ}C$ .

Fig.4 Collector-emitter saturation voltage as a function of collector current; typical values.



 $I_{\rm C}/I_{\rm B} = 10.$ 

- (1)  $T_{amb} = 150 \, ^{\circ}C$ .
- (2)  $T_{amb} = 25 \, ^{\circ}C$ .
- (3)  $T_{amb} = -55 \, ^{\circ}C$ .

Fig.5 Equivalent on-resistance as a function of collector current; typical values.

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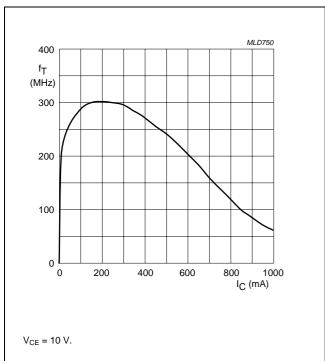


Fig.6 Transition frequency as a function of collector current.

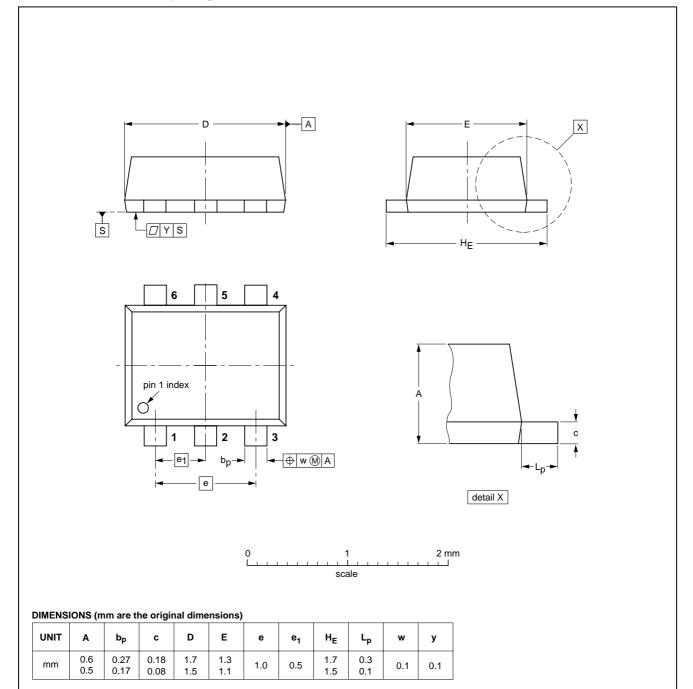
# 40 V low $V_{\text{CEsat}}$ NPN transistor

**PBSS4140V** 

### **PACKAGE OUTLINE**

Plastic surface mounted package; 6 leads

**SOT666** 



OUTLINE	REFERENCES		EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT666						<del>-01-01-04</del> 01-08-27

### 40 V low V<sub>CEsat</sub> NPN transistor

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#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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