

UPD78F1816AGAA2-GAM-G

User's Manual: Hardware

Product Info

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Product Spec



Basic Information Memory CPU (Bit, Clock, DMA, External Bus etc) Timers Analog
Interfaces Operating Conditions Remarks Package

Basic Information

Product Status	Mass Production
Family	78K
Series	78K0R/Fx
Group	78K0R/Fx3

Memory

Program memory	96 KB Flash memory
RAM	6 KB
Data Flash	16 KB

CPU (Bit, Clock, DMA, External Bus etc)

CPU	78K0R (16-bit)
Max. Frequency	24 MHz
On-chip Oscillator Freq.	8, 4 MHz
Power-On Reset	YES
Low Voltage Detection	YES
DMA Remarks	DMAC x 2 ch
External Interrupt Pins	8
I/O Ports	41

Timers

16-bit Timers	16 ch
Watchdog Timers	1 ch
PWM Output	16

Analog	
A/D Converters	10-bit x 11 ch
Interfaces	
CSIs	2 ch
UARTs	2 ch
I2Cs	1 ch
LIN	2 ch
Operating Conditions	
Operating Voltage	2.7 to 5.5 V
Operating Temperature	-40 to 125 °C (Ambient Temperature)
Remarks	
Remarks	Automotive

Package	
Pins	48
Terminal pitch (mm)	0.5 mm
Package Type	LFQFP
Dimensions (mm)	7x7 mm ²
Drawing Link	 p48ga-50-gam
Mount pad	 fig0009e
Mounting height (mm)[MAX]	1.6 mm
Mass (g) [TYP,]	0.16 g
Renesas code	PLQP0048KF-B (old: P48GA-50-GAM-2)
JEITA code	P-LFQFP48-7x7-0.50
Remarks	LQFP(matrix line),PPF TYPE
Terminal material - Base	Cu alloy
Terminal material - Surface	Ni/Pd/Au

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