SMD 14 Pin 10 Tap TTL Compatible Active Delay Modules

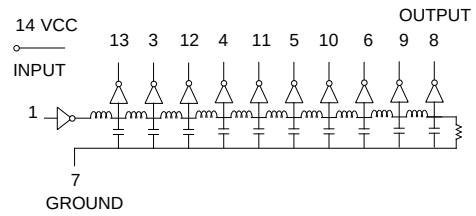
Tap Delays ±5% or ±2 nS†	Total Delays ±5% or ±2 nS†	Gull-Wing Part Number	J-Lead Part Number	Tap Delays ±5% or ±2 nS†	Total Delays ±5% or ±2 nS†	Gull-Wing Part Number	J-Lead Part Number
5	50	EPA245-50	EPA247-50	44	440	EPA245-440	EPA247-440
6	60	EPA245-60	EPA247-60	45	450	EPA245-450	EPA247-450
7.5	75	EPA245-75	EPA247-75	47	470	EPA245-470	EPA247-470
10	100	EPA245-100	EPA247-100	50	500	EPA245-500	EPA247-500
12.5	125	EPA245-125	EPA247-125	55	550	EPA245-550	EPA247-550
15	150	EPA245-150	EPA247-150	60	600	EPA245-600	EPA247-600
17.5	175	EPA245-175	EPA247-175	65	650	EPA245-650	EPA247-650
20	200	EPA245-200	EPA247-200	70	700	EPA245-700	EPA247-700
22.5	225	EPA245-225	EPA247-225	75	750	EPA245-750	EPA247-750
25	250	EPA245-250	EPA247-250	80	800	EPA245-800	EPA247-800
30	300	EPA245-300	EPA247-300	85	850	EPA245-850	EPA247-850
35	350	EPA245-350	EPA247-350	90	900	EPA245-900	EPA247-900
40	400	EPA245-400	EPA247-400	95	950	EPA245-950	EPA247-950
42	420	EPA245-420	EPA247-420	100	1000	EPA245-1000	EPA247-1000

†Whichever is greater. Delay times referenced from input to leading edges at 25°C, 5.0V, with no load.

DC Electrical Characteristics

Parameter		Test Conditions	Min	Max	Unit
V_{OH}	High-Level Output Voltage	V_{CC} = min. V_{IL} = max. I_{OH} = max	2.7		V
V_{OL}	Low-Level Output Voltage	V_{CC} = min. V_{IH} = min. I_{OL} = max		0.5	V
V_{IK}	Input Clamp Voltage	V_{CC} = min. I_I = II_K		-1.2	V
I_{IH}	High-Level Input Current	V_{CC} = max. V_{IN} = 2.7V		50	µA
		V_{CC} = max. V_{IN} = 5.25V		1.0	mA
I_{IL}	Low-Level Input Current	V_{CC} = max. V_{IN} = 0.5V		-2	mA
I_{OS}	Short Circuit Output Current	V_{CC} = max. V_{OUT} = 0. (One output at a time)	-40	-100	mA
I_{CCH}	High-Level Supply Current	V_{CC} = max. V_{IN} = OPEN		150	mA
I_{CCL}	Low-Level Supply Current	V_{CC} = max. V_{IN} = 0		150	mA
T_{RO}	Output Rise Time	Td ≤ 500 nS (0.75 to 2.4 Volts)		4	nS
		Td > 500 nS		5	nS
N_H	Fanout High-Level Output	V_{CC} = max. V_{OH} = 2.7V		20 TTL LOAD	
N_L	Fanout Low-Level Output	V_{CC} = max. V_{OL} = 0.5V		10 TTL LOAD	

Schematic



Input Pulse Test Conditions @ 25° C

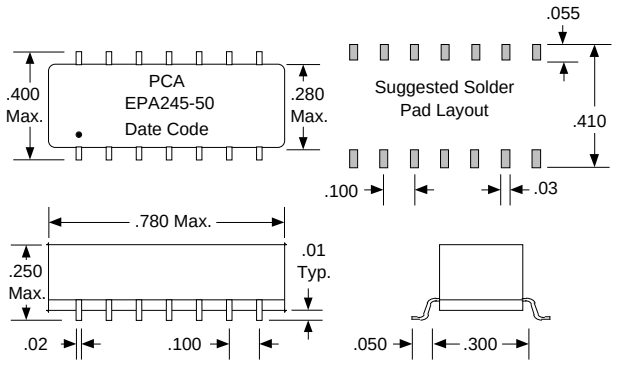
			Unit
E_{IN}	Pulse Input Voltage	3.2	Volts
P_W	Pulse Width % of Total Delay	110	%
T_{RI}	Pulse Rise Time (0.75 - 2.4 Volts)	2.0	nS
P_{RR}	Pulse Repetition Rate @ Td ≤ 200 nS	1.0	MHz
	Pulse Repetition Rate @ Td > 200 nS	100	KHz
V_{CC}	Supply Voltage	5.0	Volts

Recommended Operating Conditions

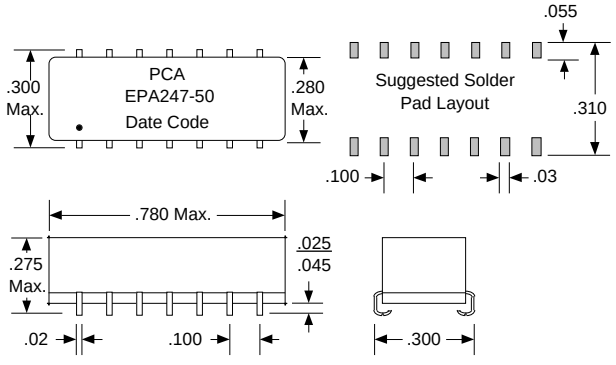
		Min	Max	Unit
V_{CC}	Supply Voltage	4.75	5.25	V
V_{IH}	High-Level Input Voltage	2.0		V
V_{IL}	Low-Level Input Voltage		0.8	V
I_{IK}	Input Clamp Current		-18	mA
I_{OH}	High-Level Output Current		-1.0	mA
I_{OL}	Low-Level Output Current		20	mA
P_W*	Pulse Width of Total Delay	40		%
d*	Duty Cycle		40	%
T_A	Operating Free-Air Temperature	0	+70	°C

**These two values are inter-dependent.*

Gull-Wing Package



J-Lead Package



Unless Otherwise Noted Dimensions in Inches
Tolerances:
Fractional = ± 1/32
.XX = ± .030 .XXX = ± .010

PCA ELECTRONICS INC.

16799 SCHOENBORN ST.
NORTH HILLS, CA 91343
TEL: (818) 892-0761
FAX: (818) 894-5791