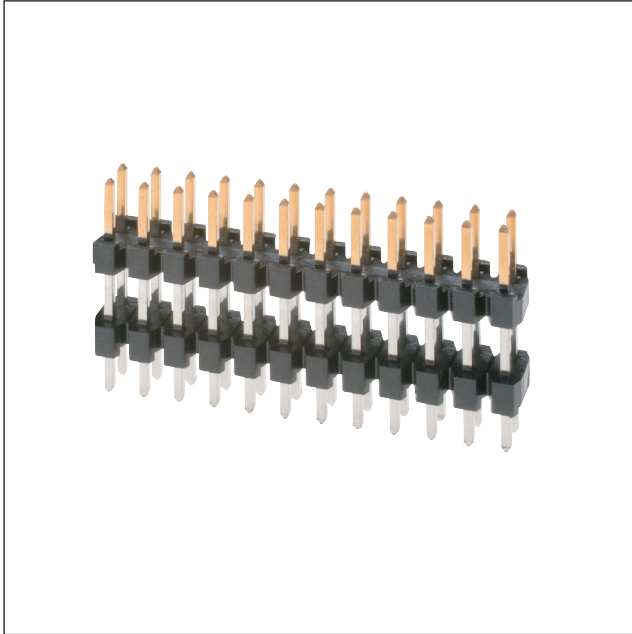


3M™ Pin Strip Header

2 mm and 2 mm x 2 mm Dual Insulator, Straight, Solder Tail

951 Series



- High temperature dielectric
- “No lead” wave and reflow solder compatible
- Optimizes use of PC board space
- End stackable feature
- Side stackable feature
- RoHS Compliant. See the Regulatory Information Appendix (RIA) in the „RoHS compliance“ section of www.3Mconnectors.com for compliance information (RIA E1 & C1 apply)

Date Modified: November 27, 2012

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Sheet 1 of 2

Physical

Insulation

Material: High Temperature Thermoplastic
Flammability: UL 94V-0
Color: Black

Contact

Material: Copper Alloy

Plating

Underplating: 1.25–2.5 μm [50–100 μ''] Nickel
Wiping Area: Flash Gold
Solder Tail Area: 1–3 μm [40–118 μ''] Matte Tin

Electrical

Current Rating: 1 A

Insulation Resistance: $> 5 \times 10^8$ Ohm at 100 V_{DC}

Withstanding Voltage: 1000 V_{DC}

Transition Resistance: ≤ 20 mOhm

Environmental

Temperature Range: -40 °C to +105 °C

Processing Temperature: max. 260 °C (Profile per J-STD-020C)

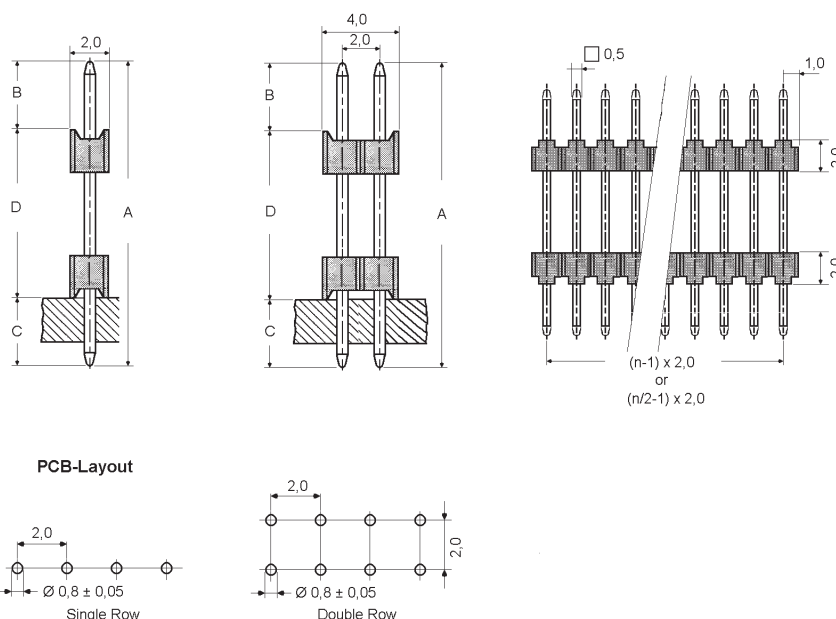
Moisture Sensitivity Level: 1 (per J-STD-020C)

UL File No.: E68080

3M™ Pin Strip Header

2 mm and 2 mm x 2 mm Dual Insulator, Straight, Solder Tail

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Contact Dimensions				
Coding No.	A	Mating Length B	C	D
08	10.8	4.0	2.8	4.0
11	11.9	4.0	2.8	5.1
15	14.3	4.0	2.8	7.5
19	16.5	4.0	2.8	9.7
22	18.6	4.0	2.8	11.8
25	21.2	4.0	2.8	14.4
Others on Request				

Contact Dimensions				
Coding No.	A	Mating Length B	C	D
08	12.7	5.9	2.8	4.0
11	13.8	5.9	2.8	5.1
15	16.2	5.9	2.8	7.5
19	18.4	5.9	2.8	9.7
22	20.5	5.9	2.8	11.8
25	23.1	5.9	2.8	14.4
Others on Request				

n = No. of Contacts

Tolerance Unless Noted [mm]				
Nominal Size	0.5–3	3–6	6–30	30–120
Linear Dim	+/- 0.1	+/- 0.1	+/- 0.2	+/- 0.3
Radii	+/- 0.2	+/- 0.5	+/- 1	+/- 1

Ordering Information

9514XX — XXX XX 06 — AR (RIA E1 & C1 Apply)

Contact Quantity:
02–40 Pins / 1 Row
04–80 Pins / 2 Row

Mounting Style:
8 = Solder Tail, 2 Row
9 = Solder Tail, 1 Row

Mating Pin Length
Dimension B:
01 = 4.0 mm
06 = 5.9 mm

Bord Stacking Height (D):
Coding Number: see Table

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Sheet 2 of 2

Important Notice

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