TOSHIBA MULTI CHIP DISCRETE DEVICE

HN2E05J

Super High Speed Switching Application Interface Circuit Driver Circuit Applications

Q1

Since bias resistor is built in the transistor, the miniaturization of the apparatus by curtailment of the number of parts and laborsaving of an assembly are possible.

Q2

Q1(Transistor) : RN2104F equivalent Q2(Transistor) : 1SS352 equivalent

Q1(Transistor) Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol (Rating	Unit
Collector-base voltage	V _{CBO}	50	<\v
Collector-emitter voltage	VCEO	_50	V
Emitter-base voltage	VEBO	-10	\ V
Collector current	((Ic))	-100	mΑ

Unit: mm 28-0.3 1.6-0.1 1.8ASE 2.EMITTER 3.ANODE 4.CATHODE 5.COLLECTOR JEDEC JETA TOSHIBA 2-3L1E

Weight:0.014g (typ.)

Q2(Diode) Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Maximum (peak) reverse voltage	→ V _{RM}	85	V
Reverse voltage	VR	80	V
Maximum (peak) forward current	I _{FM}	200	mA
Average forward current	ΙQ	100	mA
Surge current (10ms)	JESM	1	Α

Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

Characteristic		Symbol	Rating	Unit
Collector power dissipation	>	P _C *	300	mW
Junction temperature		Tj	150	°C
Storage temperature range		T _{stg}	−55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

^{*} Total rating. 200mW per 1 element must not be exceeded.

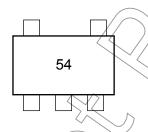
Q1(Transistor) Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off current	I _{CBO}	$V_{CB} = -50 \text{ V}, I_E = 0$	_	_	-100	nA	
	I _{CEO}	$V_{CE} = -50 \text{ V}, I_B = 0$	_	_	-500	ш	
Emitter cut-off current	I _{EBO}	$V_{EB} = -10 \text{ V}, I_{C} = 0$	-0.082	_	-0.15	mA	
DC current gain	h _{FE}	$V_{CE} = -5 \text{ V}, I_{C} = -10 \text{ mA}$	80	_	_		
Collector-emitter saturation voltage	V _{CE} (sat)	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	(F) 🗡 0.1	-0.3	٧	
Input voltage (ON)	V _{I (ON)}	$V_{CE} = -0.2 \text{ V}, I_{C} = -5 \text{ mA}$	-1.5	_	-5.0	V	
Input voltage (OFF)	V _{I (OFF)}	V _{CE} = -5 V, I _C = -0.1 mA	_1)0	_	-1.5	V	
Transition frequency	f _T	V _{CE} = -10 V, I _C = -5 mA	_	200	_	MHz	
Collector output capacitance	C _{ob}	V _{CB} = -10 V, I _E = 0, f = 1 MHz	^ —	3	_	pF	
Input resistor	R1	-	32.9	47	61.1	kΩ	
Resistor ratio	R1/R2		0.9	<1 <u>0</u>	1.1		

Q2(Diode) Electrical Characteristics (Ta = 25°C)

					_ (//		
Characteristic	Symbol	Test Circuit	Test Condition	Min	> Typ.	Max	Unit
	V _{F (1)}	_	IF = 1mA	(-)	0.62	_	
Forward voltage	V _{F (2)}		I _F = 10mA)	0.75	_	V
	V _{F (3)}		I _F = 100mA) —	0.98	1.20	1
Reverse current	I _{R (1)}	(-)	V _R = 30V	_	_	0.1	μA
	I _{R (2)}		V _R = 80V	_	-	0.5	
Total capacitance	C _T () }	V _R = 0, f = 1MHz	_	0.5	_	pF
Reverse recovery time	triy	<i></i>	I _F = 10mA (fig.1)	_	1.6	_	ns

Marking



Equivalent Circuit (Top View)

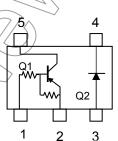
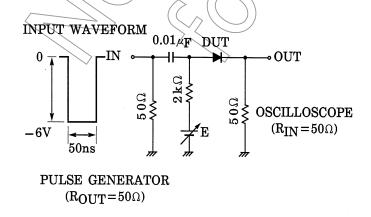
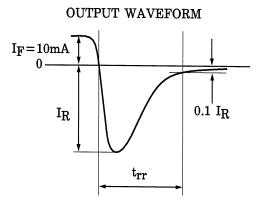
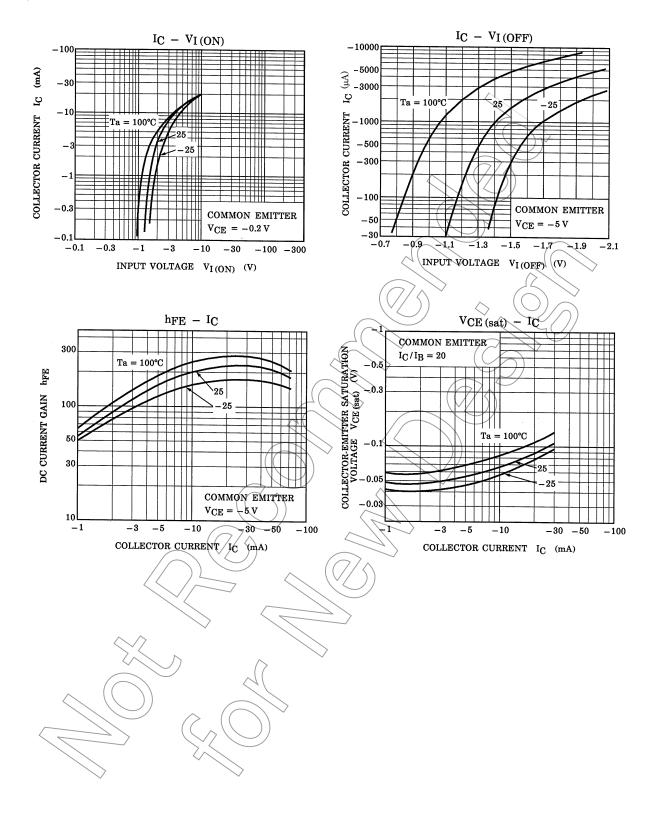


Fig.1 : Reverse Recovery Time (t_{rr}) Test Circuit

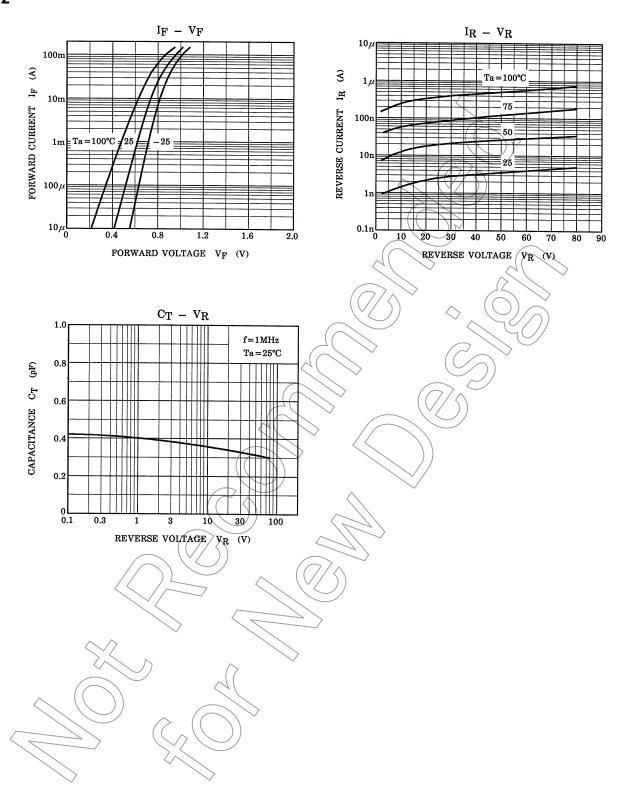




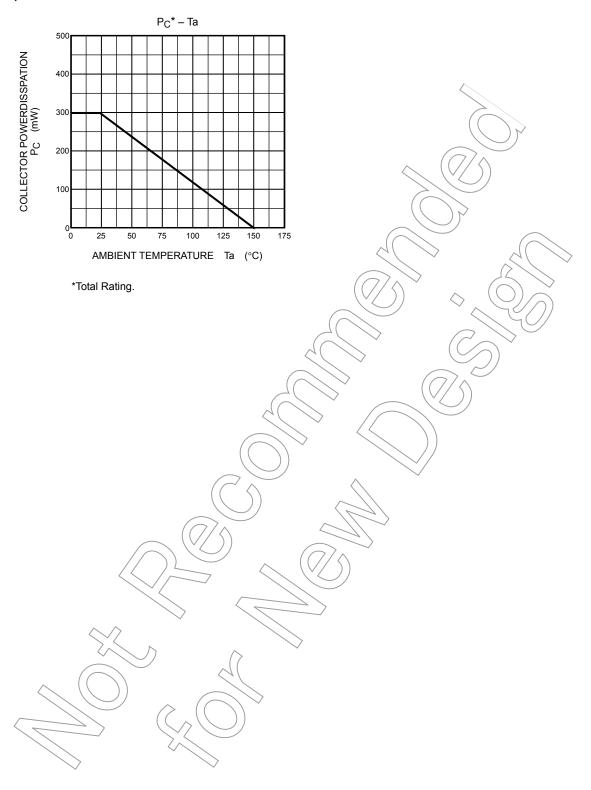
Q1



Q2



Q1,Q2 Common



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