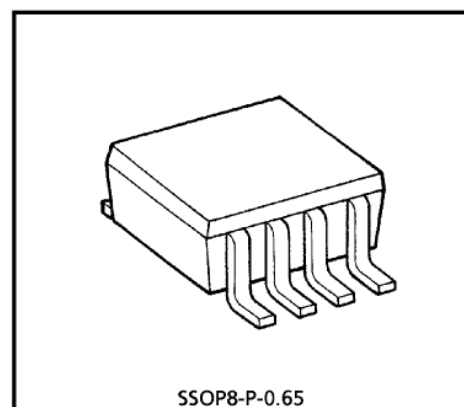


TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC3W03FU

CRYSTAL OSCILLATOR

The TC3W03FU is a IC for high speed CMOS crystal oscillator fabricated with silicon gate C²MOS technology. It can be used to make high efficient crystal oscillator with certain output signal by added external crystal oscillation unit, some capacitor and resistor. It has selective 4-step (f_0 , $1/2f_0$, $1/4f_0$, $1/8f_0$) frequency divide down function. And by setting the disable oscillate input (CE) to low level, the output (Q) becomes high impedance. All inputs are equipped with protection circuits against static discharge or transient excess voltage.



SSOP8-P-0.65

Weight : 0.02g (Typ.)

FEATURES

- Wide oscillation frequency range ... $f_{osc} = 1\text{MHz} \sim 40\text{MHz}$
- Incorporated frequency divide down step
... selective f_0 , $1/2f_0$, $1/4f_0$ or $1/8f_0$
- 3-state output
- Output drive capability ... 10 LSTTL loads
- Very small package

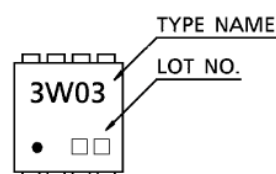
MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage Range	V_{CC}	$-0.5 \sim 7.0$	V
DC Input Voltage	V_{IN}	$-0.5 \sim V_{CC} + 0.5$	V
DC Output Voltage	V_{OUT}	$-0.5 \sim V_{CC} + 0.5$	V
Input Diode Current	I_{IN}	± 20	mA
Output Diode Current	I_{OUT}	± 25	mA
Power Dissipation	P_C	300	mW
Storage Temperature	T_{stg}	$-65 \sim 150$	°C
Lead Temperature (10s)	T_L	260	°C

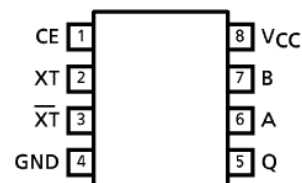
(Note)

This IC is used only for crystal oscillation. So, this is unfit for DC~low frequency range operation and frequency divide down.

MARKING



PIN ASSIGNMENT (TOP VIEW)



TRUTH TABLE

INPUTS			OUTPUTS
CE	A	B	Q
H	L	L	f_o
	L	H	$1/2f_o$
	H	L	$1/4f_o$
	H	H	$1/8f_o$
L	Don't care		Z

Z : High impedance

PIN DESIGNATIONS

PIN NO.	SYMBOL	FUNCTION
1	CE	Disable oscillate and Reset
2	XT	Connect to crystal unit divide down step.
3	\overline{XT}	Connect to crystal unit divide down step.
4	GND	Ground
5	Q	Output
6	A	Select for divide down ratio
7	B	Select for divide down ratio
8	V _{CC}	Supply voltage

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	V _{CC}	5 ± 0.5	V
Input Voltage	V _{IN}	0~V _{CC}	V
Operating Temperature	T _{opr}	-45~85	°C

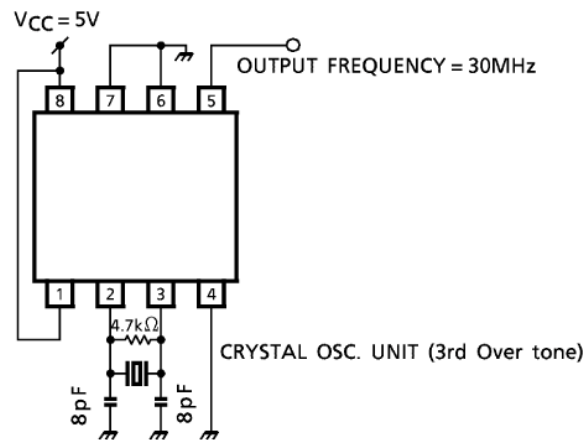
DC ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CIR-CUIT	TEST CONDITION	Ta = 25°C			Ta = -40~85°C		UNIT
				MIN.	TYP.	MAX.	MIN.	MAX.	
High-Level Input Voltage	V _{IH}	—	V _{CC} = 5V	3.5	—	—	3.5	—	V
Low-Level Input Voltage	V _{IL}	—	V _{CC} = 5V	—	—	1.5	—	1.5	V
High-Level Output Current	I _{OH}	—	V _{CC} = 5V	-4.0	—	—	-3.8	—	mA
Low-Level Output Current	I _{OL}	—	V _{CC} = 5V	4.0	—	—	3.8	—	mA
High-Level Input Current	I _{IH}	—	CE = A = B = V _{CC}	—	—	0.1	—	1.0	μA
Low-Level Input Current	I _{IL}	—	A = B = GND	—	—	-0.1	—	-1.0	μA
Quiescent Supply Current	I _{CC}	—	CE = V _{CC} , A = B = GND	—	—	1.0	—	10	μA
Stand by Current	I _{STN}	—	CE = GND	—	—	500	—	700	μA
3-State Output Off-State Current	I _{OZ}	—	A = B = V _{IH} or V _{IL} V _{OUT} = V _{CC} or GND	—	—	±0.5	—	±5.0	μA
Built-in Pull-up Resistor	R _{CE}	—	—	154	220	286	154	286	kΩ

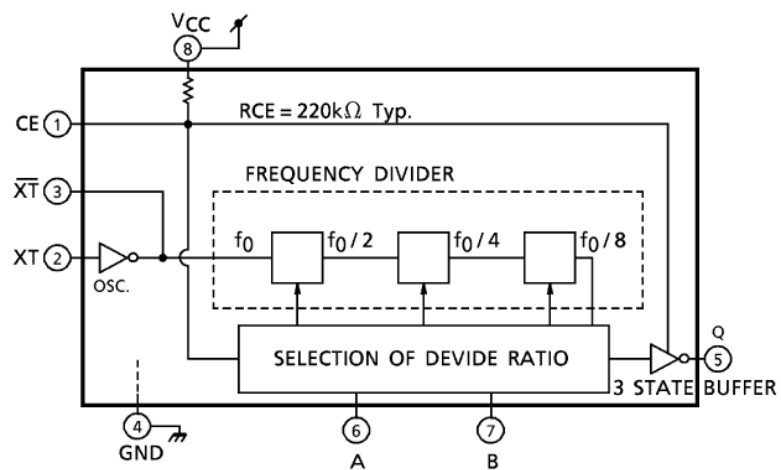
AC ELECTRICAL CHARACTERISTICS (V_{CC} = 5V, Ta = 25°C)

PARAMETER	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	I _{CC(opr)}	—	See application circuit	—	—	22	mA
Operating Frequency Range	f _{opr}	—	See application circuit	1.0	—	40	MHz
Output Wave form Duty	Duty	—	See application circuit	45	50	55	%

APPLICATION CIRCUIT (Example)

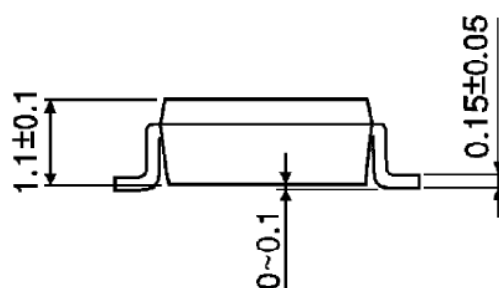
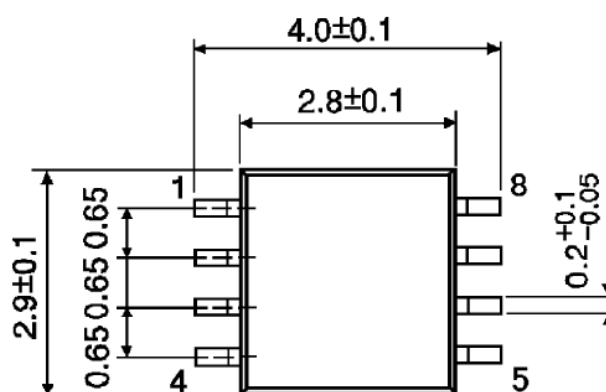


SYSTEM DIAGRAM



PACKAGE DIMENSIONS
SSOP8-P-0.65

Unit : mm



Weight : 0.02g (Typ.)

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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