



# PI74FCT16543T PI74FCT162543T PI74FCT162H543T

## Fast CMOS 16-Bit Latched Transceivers

### Product Features:

#### Common Features:

- PI74FCT16543T, PI74FCT162543T and PI74FCT162H543T are high-speed, low power devices with high current drive.
- $V_{CC} = 5V \pm 10\%$
- Hysteresis on all inputs
- Packages available:
  - 56-pin 240 mil wide plastic TSSOP (A)
  - 56-pin 300 mil wide plastic SSOP (V)

#### PI74FCT16543T Features:

- High output drive:  $I_{OH} = -32\text{ mA}$ ;  $I_{OL} = 64\text{ mA}$
- Power off disable outputs permit “live insertion”
- Typical  $V_{OLP}$  (Output Ground Bounce)  $< 1.0V$  at  $V_{CC} = 5V$ ,  $T_A = 25^\circ C$

#### PI74FCT162543T Features:

- Balanced output drivers:  $\pm 24\text{ mA}$
- Reduced system switching noise
- Typical  $V_{OLP}$  (Output Ground Bounce)  $< 0.6V$  at  $V_{CC} = 5V$ ,  $T_A = 25^\circ C$

#### PI74FCT162H543T Features:

- Bus Hold retains last active state during 3-state
- Eliminates the need for external pull-up resistors

### Product Description:

Pericom Semiconductor's PI74FCT series of logic circuits are produced in the Company's advanced 0.6 micron CMOS technology, achieving industry leading speed grades.

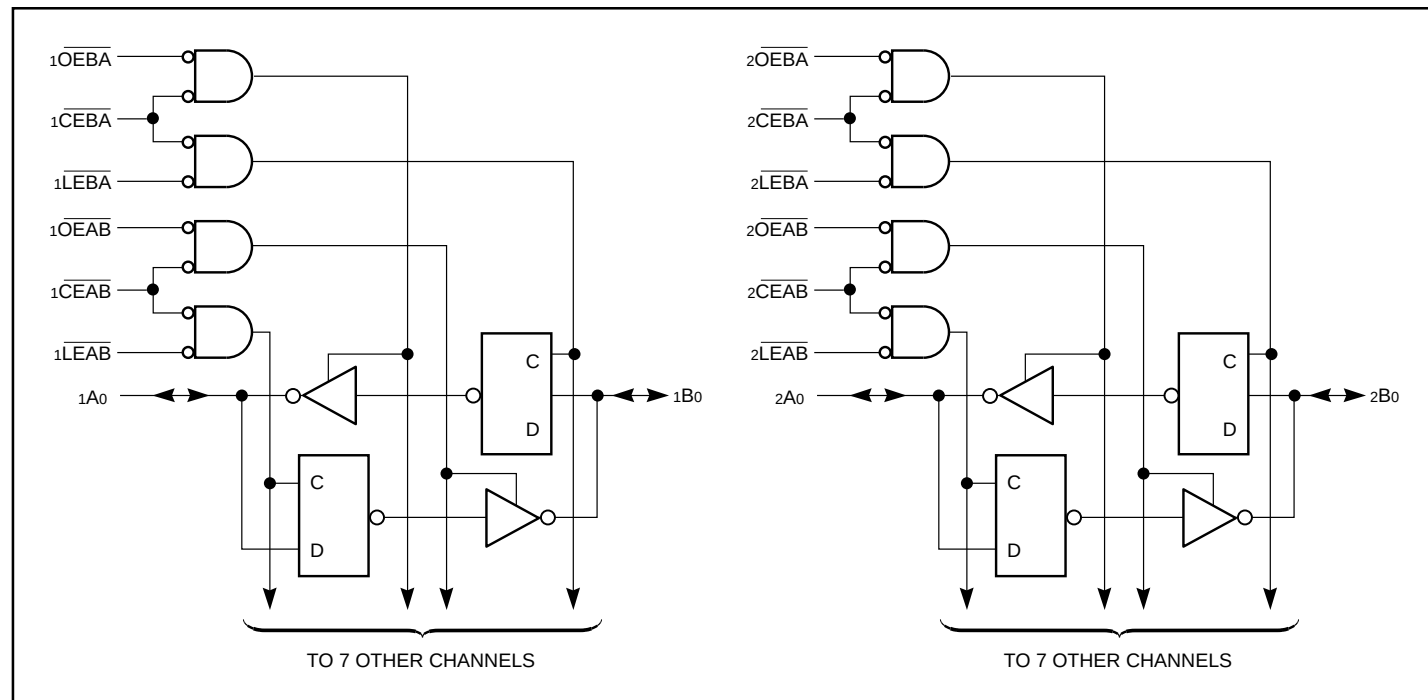
The PI74FCT16543T, PI74FCT162543T and PI74FCT162H543T are 16-bit latched transceivers organized with two sets of eight D-type latches with separate input and output controls for each set. For data flow from A to B, for example, the A-to-B Enable ( $\overline{xCEAB}$ ) input must be LOW in order to enter data from  $xAx$  or to take data from  $xBx$ , as indicated in the Truth Table. With  $\overline{xCEAB}$  LOW, a LOW signal makes the A-to-B latches transparent; a subsequent LOW-to-HIGH transition of the  $\overline{xLEAB}$  signal puts the A latches in the storage mode and their outputs no longer change the A inputs. With  $\overline{xCEAB}$  and  $\overline{xOEAB}$  both LOW, the 3-state B output buffers are active and reflect the data present at the output of the A latches. Control of data from B to A is similar, but uses the  $\overline{xCEBA}$ ,  $\overline{xLEBA}$ , and  $\overline{xOEBA}$  inputs.

The PI74FCT16543T output buffers are designed with a Power-Off disable allowing “live insertion” of boards when used as backplane drivers.

The PI74FCT162543T has  $\pm 24\text{ mA}$  balanced output drivers. It is designed with current limiting resistors at its outputs to control the output edge rate resulting in lower ground bounce and undershoot. This eliminates the need for external terminating resistors for most interface applications.

The PI74FCT162H543T has “Bus Hold” which retains the input's last state whenever the input goes to high-impedance preventing “floating” inputs and eliminating the need for pull-up/down resistors.

### Logic Block Diagram



## Product Pin Description

| Pin Name           | Description                                                 |
|--------------------|-------------------------------------------------------------|
| $\overline{xOEAB}$ | A-to-B Output Enable Input (Active LOW)                     |
| $\overline{xOEBA}$ | B-to-A Output Enable Input (Active LOW)                     |
| $\overline{xCEAB}$ | A-to-B Enable Input (Active LOW)                            |
| $\overline{xCEBA}$ | B-to-A Enable Input (Active LOW)                            |
| $\overline{xLEAB}$ | A-to-B Latch Enable Input (Active LOW)                      |
| $\overline{xLEBA}$ | B-to-A Latch Enable Input (Active LOW)                      |
| $xAx$              | A-to-B Data Inputs or B-to-A 3-State Outputs <sup>(1)</sup> |
| $xBx$              | B-to-A Data Inputs or B-to-A 3-State Outputs <sup>(1)</sup> |
| GND                | Ground                                                      |
| VCC                | Power                                                       |

**Note:** 1. For the PI74FCT162H543T, these pins have “Bus Hold.” All other pins are standard, outputs, or I/Os.

## Truth Table<sup>(1)</sup>

| Inputs             |                    |                    | Latch Status   | Output Buffers     |
|--------------------|--------------------|--------------------|----------------|--------------------|
| $\overline{xCEAB}$ | $\overline{xLEAB}$ | $\overline{xOEAB}$ | $xAx$ to $xBx$ | $xBx$              |
| H                  | X                  | X                  | Storing        | High Z             |
| X                  | H                  | X                  | Storing        | X                  |
| X                  | X                  | H                  | X              | High Z             |
| L                  | L                  | L                  | Transparent    | Current A Inputs   |
| L                  | H                  | L                  | Storing        | Previous* A Inputs |

### Notes:

- \*Before  $\overline{xLEAB}$  LOW-to-HIGH Transistion  
H = High Voltage Level  
L = Low Voltage Level  
X = Don't Care or Irrelevant  
Z = High Impedance
- A-to-B data flow shown. B-to-A flow control is the same, except using  $\overline{xCEBA}$ ,  $\overline{xLEBA}$ , and  $\overline{xOEBA}$ .

## Product Pin Configuration

|                    |    |    |                    |
|--------------------|----|----|--------------------|
| $\overline{1OEAB}$ | 1  | 56 | $\overline{1OEBA}$ |
| $\overline{1LEAB}$ | 2  | 55 | $\overline{1LEBA}$ |
| $\overline{1CEAB}$ | 3  | 54 | $\overline{1CEBA}$ |
| GND                | 4  | 53 | GND                |
| 1A0                | 5  | 52 | 1B0                |
| 1A1                | 6  | 51 | 1B1                |
| VCC                | 7  | 50 | VCC                |
| 1A2                | 8  | 49 | 1B2                |
| 1A3                | 9  | 48 | 1B3                |
| 1A4                | 10 | 47 | 1B4                |
| GND                | 11 | 46 | GND                |
| 1A5                | 12 | 45 | 1B5                |
| 1A6                | 13 | 44 | 1B6                |
| 1A7                | 14 | 43 | 1B7                |
| 2A0                | 15 | 42 | 2B0                |
| 2A1                | 16 | 41 | 2B1                |
| 2A2                | 17 | 40 | 2B2                |
| GND                | 18 | 39 | GND                |
| 2A3                | 19 | 38 | 2B3                |
| 2A4                | 20 | 37 | 2B4                |
| 2A5                | 21 | 36 | 2B5                |
| VCC                | 22 | 35 | VCC                |
| 2A6                | 23 | 34 | 2B6                |
| 2A7                | 24 | 33 | 2B7                |
| GND                | 25 | 32 | GND                |
| $\overline{2CEAB}$ | 26 | 31 | $\overline{2CEBA}$ |
| $\overline{2LEAB}$ | 27 | 30 | $\overline{2LEBA}$ |
| $\overline{2OEAB}$ | 28 | 29 | $\overline{2OEBA}$ |

### Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

|                                                                          |                 |
|--------------------------------------------------------------------------|-----------------|
| Storage Temperature .....                                                | –65°C to +150°C |
| Ambient Temperature with Power Applied .....                             | –40°C to +85°C  |
| Supply Voltage to Ground Potential (Inputs & V <sub>CC</sub> Only) ..... | –0.5V to +7.0V  |
| Supply Voltage to Ground Potential (Outputs & D/O Only) .....            | –0.5V to +7.0V  |
| DC Input Voltage .....                                                   | –0.5V to +7.0V  |
| DC Output Current .....                                                  | 120mA           |
| Power Dissipation .....                                                  | 1.0W            |

### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

### DC Electrical Characteristics (Over the Operating Range, T<sub>A</sub> = –40°C to +85°C, V<sub>CC</sub> = 5.0V ± 10%)

| Parameters          | Description                         | Test Conditions <sup>(1)</sup>                                  |                                   | Min. | Typ <sup>(2)</sup> | Max. | Units |
|---------------------|-------------------------------------|-----------------------------------------------------------------|-----------------------------------|------|--------------------|------|-------|
| V <sub>IH</sub>     | Input HIGH Voltage                  | Guaranteed Logic HIGH Level                                     |                                   | 2.0  |                    |      | V     |
| V <sub>IL</sub>     | Input LOW Voltage                   | Guaranteed Logic LOW Level                                      |                                   |      |                    | 0.8  | V     |
| I <sub>IH</sub>     | Input HIGH Current                  | Standard Input, V <sub>CC</sub> = Max.                          | V <sub>IN</sub> = V <sub>CC</sub> |      |                    | 1    | μA    |
| I <sub>IH</sub>     | Input HIGH Current                  | Standard I/O, V <sub>CC</sub> = Max.                            | V <sub>IN</sub> = V <sub>CC</sub> |      |                    | 1    | μA    |
| I <sub>IH</sub>     | Input HIGH Current                  | Bus Hold Input <sup>(4)</sup> , V <sub>CC</sub> = Max.          | V <sub>IN</sub> = V <sub>CC</sub> |      |                    | ±100 | μA    |
| I <sub>IH</sub>     | Input HIGH Current                  | Bus Hold I/O <sup>(4)</sup> , V <sub>CC</sub> = Max.            | V <sub>IN</sub> = V <sub>CC</sub> |      |                    | ±100 | μA    |
| I <sub>IL</sub>     | Input LOW Current                   | Standard Input, V <sub>CC</sub> = Min.                          | V <sub>IN</sub> = GND             |      |                    | –1   | μA    |
| I <sub>IL</sub>     | Input LOW Current                   | Standard I/O, V <sub>CC</sub> = Min.                            | V <sub>IN</sub> = GND             |      |                    | –1   | μA    |
| I <sub>IL</sub>     | Input LOW Current                   | Bus Hold Input <sup>(4)</sup> , V <sub>CC</sub> = Min.          | V <sub>IN</sub> = GND             |      |                    | ±100 | μA    |
| I <sub>IL</sub>     | Input LOW Current                   | Bus Hold I/O <sup>(4)</sup> , V <sub>CC</sub> = Min.            | V <sub>IN</sub> = GND             |      |                    | ±100 | μA    |
| I <sub>BHH</sub>    | Bus Hold                            | Bus Hold Input <sup>(4)</sup> , V <sub>CC</sub> = Min.          | V <sub>IN</sub> = 2.0V            | –50  |                    |      | μA    |
| I <sub>BHL</sub>    | Sustain Current                     |                                                                 | V <sub>IN</sub> = 0.8V            | +50  |                    |      | μA    |
| IOZH <sup>(5)</sup> | High-Impedance                      | V <sub>CC</sub> = Max.                                          | V <sub>OUT</sub> = 2.7V           |      |                    | 1    | μA    |
| IOZL <sup>(5)</sup> | Output Current<br>(3-STATE OUTPUTS) | V <sub>CC</sub> = Max.                                          | V <sub>OUT</sub> = 0.5V           |      |                    | –1   | μA    |
| V <sub>IK</sub>     | Clamp Diode Voltage                 | V <sub>CC</sub> = Min., I <sub>IN</sub> = –18mA                 |                                   |      | –0.7               | –1.2 | V     |
| I <sub>OS</sub>     | Short Circuit Current               | V <sub>CC</sub> = Max. <sup>(3)</sup> , V <sub>OUT</sub> = GND  |                                   | –80  | –140               | –200 | mA    |
| I <sub>O</sub>      | Output Drive Current                | V <sub>CC</sub> = Max. <sup>(3)</sup> , V <sub>OUT</sub> = 2.5V |                                   | –50  |                    | –180 | mA    |
| V <sub>H</sub>      | Input Hysteresis                    |                                                                 |                                   |      | 100                |      | mV    |

### Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V<sub>CC</sub> = 5.0V, +25°C ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. Pins with Bus Hold are identified in the pin description.
5. This specification does not apply to bi-directional functionalities with Bus Hold.

**PI74FCT16543T Output Drive Characteristics** (Over the Operating Range)

| Parameters | Description         | Test Conditions <sup>(1)</sup>    |                | Min. | Typ <sup>(2)</sup> | Max. | Units |
|------------|---------------------|-----------------------------------|----------------|------|--------------------|------|-------|
| VOH        | Output HIGH Voltage | VCC = Min., VIN = VIH or VIL      | IOH = -3.0 mA  | 2.5  | 3.5                |      | V     |
|            |                     |                                   | IOH = -15.0 mA | 2.4  | 3.5                |      |       |
|            |                     |                                   | IOH = -32.0 mA | 2.0  | 3.0                |      |       |
| VOL        | Output LOW Voltage  | VCC = Min., VIN = VIH or VIL      | IOL = 64 mA    |      | 0.2                | 0.55 | V     |
| IOFF       | Power Down Disable  | VCC = 0V, VIN or VOUT $\leq$ 4.5V |                | —    | —                  | ±100 | μA    |

**PI74FCT162543T/162H543T Output Drive Characteristics** (Over the Operating Range)

| Parameters | Description         | Test Conditions <sup>(1)</sup>                         |                | Min. | Typ <sup>(2)</sup> | Max. | Units |
|------------|---------------------|--------------------------------------------------------|----------------|------|--------------------|------|-------|
| VOH        | Output HIGH Voltage | VCC = Min., VIN = VIH or VIL                           | IOH = -24.0 mA | 2.4  | 3.3                |      | V     |
| VOL        | Output LOW Voltage  | VCC = Min., VIN = VIH or VIL                           | IOL = 24 mA    |      | 0.3                | 0.55 | V     |
| IODL       | Output LOW Current  | VCC = 5V, VIN = VIH or VIL, VOUT = 1.5V <sup>(3)</sup> |                | 60   | 115                | 150  | mA    |
| IODH       | Output HIGH Current | VCC = 5V, VIN = VIH or VIL, VOUT = 1.5V <sup>(3)</sup> |                | -60  | -115               | -150 | mA    |

**Capacitance** (TA = 25°C, f = 1 MHz)

| Parameters <sup>(4)</sup> | Description        | Test Conditions | Typ | Max. | Units |
|---------------------------|--------------------|-----------------|-----|------|-------|
| CIN                       | Input Capacitance  | VIN = 0V        | 4.5 | 6    | pF    |
| COUT                      | Output Capacitance | VOUT = 0V       | 5.5 | 8    | pF    |

**Notes:**

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at VCC = 5.0V, +25°C ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. This parameter is determined by device characterization but is not production tested.

### Power Supply Characteristics

| Parameters       | Description                                     | Test Conditions <sup>(1)</sup>                                                                                                                                                                                             |                                                            | Min. | Typ <sup>(2)</sup> | Max.                | Units      |
|------------------|-------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------|------|--------------------|---------------------|------------|
| I <sub>CC</sub>  | Quiescent Power Supply Current                  | V <sub>CC</sub> = Max.                                                                                                                                                                                                     | V <sub>IN</sub> = GND or V <sub>CC</sub>                   |      | 0.1                | 500                 | μA         |
| ΔI <sub>CC</sub> | Supply Current per Input @ TTL HIGH             | V <sub>CC</sub> = Max.                                                                                                                                                                                                     | V <sub>IN</sub> = 3.4V <sup>(3)</sup>                      |      | 0.5                | 1.5                 | mA         |
| I <sub>CCD</sub> | Supply Current per Input per MHz <sup>(4)</sup> | V <sub>CC</sub> = Max., Outputs Open<br>x <sub>CEAB</sub> & x <sub>OEAB</sub> = GND<br>x <sub>CEBA</sub> = V <sub>CC</sub><br>One Bit Toggling<br>50% Duty Cycle                                                           | V <sub>IN</sub> = V <sub>CC</sub><br>V <sub>IN</sub> = GND |      | 60                 | 100                 | μA/<br>MHz |
| I <sub>C</sub>   | Total Power Supply Current <sup>(6)</sup>       | V <sub>CC</sub> = Max.,<br>Outputs Open<br>f <sub>I</sub> = 10 MHz<br>50% Duty Cycle<br>x <sub>LEAB</sub> , x <sub>CEAB</sub> , and<br>x <sub>OEAB</sub> = GND<br>x <sub>CEBA</sub> = V <sub>CC</sub><br>One Bit Toggling  | V <sub>IN</sub> = V <sub>CC</sub><br>V <sub>IN</sub> = GND |      | 0.6                | 1.5 <sup>(5)</sup>  | mA         |
|                  |                                                 |                                                                                                                                                                                                                            | V <sub>IN</sub> = 3.4V<br>V <sub>IN</sub> = GND            |      | 0.9                | 2.3 <sup>(5)</sup>  |            |
|                  |                                                 | V <sub>CC</sub> = Max.,<br>Outputs Open<br>f <sub>I</sub> = 2.5 MHz<br>50% Duty Cycle<br>x <sub>LEAB</sub> , x <sub>CEAB</sub> , and<br>x <sub>OEAB</sub> = GND<br>x <sub>CEBA</sub> = V <sub>CC</sub><br>16 Bits Toggling | V <sub>IN</sub> = V <sub>CC</sub><br>V <sub>IN</sub> = GND |      | 2.4                | 4.5 <sup>(5)</sup>  |            |
|                  |                                                 |                                                                                                                                                                                                                            | V <sub>IN</sub> = 3.4V<br>V <sub>IN</sub> = GND            |      | 6.4                | 16.5 <sup>(5)</sup> |            |

#### Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- Typical values are at V<sub>CC</sub> = 5.0V, +25°C ambient.
- Per TTL driven input (V<sub>IN</sub> = 3.4V); all other inputs at V<sub>CC</sub> or GND.
- This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
- Values for these conditions are examples of the I<sub>CC</sub> formula. These limits are guaranteed but not tested.
- I<sub>C</sub> = I<sub>QUIESCENT</sub> + I<sub>INPUTS</sub> + I<sub>DYNAMIC</sub>  
I<sub>C</sub> = I<sub>CC</sub> + ΔI<sub>CC</sub> D<sub>H</sub>N<sub>T</sub> + I<sub>CCD</sub> (f<sub>CP</sub>/2 + f<sub>I</sub>N<sub>I</sub>)  
I<sub>CC</sub> = Quiescent Current  
ΔI<sub>CC</sub> = Power Supply Current for a TTL High Input (V<sub>IN</sub> = 3.4V)  
D<sub>H</sub> = Duty Cycle for TTL Inputs High  
N<sub>T</sub> = Number of TTL Inputs at D<sub>H</sub>  
I<sub>CCD</sub> = Dynamic Current Caused by an Input Transition Pair (HLH or LHL)  
f<sub>CP</sub> = Clock Frequency for Register Devices (Zero for Non-Register Devices)  
f<sub>I</sub> = Input Frequency  
N<sub>I</sub> = Number of Inputs at f<sub>I</sub>  
All currents are in milliamps and all frequencies are in megahertz.

**PI74FCT16543T Switching Characteristics over Operating Range**

| Parameters   | Description                                                        | Conditions <sup>(1)</sup> | 16543T |      | 16543AT |     | 16543CT |     | 16543DT |     | 16543ET |     | Unit |
|--------------|--------------------------------------------------------------------|---------------------------|--------|------|---------|-----|---------|-----|---------|-----|---------|-----|------|
|              |                                                                    |                           | Com.   |      | Com.    |     | Com.    |     | Com.    |     | Com.    |     |      |
|              |                                                                    |                           | Min    | Max  | Min     | Max | Min     | Max | Min     | Max | Min     | Max |      |
| tPLH<br>tPHL | PropagationDelay<br>TransparentMode<br>xAx to xBx or xBx to xAx    | CL = 50 pF<br>RL = 500Ω   | 2.5    | 8.5  | 2.5     | 6.5 | 2.5     | 5.3 | 2.5     | 4.4 | 1.5     | 3.4 | ns   |
| tPLH<br>tPHL | Propagation Delay<br>xLEBA to xAx, xLEAB to xBx                    |                           | 2.5    | 12.5 | 2.5     | 8.0 | 2.5     | 7.0 | 2.5     | 5.0 | 1.5     | 3.7 | ns   |
| tpZH<br>tpZL | Output Enable Time<br>xOEBA or xOEAB to xAx or xBx                 |                           | 2.0    | 12.0 | 2.0     | 9.0 | 2.0     | 8.0 | 2.0     | 5.4 | 1.5     | 4.8 | ns   |
| tpHZ<br>tPLZ | Output Disable Time <sup>(3)</sup><br>xOEBA or xOEAB to xAx or xBx |                           | 2.0    | 9.0  | 2.0     | 7.5 | 2.0     | 6.5 | 2.0     | 4.3 | 1.5     | 4.0 | ns   |
| tsu          | Setup Time HIGH or LOW<br>xAx or xBx to xLEAB or xLEBA             |                           | 3.0    | —    | 2.0     | —   | 2.0     | —   | 2.0     | —   | 1.0     | —   | ns   |
| th           | Hold Time HIGH or LOW<br>xAx or xBx to xLEAB or xLEBA              |                           | 2.0    | —    | 2.0     | —   | 2.0     | —   | 1.5     | —   | 1.0     | —   | ns   |
| tw           | xLEABorxLEBA Pulse Width 5.0<br>LOW <sup>(3)</sup>                 |                           | —      | 5.0  | —       | 5.0 | —       | 3.0 | —       | 3.0 | —       | 3.0 | ns   |
| tsk(o)       | Output Skew <sup>(4)</sup>                                         |                           | —      | 0.5  | —       | 0.5 | —       | 0.5 | —       | 0.5 | —       | 0.5 | ns   |

**PI74FCT162543T Switching Characteristics over Operating Range**

| Parameters   | Description                                                        | Conditions <sup>(1)</sup> | 162543T |      | 162543AT |     | 162543CT |     | 162543DT |     | 162543ET |     | Unit |
|--------------|--------------------------------------------------------------------|---------------------------|---------|------|----------|-----|----------|-----|----------|-----|----------|-----|------|
|              |                                                                    |                           | Com.    |      | Com.     |     | Com.     |     | Com.     |     | Com.     |     |      |
|              |                                                                    |                           | Min     | Max  | Min      | Max | Min      | Max | Min      | Max | Min      | Max |      |
| tPLH<br>tPHL | PropagationDelay<br>TransparentMode<br>xAx to xBx or xBx to xAx    | CL = 50 pF<br>RL = 500Ω   | 25      | 85   | 25       | 65  | 25       | 53  | 25       | 44  | 15       | 34  | ns   |
| tPLH<br>tPHL | Propagation Delay<br>xLEBA to xAx, xLEAB to xBx                    |                           | 2.5     | 12.5 | 2.5      | 8.0 | 2.5      | 7.0 | 2.5      | 5.0 | 1.5      | 3.7 | ns   |
| tpZH<br>tpZL | Output Enable Time<br>xOEBA or xOEAB to xAx or xBx                 |                           | 2.0     | 12.0 | 2.0      | 9.0 | 2.0      | 8.0 | 2.0      | 5.4 | 1.5      | 4.8 | ns   |
| tpHZ<br>tPLZ | Output Disable Time <sup>(3)</sup><br>xOEBA or xOEAB to xAx or xBx |                           | 2.0     | 9.0  | 2.0      | 7.5 | 2.0      | 6.5 | 2.0      | 4.3 | 1.5      | 4.0 | ns   |
| tsu          | Setup Time HIGH or LOW<br>xAx or xBx to xLEAB or xLEBA             |                           | 3.0     | —    | 2.0      | —   | 2.0      | —   | 2.0      | —   | 1.0      | —   | ns   |
| th           | Hold Time HIGH or LOW<br>xAx or xBx to xLEAB or xLEBA              |                           | 2.0     | —    | 2.0      | —   | 2.0      | —   | 1.5      | —   | 1.0      | —   | ns   |
| tw           | xLEABorxLEBA Pulse Width 5.0<br>LOW <sup>(3)</sup>                 |                           | —       | 5.0  | —        | 5.0 | —        | 3.0 | —        | 3.0 | —        | 3.0 | ns   |
| tsk(0)       | Output Skew <sup>(4)</sup>                                         |                           | —       | 0.5  | —        | 0.5 | —        | 0.5 | —        | 0.5 | —        | 0.5 | ns   |

**Notes:**

1. See test circuit and wave forms.
2. Minimum limits are guaranteed but not tested on Propagation Delays.
3. This parameter is guaranteed but not production tested.
4. Skew between any two outputs, of the same package, switching in the same direction.  
This parameter is guaranteed by design.

**PI74FCT162H543T Switching Characteristics over Operating Range**

| Parameters   | Description                                                  | Conditions <sup>(1)</sup> | 162H543T |      | 162H543AT |     | 162H543CT |     | 162H543DT |     | 162H543ET |     | Unit |
|--------------|--------------------------------------------------------------|---------------------------|----------|------|-----------|-----|-----------|-----|-----------|-----|-----------|-----|------|
|              |                                                              |                           | Com.     |      | Com.      |     | Com.      |     | Com.      |     | Com.      |     |      |
|              |                                                              |                           | Min      | Max  | Min       | Max | Min       | Max | Min       | Max | Min       | Max |      |
| tPLH<br>tPHL | PropagationDelay<br>TransparentMode<br>xAxtoxBxorxBxtoxAx    | CL = 50 pF<br>RL = 500Ω   | 2.5      | 8.5  | 2.5       | 6.5 | 2.5       | 5.3 | 2.5       | 4.4 | 1.5       | 3.4 | ns   |
| tPLH<br>tPHL | PropagationDelay<br>xLEBAtoxAx,xLEABtoxBx                    |                           | 2.5      | 12.5 | 2.5       | 8.0 | 2.5       | 7.0 | 2.5       | 5.0 | 1.5       | 3.7 | ns   |
| tpZH<br>tpZL | Output EnableTime<br>xOEBAorxOEABtoxAxorxBx                  |                           | 2.0      | 12.0 | 2.0       | 9.0 | 2.0       | 8.0 | 2.0       | 5.4 | 1.5       | 4.8 | ns   |
| tpHZ<br>tPLZ | Output Disable Time <sup>(3)</sup><br>xOEBAorxOEABtoxAxorxBx |                           | 2.0      | 9.0  | 2.0       | 7.5 | 2.0       | 6.5 | 2.0       | 4.3 | 1.5       | 4.0 | ns   |
| tsU          | Setup TimeHIGHorLOW<br>xAxorxBxtoxLEABorxLEBA                |                           | 3.0      | —    | 2.0       | —   | 2.0       | —   | 2.0       | —   | 1.0       | —   | ns   |
| tH           | Hold TimeHIGHorLOW<br>xAxorxBxtoxLEABorxLEBA                 |                           | 2.0      | —    | 2.0       | —   | 2.0       | —   | 1.5       | —   | 1.0       | —   | ns   |
| tw           | xLEABorxLEBAPulse Width 5.0<br>LOW <sup>(3)</sup>            |                           | —        | 5.0  | —         | 5.0 | —         | 3.0 | —         | 3.0 | —         | 3.0 | ns   |
| tsk(o)       | Output Skew <sup>(4)</sup>                                   |                           | —        | 0.5  | —         | 0.5 | —         | 0.5 | —         | 0.5 | —         | 0.5 | ns   |

**Notes:**

1. See test circuit and wave forms.
2. Minimum limits are guaranteed but not tested on Propagation Delays.
3. This parameter is guaranteed but not production tested.
4. Skew between any two outputs, of the same package, switching in the same direction. This parameter is guaranteed by design.