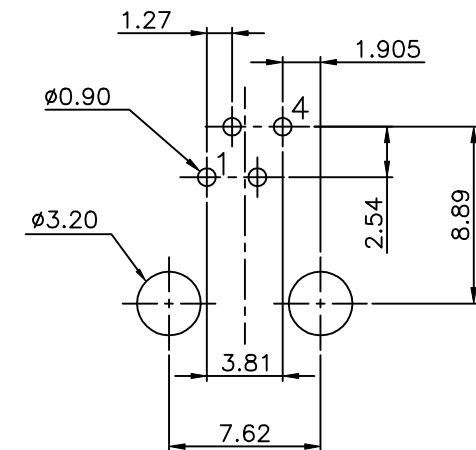
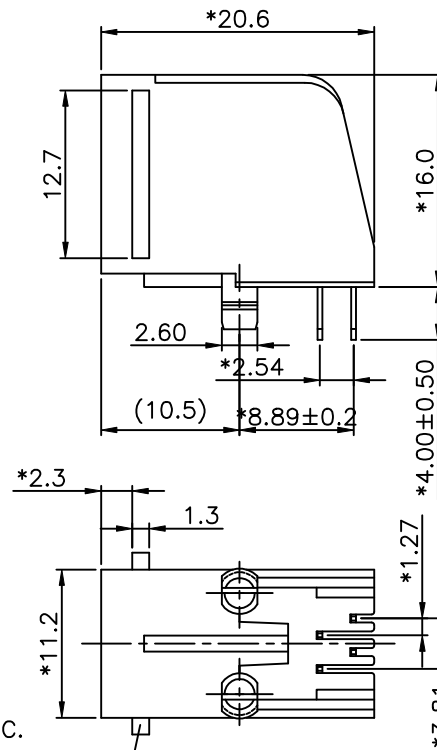
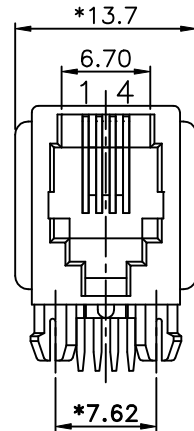


REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD



PC Board Layout
Component Side Shown

NOTES:

ELECTRICAL:

1. VOLTAGE RATING : 125 VAC RMS MIN.
2. CURRENT RATING : 1.5 AMP MIN.
3. CONTACT RESISTANCE : 30 MILLIOHMS MAX.
4. INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 1000 VDC.
5. DIELECTRIC STRENGTH : 1000 VAC RMS 60Hz, 1MIN.

MECHANICAL:

1. HOUSING MATERIAL : GLASS FILLED POLYESTER UL94V-0.
2. CONTACT MATERIAL : PHOSPHOR BRONZE t=0.35mm.
3. PLATING : 1: CONTACT AREA- SELECTIVE GOLD PLATING.
2: TAILS- PLATED TIN OVER NICKEL
4. OPERATING LIFE : 750 CYCLES MIN.
5. PCB RETENTION PRE-SOLDER : 1 LB MIN.
6. PCB RETENTION POST-SOLDER: 10 LBS MIN.

ENVIRONMENTAL:

1. STORAGE : -40°C TO +85°C.
 2. OPERATION: 0°C TO 70°C.
 3. WAVE SOLDERING TEMPERATURE: 230°C TO 250°C 3-5SECOND
- MATES WITH MODULAR PLUG CONFORMING TO
FCC PART 68, SUBPART F.
CUL FILE NO. E163191

△ PART NUMBER: E574X-0000XX-L

2- 2 CONTACTS
4- 4 CONTACTS

1- 3u" 2- 6u" 3-15u"
4-30u" 5-50u" 6-Pd20u"
3-W/ PANNEL STOP
4-W/O PANNEL STOP

DETAILED LISTS	↔ MM (INCH)		DFTO SAMMY	DATE 04/22/05	FULL RISE ELECTRONIC CO., LTD		
	TOLERANCES EXCEPT AS NOTED		CHKD JAKE	DATE 2005/4/22			
			MFO	DATE	TITLE SIDE ENTRY PCB JACK 4P		
			APPV LUSHENG	DATE 2005/4/22			
			MATERIAL :		DRAWING NO. GE573008-LC /PART NO. SEE NOTE		
			QT'Y :				
	ANGLES ±		FINISH :		DO NOT SCALE DRAWING		
			SCALE :				
	THIRD ANGLE PROJECTION						