

NC7ST00

TinyLogic™ HST 2-Input NAND Gate

General Description

The NC7ST00 is a single 2-Input high performance CMOS NAND Gate, with TTL-compatible inputs. Advanced Silicon Gate CMOS fabrication assures high speed and low power circuit operation. ESD protection diodes inherently guard both inputs and output with respect to the V_{CC} and GND rails. High gain circuitry offers high noise immunity and reduced sensitivity to input edge rate. The TTL-compatible inputs facilitate TTL to NMOS/CMOS interfacing. Device performance is similar to MM74HCT but with 1/2 the output current drive of HC/HCT.

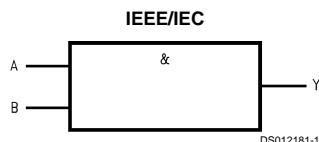
Features

- Space saving SOT23 or SC70 5-lead surface mount package
- High Speed; $T_{PD} < 7$ ns typ, $V_{CC} = 5$ V, $C_L = 15$ pF
- Low Quiescent Power; $I_{CC} < 1$ μ A typ, $V_{CC} = 5.5$ V
- Balanced Output Drive; 2 mA IOL, -2 mA IOH
- TTL-compatible inputs

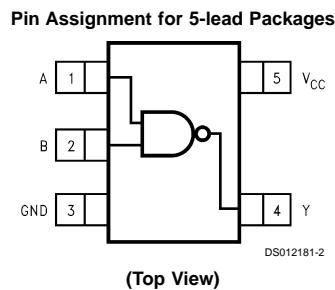
Ordering Code:

Product Code	Package	Package Drawing	Package Top Mark	Supplied As
NC7ST00M5	SOT23-5	MA05B	8S00	250 Units on Tape and Reel
NC7ST00M5X	SOT23-5	MA05B	8S00	3k Units on Tape and Reel
NC7ST00P5	SC70-5	MAA05A	T00	250 Units on Tape and Reel
NC7ST00P5X	SC70-5	MAA05A	T00	3k Units on Tape and Reel

Logic Symbol Logic Symbol



Connection Diagram



Pin Descriptions

Pin Names	Description
A, B	Inputs
Y	Output

Function Table

$$Y = \overline{AB}$$

Inputs		Output
A	B	Y
L	L	H
L	H	H
H	L	H
H	H	L

H = HIGH Logic Level

L = LOW Logic Level

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Absolute Maximum Ratings (Note 1)

Supply Voltage (V_{CC})	-0.5V to +7.0V	ESD Tolerance (Human Body Model)
DC Input Diode Current (I_{IK})		MIL-STD-883D Method
$V_{IN} < -0.5V$	-20 mA	3015.7
$V_{IN} \geq V_{CC} + 0.5V$	+20 mA	>2000V
DC Input Voltage V_{IN}	-0.5V to $V_{CC} + 0.5V$	DC Latchup Tolerance
DC Output Diode Current (I_{OK})		Source Current (JEDEC Method 17)
$V_{OUT} < -0.5V$	-20 mA	± 500 mA
$V_{OUT} > V_{CC} + 0.5V$	+20 mA	
Output Voltage (V_{OUT})	-0.5V to $V_{CC} + 0.5V$	
DC Output Source or Sink Current (I_{OUT})	± 12.5 mA	
DC V_{CC} or Ground Current per Supply Pin (I_{CC} or I_{GND})	± 25 mA	
Storage Temperature (T_{STG})	-65°C to +150°C	Operating Temperature (T_A)
Junction Temperature (T_J)	150°C	-40°C to +85°C
Lead Temp. (T_L); (Soldering, 10s)	260°C	Input Rise and Fall Time (t_r, t_f)
Power Dissipation (P_D) @ +85°C		$V_{CC} = 5.0V$
SOT23-5	200 mW	0–500 ns
SC70-5	150 mW	Thermal Resistance (θ_{JA})
		SOT23-5
		SC70-5

Recommended Operating Conditions

Supply Voltage	4.5V–5.5V
Input Voltage (V_{IN})	0.0V– V_{CC}
Output Voltage (V_{OUT})	0V– V_{CC}
Operating Temperature (T_A)	-40°C to +85°C
Input Rise and Fall Time (t_r, t_f)	
$V_{CC} = 5.0V$	0–500 ns
Thermal Resistance (θ_{JA})	
SOT23-5	300°C/W
SC70-5	425°C/W

Note 1: Absolute Maximum Ratings are those values beyond which damage to the device may occur. The databook specifications should be met, without exception, to ensure that the design is reliable over its power supply, temperature, and output/input loading variables. Fairchild does not recommend operation of circuits outside the databook specifications.

DC Electrical Characteristics

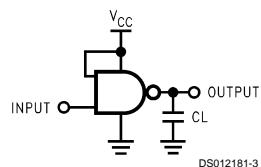
Symbol	Parameter	V_{CC} (V)	NC7ST00			Units	Conditions		
			TA = +25°C						
			Min	Typ	Max				
V_{IH}	High Level Input Voltage	4.5–5.5	2.0		2.0	V			
V_{IL}	Low Level Input Voltage	4.5–5.5			0.8	V			
V_{OH}	High Level Output Voltage	4.5 4.5	4.4 4.18	4.5 4.35	4.4 4.13	V V	$I_{OH} = -20$ μ A $I_{OH} = -2$ mA $V_{IN} = V_{IL}$		
V_{OL}	Low Level Output Voltage	4.5 4.5		0 0.10	0.1 0.26	0.1 0.33	V V	$I_{OL} = 20$ μ A $I_{OL} = 2$ mA $V_{IN} = V_{IH}$	
I_{IN}	Input Leakage Current	5.5			± 0.1	± 1.0	μ A		
I_{CC}	Quiescent Supply Current	5.5			1.0	10.0	μ A		
I_{CCT}	I_{CC} per Input	5.5			2.0	2.9	mA		
							One input $V_{IN} = 0.5V$ or 2.4V, other input V_{CC} or GND		

AC Electrical Characteristics

Symbol	Parameter	V _{CC} (V)	NC7ST00			NC7ST00		Units	Conditions	Fig. No.			
			T _A = +25°C			T _A = -40°C to +85°C							
			Min	Typ	Max	Min	Max						
t _{PLH} , t _{PHL}	Propagation Delay	5.0		3.4	12			ns	C _L = 15 pF	Figure 1, Figure 3			
				6.3	17								
		4.5		6.0	16		20		C _L = 50 pF				
				11.5	27		31						
		5.5		4.1	14		18						
				11.2	26		30						
t _{TLH} , t _{THL}	Output Transition Time	5.0	4	10				ns	C _L = 15 pF	Figure 1, Figure 3			
		4.5	11	25		31		ns	C _L = 50 pF				
		5.5	10	21		26							
C _{IN} (Note 2)	Input Capacitance (Note 2)	Open	2	10				pF					
C _{PD}	Power Dissipation Capacitance	5.0	6					pF	(Note 3)	Figure 2			

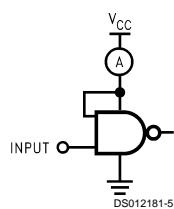
Note 2: Parameter guaranteed by design. Not tested.

Note 3: C_{PD} is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current. Current consumption (ICCD) at no output loading and operating at 50% duty cycle. (See Figure 2). CPD is related to ICCD dynamic operating current by the expression: ICCD = (C_{PD})(V_{CC})(f_{IN}) + (IC_{static}).



C_L includes load and stray capacitance
Input PRR = 1.0 MHz; t_w = 500 ns

FIGURE 1. AC Test Circuit



Input = AC Waveform; PRR = variable; Duty Cycle = 50%

FIGURE 2. ICCD Test Circuit

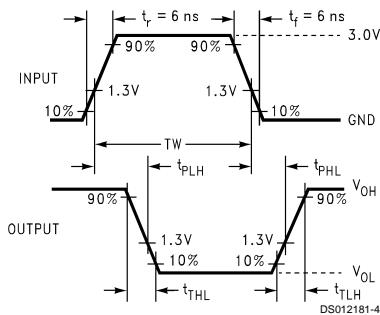
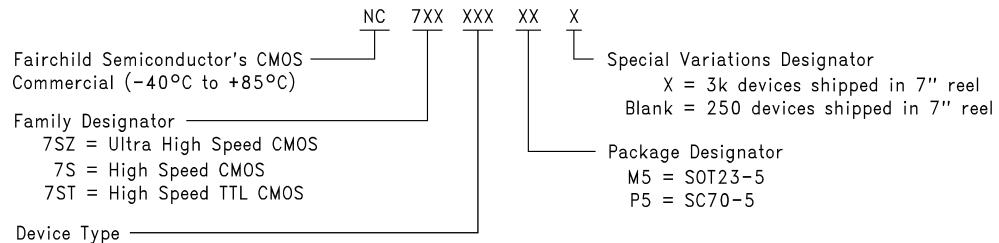


FIGURE 3. AC Waveforms

Ordering Information

The device number is used to form part of a simplified purchasing code where the package type and temperature range are defined as follows:



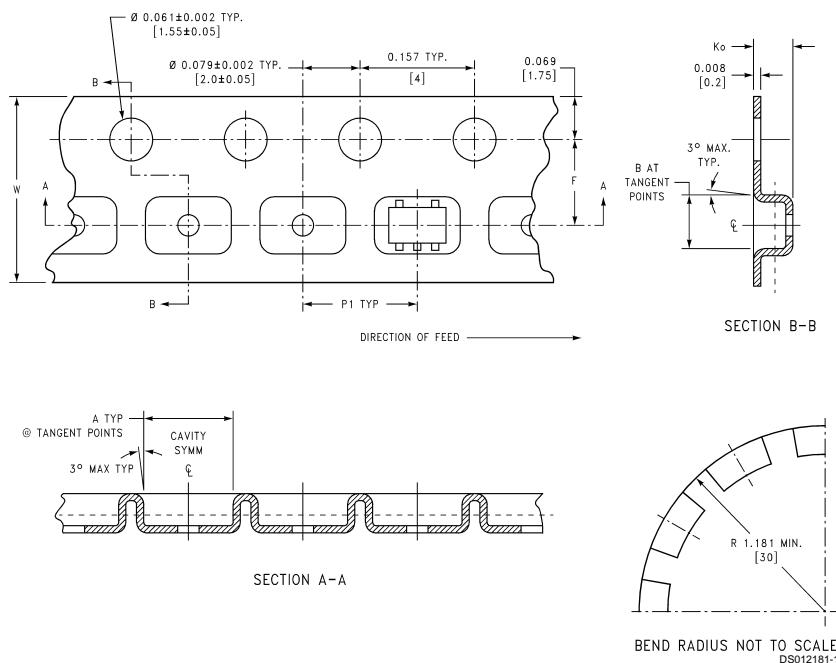
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Tape and Reel Specification

TAPE FORMAT

Package Designator	Tape Section	Number Cavities	Cavity Status	Cover Tape Status
M5, P5	Leader (Start End)	125 (typ)	Empty	Sealed
	Carrier	250	Filled	Sealed
	Trailer (Hub End)	75 (typ)	Empty	Sealed
M5X, P5X	Leader (Start End)	125 (typ)	Empty	Sealed
	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (typ)	Empty	Sealed

TAPE DIMENSIONS inches (millimeters)

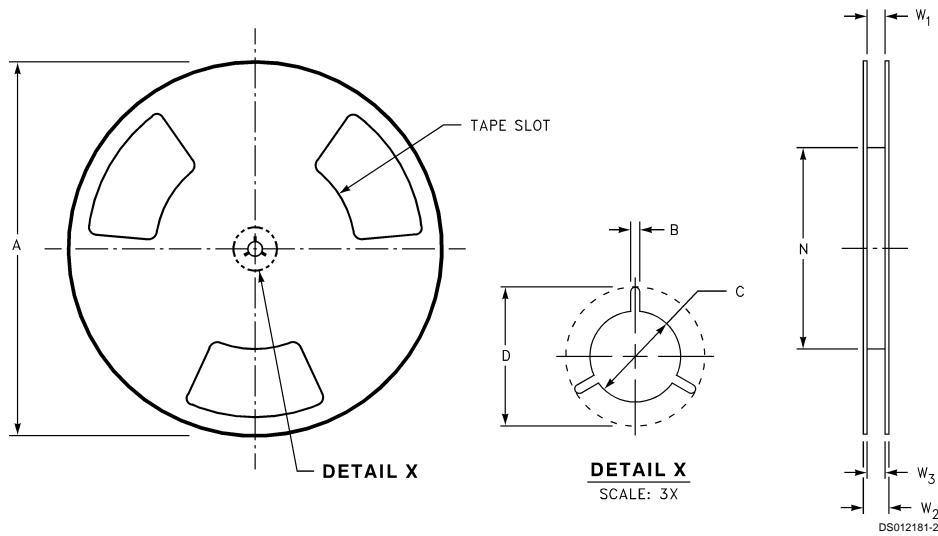


Pkg	Tape Size	DIM A	DIM B	DIM F	DIM K _o	DIM P1	DIM W
SC70-5	8 mm	0.093 (2.35)	0.096 (2.45)	0.138 ± 0.004 (3.5 ± 0.10)	0.053 ± 0.004 (1.35 ± 0.10)	0.157 (4)	0.315 ± 0.004 (8 ± 0.1)
SOT23-5	8 mm	0.130 (3.3)	0.130 (3.3)	0.138 ± 0.002 (3.5 ± 0.05)	0.055 ± 0.004 (1.4 ± 0.11)	0.157 (4)	0.315 ± 0.012 (8 ± 0.3)

Tape and Reel Specification

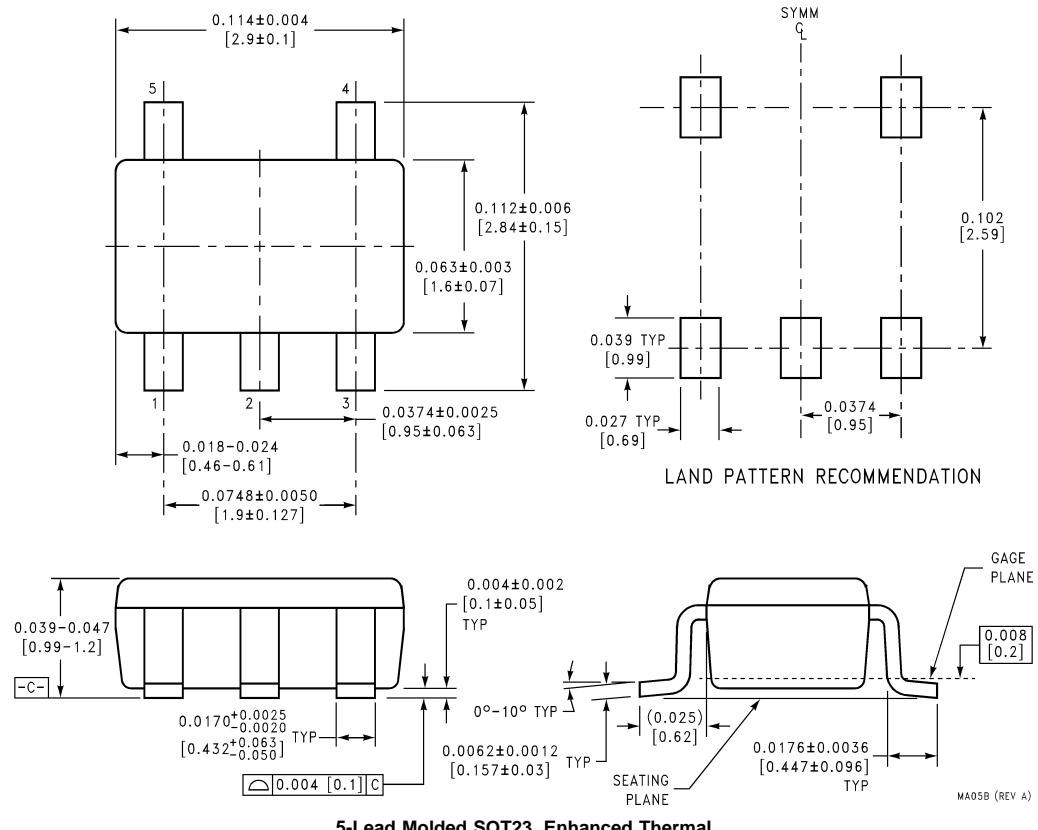
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REEL DIMENSIONS inches (millimeters)



Tape Size	A	B	C	D	N	W1	W2	W3
8 mm	7.0 (177.8)	0.059 (1.50)	0.512 (13.00)	0.795 (20.20)	2.165 (55.00)	0.331 +0.059/-0.000 (8.40 +1.50/-0.00)	0.567 (14.40)	W1 +0.078/-0.039 (W1 +2.00/-1.00)

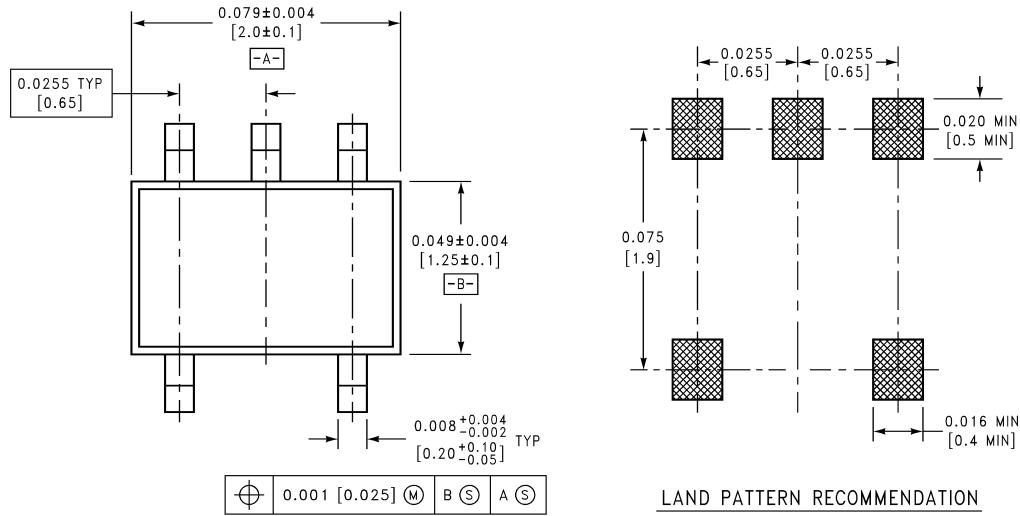
Physical Dimensions inches (millimeters) unless otherwise noted



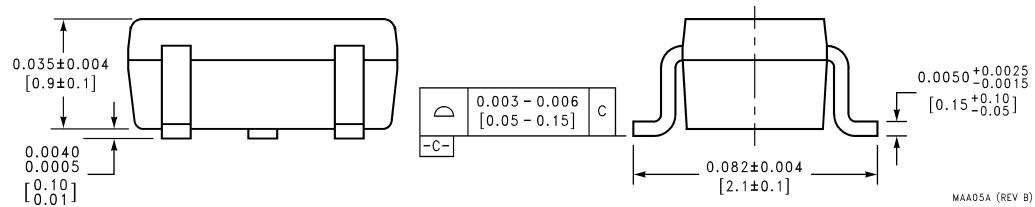
5-Lead Molded SOT23, Enhanced Thermal
Package Number MA05B

NC7ST00 TinyLogic HST 2-Input NAND Gate

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



LAND PATTERN RECOMMENDATION



5-Lead Molded SC70, Enhanced Thermal
Package Number MAA05A

MAA05A (REV B)

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